R18

[5+5]

Code No: 156CW

7.a

b)

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech III Year II Semester Examinations, February - 2023 SOFTWARE TESTING METHODOLOGIES

	(Common to CSE, 11)				
Time	3 Hours M	ax. Marks: 75			
NT.4	i) O di				
Note:	i) Question paper consists of Part A, Part B.	.•			
ii) Part A is compulsory, which carries 25 marks. In Part A, Answer all questions.					
iii) In Part B, Answer any one question from each unit. Each question carries 10 marks					
and may have a, b as sub questions.					
D. D. D. C.					
	PART – A	(25.15.1.)			
		(25 Marks)			
4 \		503			
1.a)	Define path testing.	[2]			
b)	What is the difference between an error and a bug?	[3]			
c)	Write a short note on random testing.	[2]			
d)	What is the significance of data flow testing?	[3]			
e)	Write a short note on path expressions.	[2]			
f)	List out the different operators that are used to solve any boolean algebra	. [3]			
g)	Define a transition bug.	[2]			
h)	What is good state graph?	[3]			
i)	Define a connection matrix.	[2]			
j)	List the applications of graph matrices.	[3]			
	PART – B				
	PART – B Differentiate between testing and debugging.	(50 Marks)			
2 0)	Differentiate between testing and debugging				
2.a)	8 4 4 4 4 6 8	[5 5]			
b)	Describe the model for testing. OR	[5+5]			
3.		[10]			
3.	Classify the different types of bugs and explain.	[10]			
4.a)	State and explain the transaction flow testing techniques.				
+.a) b)	Compare static slicing with dynamic slicing.	[6+4]			
U)	OR	[0+4]			
5.	How developers and testers treat nice and ugly domains? Illustrate w	ith the help of			
5.	examples.	[10]			
	examples.	[10]			
6.a)	Illustrate maximum path count arithmetic with an example.				
b)	Describe the usage of regular expression in flow anomaly detection.	[6+4]			
0)	Describe the usuge of regular expression in now unontary detection.	[[]			

OR

Justify the use of decision table implementation for designing test cases. Explain the procedure for specification validation using KV charts.

8.	Explain the following terms:		
	a) Design guideline for building finitb) Inessential finite state behavior.	e state machine OR	[5+5]
9.a) b)	Write short notes on testability tips. Summarize the concept transition test	ting.	[5+5]
10.a) b)	Describe node-term reduction optimic Give a brief summary on relations.	zation. OR	[5+5]
11.	Write an algorithm for node reduction	n using matrix operations and explain.	[10]
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