IDP - EE3025

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Project Idea

Sobel Filter For Edge Detection on Images

Implementation Plan

- Load image into FPGA
  - Convert image to hex file using *ffmpeg*
  - Stream these pixel values as bytes from Raspberry Pi to FPGA RAM using *ffmpeg*
  - Use coordinate decoder module on sequence of pixels (bytes). For this we need to specify the image size. Later we plan to extend to use pilot bytes to detect row endings.
- To Implement an efficient Sobel Filter on FPGA
  - (write about sobel filter implementation in verilog)

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Loading Image into FPGA using Raspberry Pi

write about SPI interface
maybe about expected progress by next presentation