IDP - EE3025

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Sobel Filter For Edge Detection on Images

- Load image into FPGA
  - (write about spi interface and ffmpeg)
- To Implement an efficient Sobel Filter on FPGA
  - (write about sobel filter implementation in verilog)

Implementation Plan

- Load image into FPGA
    - (write about spi interface and ffmpeg)
- To Implement an efficient Sobel Filter on FPGA
    - (write about sobel filter implementation in verilog)

write about SPI interface
maybe about expected progress by next presentation