U.S.N.					

BMS College of Engineering, Bengaluru-560019

(Autonomous Institute, Affiliated to VTU, Belgaum)

December 2015 Semester End Main Examinations

Duration: 3 Hours Course: Computer Organization And Embedded Systems Course Code: 15IS3DCCOE Max Marks: 100 Date: 10.12.2015 Instruction: Answer any five full questions choosing one from each unit. **UNIT-I** With the help of neat diagram, list and explain the steps needed to execute the 1. 10 Machine instructions LOAD R4, LOCA ADD R4, R2,R3 Explain subroutine nesting through parameter passing with example. 10 **UNIT-II** 2. Describe the design of a hardwired control unit. 10 a) Illustrate the sequence of actions needed to fetch and execute the instruction 10 b) given below using data path diagram. Load R5,X(R7)**UNIT-III** 3. With an neat diagram describe an serial interface circuit. 10 Illustrate how the keyboard and display devices are connected to the 10 processor from the software point of view and explain the registers in the Keyboard and display interfaces. OR 4. Explain the basic structure of memory hierarchy and define the different types of 10 locality. Explain the concept of page fault in virtual memory with an neat diagram. 10 **UNIT-IV** Multiply the following pairs of signed 2's complement numbers using 5. 12 (I) Booth algorithm (II) Bit-pair recoding of multipliers. Assume A is the multiplicand and B is the multiplier. A=010111, B=110110 (i)

(ii)

A=110011, B=101100

	b)	Explain Carry-Lookahead adder. Design 16-bit carry-look ahead adder from	08
		4-bit adders.	
		UNIT-V	
6.	a)	Explain with an block diagram the microcontroller chip for embedded applications.	10
	b)	Illustrate the processing and control capability of an digital camera.	10
		OR	
7.	a)	Illustrate how sensors and actuators are used in the following applications: i) Home heating control	10
		ii) Cruise control	
	b)	Explain Serial Interface Registers of an simple microcontroller.	10
