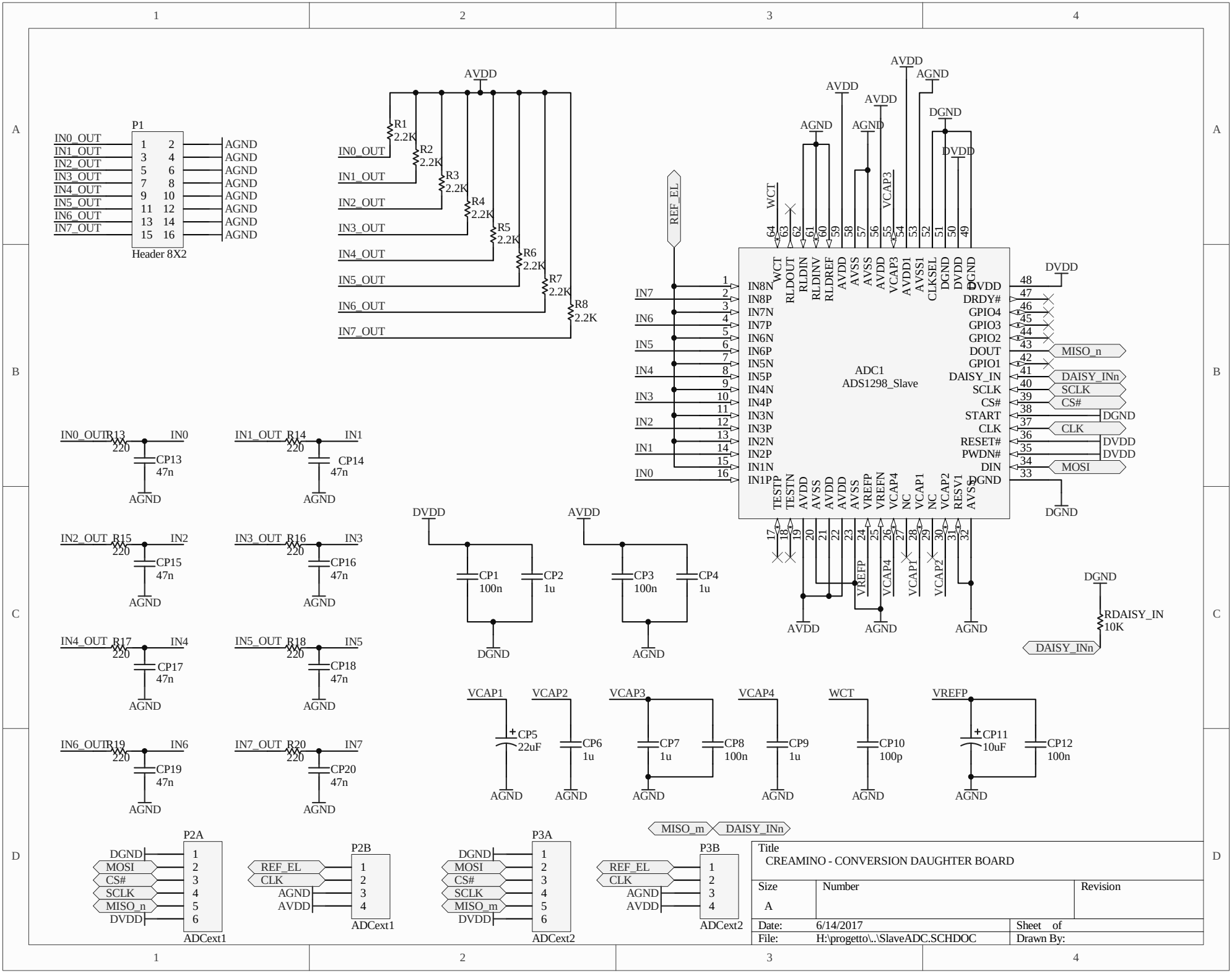
P1
Header 8X2
IN0_OUT
IN1_OUT
IN2_OUT
IN3_OUT
IN4_OUT
IN5_OUT
IN6_OUT
IN7_OUT
AGND
AVDD
DVDD
DGND
REF_EL
WCT
VCAP3
ADC1
ADS1298_Slave
IN8N
IN8P
IN7N
IN7P
IN6N
IN6P
IN5N
IN5P
IN4N
IN4P
IN3N
IN3P
IN2N
IN2P
IN1N
IN1P
WCT
RLDOUT
RLDIN
RLDINV
RLDREF
AVDD
AVSS
AVSS
AVDD
VCAP3
AVDD1
AVSS1
CLKSEL
DGND
DVDD
DGND
DVDD
DRDY#
GPIO4
GPIO3
GPIO2
DOUT
GPIO1
DAISY_IN
SCLK
CS#
START
CLK
RESET#
PWDN#
DIN
DGND
TESTP
TESTN
AVDD
AVSS
AVDD
AVDD
AVSS
VREFP
VREFN
VCAP4
NC
VCAP1
NC
VCAP2
RESV1
AVSS2
MISO_n
DAISY_INn
SCLK
CS#
CLK
MOSI
R1 2.2K
R2 2.2K
R3 2.2K
R4 2.2K
R5 2.2K
R6 2.2K
R7 2.2K
R8 2.2K
IN0_OUT R13 220 IN0
CP13 47n
IN1_OUT R14 220 IN1
CP14 47n
IN2_OUT R15 220 IN2
CP15 47n
IN3_OUT R16 220 IN3
CP16 47n
IN4_OUT R17 220 IN4
CP17 47n
IN5_OUT R18 220 IN5
CP18 47n
IN6_OUT R19 220 IN6
CP19 47n
IN7_OUT R20 220 IN7
CP20 47n
CP1 100n
CP2 1u
CP3 100n
CP4 1u
RDAISY_IN 10K
DAISY_INn
VCAP1
CP5 22uF
VCAP2
CP6 1u
VCAP3
CP7 1u
CP8 100n
VCAP4
CP9 1u
WCT
CP10 100p
VREFP
CP11 10uF
CP12 100n
MISO_m
DAISY_INn
P2A
DGND
MOSI
CS#
SCLK
MISO_n
DVDD
ADCext1
P2B
REF_EL
CLK
AGND
AVDD
ADCext1
P3A
DGND
MOSI
CS#
SCLK
MISO_m
DVDD
ADCext2
P3B
REF_EL
CLK
AGND
AVDD
ADCext2
Title
CREAMINO - CONVERSION DAUGHTER BOARD
Size
A
Number
Revision
Date: 6/14/2017
Sheet of
File: H:\progetto\..\SlaveADC.SCHDOC
Drawn By: