FLASHY FASHION DEVICE: System Test Plan

Version 1.0

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Version History

Version #	Revision Date	Responsible Party	Reason
1.0	22 November, 2016	Jessica Blasch	Initial compilation of source document

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1.0 INTRODUCTION

1.1 This Document

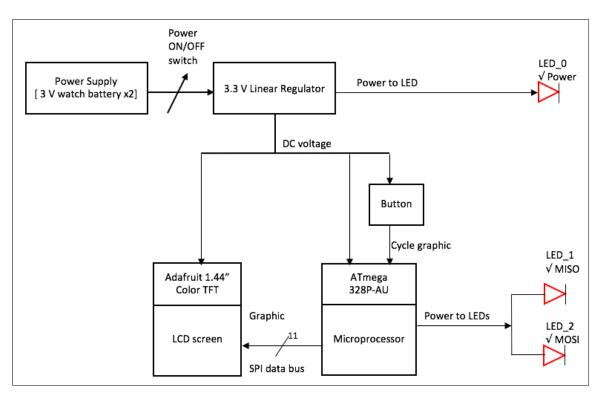
This document serves as reference guide for the Flashy Fashion device. Its contents cover pertinent information related to design, testing, and debugging necessary to ensure successful operation of the device.

2.0 REFERENCE DOCUMENTS

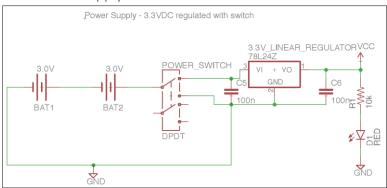
2.1 Design Documentation

2.1.1 Flashy Fashion Block Diagram

<u>Important</u>: The button and its connections are missing on the board schematics following; in particular the <u>microprocessor to LCD schematic</u>. This component will need to be added and soldered by hand to create this piece of functionality.

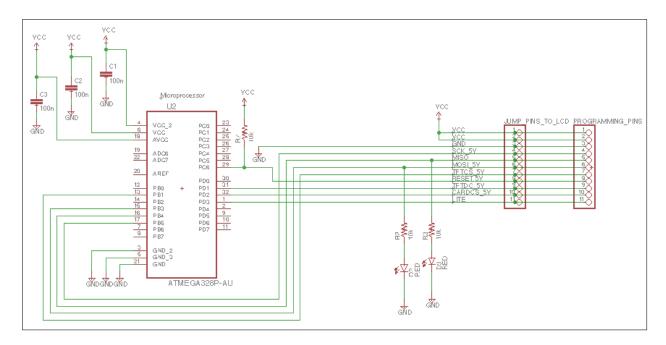


2.2.2 Power Supply and Control Switch Schematic

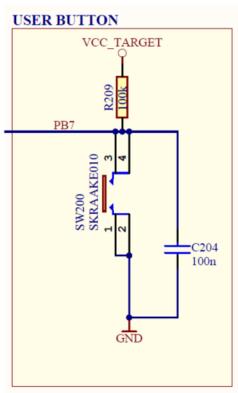


2.2.3 ATmega328P-AU Microprocessor to Breakout Board for TFT LCD Schematic

<u>Important</u>: The button and its connections, in the <u>block diagram</u>, are missing on the board schematic following. This component which is used to cycle through multiple images on the TFT LCD will need to be added and soldered by hand to create this piece of functionality. See next diagram for button schematic.



2.2.4 User Button Schematic



ST7735 | ST7

2.2.5 Adafruit 1.44" Color TFT with Micro SD Socket Schematic

3.0 FLASHY FASHION OVERVIEW

3.1 Operational Description

To use the Flashy Fashion device, it must have 2 3 V watch batteries installed and be powered on. At least one image uploaded to the microcontroller per user manual instructions. When the button to the microcontroller is pushed, the image should cycle to allow the user to select a different display graphic.

3.2 Definition of Terminology

- PCB Printed Circuit Board
- SPI –Serial Peripheral Interface Bus (ATmega Microcontroller)
- MOSI Pin on TFT LCD connected to pin 16 on ATmega microcontroller
- MISO Pin on TFT LCD connected to pin 15 on ATmega microcontroller
- TFT CS Pin on TFT LDC connected to pin 13 on ATmega microcontroller

4.0 PRETEST PREPERATION

4.1 Test Equipment

- Multimeter
- Oscilloscope
- Video Camera with time-lapse and app. (e.g. Nest Cam indoor security camera)

4.2 Test Setup and Calibration

Tests which fall under "Functional Checks" are designed to test individual components of the system and require a multimeter, oscilloscope, and time-lapse video cam. Tests which fall under "System Tests" should be completed after all components are assembled on the pcb.

4.3 Supplemental Documentation

4.3.1 ATmega328P Microcontroller

- Datasheet Summary
- <u>Data Sheet Complete</u>
- Atmel ATMEGA 8-bit instruction set
- Atmel studio

4.3.2 Adafruit 1.4" TFT LCD

- ST7735R Data Sheet
- PDF Overview

4.3.3 Coding Reference

- Atmel SPI API
- Atmel's Toolchain
- Atmel Software Framework

5.0 SYSTEM TESTS

5.1 Functional Checks

5.1.1 Power Switch

Test	Test Writer: Jessica Blasch								
Test	Case Name:	Power Switch				Test ID#:	PWR-SW-01		
Des	cription:	Test power switch functionality			Туре:	white box ⊠black box			
Test	er Information								
Nan	ne of Tester:					Date:			
Har	dware Ver:	Flashy Fashion 1.0				Time:			
Setu	ip:	Install DPDT power switch on PCB. Place switch in OFF position, and locate lower two pads on switch that face the batteries on the PCB. Use multimeter to take measurements as described.							
Step	Action	Expected Result	Pass	Fail	N/A	Comments			
1	Measure resistance across pads with switch in OFF position	Infinite resistance measured							
2	Turn switch to ON position	Near or zero resistance measured							
Ove	rall test result:								

5.1.2 Voltages: Supply and Linear Regulator

Test	Test Writer: Tyson Gieszler								
Test	Case Name:	Voltage Levels				Test ID#:	Voltage-LVL-01		
Des	cription:	Test the amount of voltage received from the battery			ery	Type:	white box		
		unit before and after the switch and regu	unit before and after the switch and regulator				black box		
Test	er Information								
Nan	ne of Tester:					Date:			
Hard	lware Ver:	Flashy Fashion 1.0				Time:			
Setu	p:	Install battery mounts, 3.3 V regulator, a	nd DF	DT p	owe	r switch on PCB. Hav	e fresh batteries		
·		on hand and ensure power switch is OFF	befor	re be	ginn	ing. Use a multimete	r to measure		
		voltages as described below.							
St	Action	Expected Result	Ģ	Fail	N/A	Comments			
Step			Pass	≝	/A				
1	Insert one battery cell. Make	Expected 3V +/- 10% before the Switch							
	sure switch is in the OFF	Expected OV after the Switch							
	position. Test voltage output								
	before and after switch.								
2	Insert second battery. Make	Expected 6V +/- 10% before the Switch							
	sure Switch is in the OFF	Expected 0V after the Switch							
	position. Test voltage before								
	and after the switch.								
3	With both batteries in, slide	Expected 6V +/- 10% before the Switch							
	the switch to the ON position.	Expected 6V +/- 10% after the Switch							
	Test the voltage before and	Expected 3.3V +/- 5% after the							
	after the Switch. Test the	regulator.							
	voltage after the regulator.								
Ove	rall test result:								

5.1.3 Microprocessor SPI Driver Output

	<u>'</u>	<u> </u>					
Test	Writer: Charley Hill						
Test	Case Name:	ATmega SPI output test				Test ID#:	SPI-01
Des	cription:	Tests ATMEGA SPI driver to ensure correct com	muni	catio	n	Type:	⊠white box
		from the microprocessor.					black box
Test	er Information						
Nan	ne of Tester:					Date:	
Hard	dware Ver:	Flashy Fashion 1.0				Time:	
Setu	p:	Connect oscilloscope probes to MOSI, SPI clock, monitor when bytes are transmitted.	and 1	FT C	S. Se	t up debugger to ha	ve control and
Step	Action	Expected Result	Pass	Fail	N/A	Comments	
1	Write program to drive MOSI output on SPI interface	Bits fluctuate on an output line in accordance with manually specified bytes.					
2	Connect MOSI CS and SPI clock to oscilloscope. Repeat for all six pins.	SPI clk fluctuating in time in accordance with MOSI output and TFT CS matching designated value.					
3	Time & monitor to see if bits are coming out in correct order	SPI clock, MOSI, and TFT CS match desired output levels and timing constraints.					
Ove	rall test result:						

5.1.4 LCD Display Image

Test	Writer: Tyson Gieszler						
Test	Case Name:	Display Image				Test ID#:	Image-DSP-01
Desc	cription:	Test to see if communication is working properl LCD screen	y with	the		Туре:	white box black box
Test	Tester Information						
Nam	ne of Tester:					Date:	
Hard	dware Ver:	Flashy Fashion 1.0				Time:	
Setu	ip:	Ensure connections between microcontroller and TFT LCD as per schemate and power switch is OFF before beginning.				er schematics, hook	ups to power on,
Step	Action	Expected Result	Pass	Fail	N/A	Comments	
1	Write code to bring TFT LCD out of sleep mode	N/A					
2	Insert both battery cells and switch to ON. Upload image following user's manual.	Display should light up and show image					
Ove	rall test result:						·

5.2 Assembled Device Checks

5.2.1 Microcontroller Button

Test	Writer: Charley Willow	Hill					
	: Case Name:	Button function test				Test ID#:	Button-01
Description: This test will ensure the consistent function of sonly button. It will ensure that there is an image every press after the initial startup.						Type:	⊠white box □black box
Test	er Information						
Nan	ne of Tester:					Date:	
Har	dware Ver:	Flashy Fashion 1.0				Time:	
Setu	ıp:	Ensure complete assembly of device with curren	nt sof	twar	e inst	talled and running.	
Step	Action	Expected Result	Pass	Fail	N/A	Comments	
1	After complete boot process, press button and observe results.	Image changes to next image in preprogrammed sequence.					
2	Rapidly press button and observe results.	Device will jump to image further in sequence relative to the number of times the button was pushed.					
3	Hold button for extended period and observe results.	Image will continue to change until button is released.					
Ove	rall test result:						

5.2.2 Backlight

Test Writer: Charley Willow Hill								
Test Case Name:	Backlight test.				Test ID#:	Backlight-01		
Description:	This test will ensure the proper functioning of the backlight				Туре:	⊠white box □black box		
Tester Information	Tester Information							
Name of Tester:					Date:			
Hardware Ver:	Flashy Fashion 1.0				Time:			
Setup:	Ensure complete assembly of device with curren	nt soft	war	e inst	talled and running.			
Action	Expected Result	Pass	Fail	N/A	Comments			
1 After complete boot process, press button and observe results.	Backlight will turn on.							
2 Rapidly press button and observe results.	No change.							
3 Hold button for extended period and observe results.	No change.							
Overall test result:								

5.2.3 Image Cycle

Test	Test Writer: Charley Willow Hill								
Test	Case Name:	Image Cycle Test				Test ID#:	IMG-Cycle-01		
Des	cription:	This test will ensure the consistent and appropr	iate ir	nage	e is	Type:	white box		
		displayed on device as it is intended by the user					black box		
Test	er Information								
Nam	ne of Tester:					Date:			
Hard	dware Ver:	Flashy Fashion 1.0				Time:			
Setu	ıp:	Ensure complete assembly of device with curre	nt sof	twar	e ins	talled and running.			
Step	Action	Expected Result	Pass	Fail	N/A	Comments			
1	After complete boot process, press button and observe results.	Image changes to next image in preprogrammed sequence.							
2	Rapidly press button and observe results.	Device will jump to image further in sequence relative to the number of times the button was pushed.							
3	Hold button for extended period and observe results.	Image will continue to change until button is released.							
Ove	rall test result:								

5.2.4 LEDs: Power, MISO, and MOSI

Test Writer: Tyson Gieszler							
Test	Case Name:	LED Levels			Test ID#:	LED-Levels-01	
Desc	cription:	Test to determine if the LEDs are lighting up and	not o	causi	ng	Type:	white box
		"brown-out" or circuit					black box
Test	er Information						
Nam	ne of Tester:					Date:	
Hard	dware Ver:	Flashy Fashion 1.0				Time:	
Setu	ıp:	Propagate all components on the PCB and test f	or fur	nctio	nality	prior to this test. Er	nsure fresh
		batteries are on hand and that the power switch	n is Ol	FF be	fore	beginning.	
Step	Action	Expected Result	Pass	Fail	N/A	Comments	
1	Insert both battery cells. Slide Switch to ON position. Observe LED.	Power LED should be visible in normal indoor lighting. Level should be constant and not flickering.					
2	Upload and display image to the LCD screen. Observe LEDs during process.	LEDs on MISO and MOSI lines should light up and be visible in normal indoor lighting. Screen should display image completely and not become dim or shut off.					
Ove	rall test result:						

5.2.5 Battery Lifetime Minimum

	<u> </u>						
Test	Writer: Tyson Gieszler						
Test	Case Name:	Battery Lifetime				Test ID#:	Battery-Life-01
Des	cription:	Test to determine if the battery lifetime is longe	r tha	n 1		Type:	white box
		hour.					⊠ black box
Test	er Information						
Nan	ne of Tester:					Date:	
Hard	dware Ver:	Flashy Fashion 1.0				Time:	
Setu	ıp:	Propagate PCB board with all components and r	un te	sts o	n ind	ividual blocks before	this test. Ensure
		fresh batteries are available before beginning.					
Step	Action	Expected Result	Pass	Fail	N/A	Comments	
1	Insert both battery	The device should display an image on the TFT LCD screen					
	cells. Upload and	LCD screen					
	display image that						
_	uses whole screen.	For extending The content of a did will be		_	_		
2	Check after 1 hour	Expectation: The system should still be					
		displaying the image and should not have					
		powered down at this point.	_				
Ove	rall test result:						

5.2.6 Battery Lifetime Extended

Test	Test Writer: Tyson Gieszler								
Test	Case Name:	Battery Lifetime Extended				Test ID#:	Battery-Life-02		
Desc	ription:	This test is to see how long the system will remain powered				Type:	white box		
		on if run on two 3 V batteries.					⊠ black box		
Test	er Information								
Nam	e of Tester:					Date:			
Hard	lware Ver:	Flashy Fashion 1.0				Time:			
Setu	p:	Propagate PCB with all components and run tes	ts on	indiv	idual	blocks before this to	est. Ensure fresh		
		are on hand and the device should be powered	OFF.						
Step	Action	Expected Result	Pass	Fail	N/N	Comments			
1	Insert new battery cells.	No change							
2	Upload and display image that uses whole screen.	Image should fill the entirety of the LCD screen							
3	Note initial start time and monitor with time-lapse video, until drained, and review for time power drained.	LCD screen will have turned off from drained batteries.							
Ove	rall test result:		1						