

R N S Institute of Technology

Department of Electronics and Communication Engineering

Time Table- Year 2017 (EVEN SEMESTER)

Semester: VI Section: A		W.E.F.13-02-2017					ROOM:301			
	Class Teacher: Chethana J				Class Counsellor: Ibrar Jahan M					
DAYS	TIME									
	8.40-9.40	9.40-10.40	10.40-11.00	11.00-12.00	12.00 -1.00	1.00 - 1.40	1.40-2.40	2.40-3.40	3.40-4.40	
MONDAY	µp (IJM)	MEC (PG)		AWP (Dr. VHR)	SATCOM/C++ (SMN,MAS/GS)		DCOM LAB (SM+VKS) A2 µp LAB (KM+CJ)			
TUESDAY	µp (IJM)	AWP (Dr. VHR)		DC (Dr. VPS)	SATCOM/C++ (SMN,MAS/GS)		µp (IJM)	MEC (PG)	OS (CJ)	
WEDNESDAY	ADCOM LAB (SM+MNV) A1 µp LAB (IJM+GG)				SATCOM/C++ (SMN,MAS/GS)		PLACEMENT			
THURSDAY	OS (CJ)	DC (Dr. VPS)		MEC (PG)	SATCOM/C++ (SMN,MAS/GS)		PLACEMENT			
FRIDAY	DCOM LAB (SM+Dr.VHR) A3 µp LAB (IJM+GG)				OS (CJ)		AWP (Dr. VHR)	DC (Dr. VPS)	µp (IJM)	
SATURDAY	MEC (PG)	OS (CJ)		DC (Dr. VPS)	AWP (Dr.VHR)					

Subject	Code	Faculty	Initial
Digital Communication	10EC61	Dr. VIPULA SINGH	Dr. VPS
Microprocessors	10EC62	IBRAR JAHAN M A	IJM
Microelectronics	10EC63	PRAVEEN G	PG
Antenna & Propagation	10EC64	Dr. RAJINI V HONNUNGAR	Dr.VHR
Operating Systems	10EC65	CHETHANA J	CJ
Satcom/C++	10EC662/665	SMITHA N, MAMATHA A S/GHOUSIA S	SMN,MAS/GS
Advanced Com Lab	10ECL67	SM, Dr,VHR, VKS, MNV	
Microprocessor Lab	10ECL68	IJM, KM,GG,CJ	

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CTTO	Signature of HOD (ECE)				Principal				Director							
R N S Institute of Technology																
Department of Electronics and Communication Engineering																
Time Table- Year 2017 (EVEN SEMESTER)																
Semester: VIII		Section: A		W.E.F.13-02-2017				ROOM:201								
	Class Teacher: Chaithra T S															
DAYS	TIME															
	8.40-9.40	9.40-10.40	10.40-11.00	11.00-12.00	12.00 -1.00	1.00 - 1.40	1.40-2.40	2.40-3.40	3.40-4.40							
MONDAY	PROJECT WORK					L U N C H B R E A K	PROJECT WORK									
TUESDAY	PROJECT WORK						PROJECT WORK									
WEDNESDAY	PROJECT WORK						PROJECT WORK									
THURSDAY	GSM (CTS)	WC (IJM)	BREAK	NS (VK)	DSS (MNV)		GSM (CTS)	SS (MNV)								
FRIDAY	SS (MNV)	GSM (CTS)		GSM (CTS)	NS (VK)		WC (IJM)	NS (VK)								
SATURDAY	WC (IJM)	NS (VK)		WC (IJM)	DSS (MNV)											
Subject		Code		Faculty				Initial								
Wireless Communication		10EC81		IBRAR JAHAN M A				IJM								
Digital Switching Systems		10EC82		MANJUNATH NAIK V				MNV								
Network Security		10EC832		VINAYA KUSUMA				VK								
GSM		10EC843		CHAITHRA T S				CTS								
Project Work		10ECP85														
Seminar		10ECS86														
CTTO	Signature of HOD (ECE)				Principal				Director							

R N S Institute of Technology

Department of Electronics and Communication Engineering

Time Table- Year 2017 (EVEN SEMESTER)

Semester: VIII Section: B			W.E.F.13-02-2017				ROOM:202		
	Class Teacher: Vanitha K S								
DAYS	TIME								
	8.40-9.40	9.40-10.40	10.40-11.00	11.00-12.00	12.00 -1.00	1.00 - 1.40	1.40-2.40	2.40-3.40	3.40-4.40
MONDAY	PROJECT WORK					L U N C H B R E A K	PROJECT WORK		
TUESDAY	PROJECT WORK						PROJECT WORK		
WEDNESDAY	PROJECT WORK						PROJECT WORK		
THURSDAY	WC (NKS)	DSS (GG)	BREAK	DSS (GG)	WC (NKS)		NS (VKS)	GSM (SMB)	
FRIDAY	GSM (SMB)	NS (VKS)		NS (VKS)	DSS (GG)		WC (NKS)	DSS (GG)	
SATURDAY	NS (VKS)	GSM (SMB)		WC (NKS)	GSM (SMB)				

Subject	Code	Faculty	Initial
Wireless Communication	10EC81	NANDINI K S	NKS
Digital Switching Systems	10EC82	GEETHA G	GG
Network Security	10EC832	VANITHA K S	VKS
GSM	10EC843	SANJAY M B	SMB
Project Work	10ECP85		
Seminar	10ECS86		

CTTO	Signature of HOD (ECE)			Principal			Director										
R N S Institute of Technology																	
Department of Electronics and Communication Engineering																	
Time Table- Year 2017 (EVEN SEMESTER)																	
Semester: VIII Section: C				W.E.F.13-02-2017				ROOM:102									
	Class Teacher: Niya Jackson																
DAYS	TIME																
	8.40-9.40	9.40-10.40	10.40-11.00	11.00-12.00	12.00 -1.00	1.00 - 1.40	1.40-2.40	2.40-3.40	3.40-4.40								
MONDAY	PROJECT WORK					L U N C H B R E A K	PROJECT WORK										
TUESDAY	PROJECT WORK						PROJECT WORK										
WEDNESDAY	PROJECT WORK						PROJECT WORK										
THURSDAY	WC (SB)	DSS (MNV)	BREAK	GSM (SM)	WC (SB)		GSM (SM)	NS (NJ)									
FRIDAY	NS (NJ)	C (SB)		DSS (MNV)	NS (NJ)		DSS (MNV)	WC (SB)									
SATURDAY	GSM (SM)	DSS (MNV)		NS (NJ)	GSM (SM)												
Subject		Code		Faculty			Initial										
Wireless Communication		10EC81		SREENAVASA BABU M O			SB										
Digital Switching Systems		10EC82		MANJUNATH NAIK V			MNV										
Network Security		10EC832		NIYA JACKSON			NJ										
GSM		10EC843		SUCHANDANA MISHRA			SM										
Project Work		10ECP85															
Seminar		10ECS86															

CTTO	Signature of HOD (ECE)			Principal				Director					
R N S Institute of Technology													
Department of Electronics and Communication Engineering													
Time Table- Year 2017 (EVEN SEMESTER)													
Semester: IV Section A			W.E.F.13-02-2017					ROOM:101					
	Class Teacher: Leena C						Class Counsellor: Sankalp S B						
DAYS	TIME												
	8.40-9.40	9.40-10.40	10.40-11.00	11.00-12.00	12.00 -1.00	1.00 - 1.40	1.40-2.40	2.40-3.40	3.40-4.40				
MONDAY	MAT (SMS)	LIC (LC)	BREAK	PCS (Dr. SVU)	µp (ARK)	LU NC H BR EA K	A1 LIC &COM LAB (Dr.SVU+SNS+SSB) A2 µp Lab (ARK+MAS+PG)						
TUESDAY	S&S (SSB)	MAT (SMS)		LIC (LC)	PCS (Dr.SVU)		PLACEMENT						
WEDNESDAY	A2 LIC &COM LAB (LC+PT+SSB) A1 µp Lab (ARK+SB+NR)				MAT (SMS)		µp (ARK)	LIC (LC)	CS (Prof.SNS)				
THURSDAY	PCS (Dr.SVU)	µp (ARK)	BREAK	CS (Prof.SNS)	LIC (LC)		MAT (SMS)	CS (Prof.SNS)	S&S (SSB)				
FRIDAY	CS (Prof.SNS)	S&S (SSB)		µp (ARK)	S&S (SSB)		LIC (LC)	MAT (SMS)	PCS (Dr.SVU)				
SATURDAY	µp (ARK)	CS (Dr.SVU)		CS (Prof.SNS)	S&S (SSB)								
Subject		Code		Faculty			Initial						
Engg.Mathematics		15MAT 41		SRIHARI			SMS						
Microprocessors		15EC42		ARCHANA R K			ARK						
Control systems		15EC43		Prof.SRIDHARA S N			Prof.SNS						
Signals & Systems		15EC44		SANKALPA S BAILUR			SSB						
Principles of Communication Systems		15EC45		Dr. S V UMA			Dr.SVU						
Linerar Integrated Circuits		15EC46		LEENA C			LC						
Microprocessors Lab		15ECL47		ARK, MAS, PG, SB, NR									
LIC & Communication Lab		15ECL48		Dr.SVU, Prof.SNS, SSK, LC, PT									

CTTO	Signature of HOD (ECE)			Principal				Director				

R N S Institute of Technology

Department of Electronics and Communication Engineering

Time Table- Year 2017 (EVEN SEMESTER)

Semester: IV Section B			W.E.F.13-02-2017				ROOM:105			
	Class Teacher: Ghousia Begum						Class Counsellor: Narendra Kumar			
DAYS	TIME									
	8.40-9.40	9.40-10.40	10.40-11.00	11.00-12.00	12.00 -1.00	1.00 - 1.40	1.40-2.40	2.40-3.40	3.40-4.40	
MONDAY	MAT (SMS/DBS)	µp (SMN)	BREAK	PCS (VKM)	CS (PT)	LUNCH BREAK	LIC (NK)	CS (PT)	µp (SMN)	
TUESDAY	S&S (GS)	MAT (SMS/DBS)		CS (PT)	PCS (VKM)		PLACEMENT			
WEDNESDAY	LIC (NK)	CS (GS)		PCS (VKM)	MAT (SMS/DBS)		B1 LIC &COM LAB (NK+NKS+SSB)			

Subject	Code	Faculty	Initial
Engg. Mathematics	15MAT 41	SRIHARI/DHRUVATHARA	SMS/DBS
Microprocessors	15EC42	SMITHA N	SMN
Control systems	15EC43	PRAKASH TUNGA P	PT
Signals & Systems	15EC44	GHOUSIA BEGUM S	GS
Principles of Communication Systems	15EC45	MANJULA V K	VKM
Linear Integrated Circuits	15EC46	NARENDRAKUMAR	NK
Microprocessors Lab	15ECL47	SMN, IJM, NR, MAS, AV	
LIC & Communication Lab	15ECL48	NK, NKS, SSB, GS	

CTTO	Signature of HOD (ECE)			Principal			Director					
R N S Institute of Technology												
Department of Electronics and Communication Engineering												
Time Table- Year 2017 (EVEN SEMESTER)												
Semester: IV Section C			W.E.F.13-02-2017				ROOM:106					
	Class Teacher: Nandini K S						Class Counsellor: Vinaya Kusuma					
DAYS	TIME											
	8.40-9.40	9.40-10.40	10.40-11.00	11.00-12.00	12.00 -1.00	1.00 - 1.40	1.40-2.40	2.40-3.40	3.40-4.40			
MONDAY	MAT (DBS)	CS (CNP)	BREAK	LIC (VK)	PCS (NKS)	LUNCH BREAK	PLACEMENT					
TUESDAY	S&S (PT)	AT (DBS)		CS (NKS)	µp (BSU)		C1 LIC &COM LAB (VK+GS+LC) C2 µp Lab (BSU+AV+NR)					
WEDNESDAY	PCS (NKS)	CS (CNP)		p (BSU)	MAT (DBS)		LIC (VK)	S&S (PT)	LIC (VK)			
THURSDAY	CS (CNP)	IC (VK)		S&S (PT)	µp (BSU)		MAT (DBS)	S&S (PT)	CS (CNP)			
FRIDAY	C2 LIC &COM LAB (VK+PT+NKS) C1 µp Lab (BSU+PG+NR)				S&S (PT)		µp (BSU)	MAT (DBS)	PCS (NKS)			
SATURDAY	µp (BSU)	S (NKS)	BREAK	CS (CNP)	LIC (VK)							
Subject		Code		Faculty			Initial					
Engg.Mathematics		15MAT 41		DHRUVATHARA B S			DBS					
Microprocessors		15EC42		B S USHA			BSU					
Control systems		15EC43		C N PRABHAVATHI			CNP					
Signals & Systems		15EC44		PRAKASH TUNGA P			PT					
Principles of Communication Systems		15EC45		NANDHINI K S			NKS					
Linerar Integrated Circuits		15EC46		VINAYA KUSUMA			VK					
Microprocessors Lab		15ECL47		BSU, NR, AV, PG								
LIC & Communication Lab		15ECL48		VK, GS, Dr. SVU, PT NKS								

CTTO	Signature of HOD (ECE)			Principal			Director						

R N S Institute of Technology

Department of Electronics and Communication Engineering

Time Table- Year 2017 (EVEN SEMESTER)

Semester: IV Section D			W.E.F.13-02-2017				ROOM:111			
	Class Teacher: Mamatha A S						Class Counsellor:Smitha N			
DAYS	TIME									
	8.40-9.40	9.40-10.40	10.40-11.00	11.00-12.00	12.00 -1.00	1.00 - 1.40	1.40-2.40	2.40-3.40	3.40-4.40	
MONDAY	S&S (MAS)	LIC (MSO)	BREAK	CS (Prof. SNS)	PCS (NK)	LUNCH BREAK	µp (SBG)	LIC (MSO)	PCS (NK)	
TUESDAY	PCS (NK)	S&S (MAS)		µp (SBG)	LIC (MSO)		CS (Prof. SNS)	DIP MATHS		
WEDNESDAY	MAT (SMS)	CS (Prof. SNS)		PCS (NK)	µp (SBG)		S&S (MAS)	MAT (SMS)		
THURSDAY	µp (SBG)	S&S (MAS)		LIC (MSO)	MAT (SMS)		D1 LIC &COM LAB (MSO+VK+NKS) D2 µp Lab (SBG+PG+NR)			
FRIDAY	MAT (SMS)	PCS (NK)		S&S (MAS)	CS (Prof.SNS)		D2 LIC &COM LAB (MSO+SSB+GS) µp Lab (SBG+MAS+AV)			
SATURDAY	LIC (MSO)	CS (Prof.SNS)		MAT (SMS)	µp (SBG)		PLACEMENT			

Subject	Code	Faculty	Initial
Engg.Mathematics	15MAT 41	SRIHARI	SMS
Microprocessors	15EC42	SANGEETHA B G	SBG
Control systems	15EC43	Prof . SRIDHARA S N	Prof. SNS
Signals & Systems	15EC44	MAMATHA A S	MAS
Principles of Communication Systems	15EC45	NARENDRAKUMAR	NK
Linerar Integrated Circuits	15EC46	M S OHILESHWARI	MSO
Microprocessors Lab	15ECL47	SBG, PG, NR, MAS, AV	
LIC & Communication Lab	15ECL48	MSO, VK, NKS, SSB, GS	

CTTO	Signature of HOD (ECE)				Principal				Director				

R N S Institute of Technology

Department of Electronics and Communication Engineering

Time Table- Year 2017 (EVEN SEMESTER)

Semester: II M.Tech			W.E.F.13-02-2017				ROOM:Ground Floor		
DAYS	TIME								
	8.40-9.40	9.40-10.40	10.40-11.00	11.00-12.00	12.00 -1.00	1.00 - 1.40	1.40-2.40	2.40-3.40	3.40-4.40
MONDAY	DAM (VKM)	SOC (ARK)	BREAK	VTV (SBG)	RTOS (BSU)	LUNCH BREAK	SEMINAR		
TUESDAY	VTV (SBG)	AVLSI (MSO)		SOC (ARK)	SEMINAR		SEMINAR		
WEDNESDAY	RTOS (BSU)	AM (VKM)		AVLSI (MSO)	SOC (ARK)		SEMINAR		
THURSDAY	AVLSI (MSO)	TOS (BSU)		DAM (VKM)	SEMINAR		VLSI & ES LAB		
FRIDAY	SOC (ARK)	AVLSI (MSO)		VTV (SBG)	SEMINAR		SEMINAR		
SATURDAY	DAM (VKM)	VTV (SBG)		TOS (BSU)	SEMINAR				

Subject	Code	Faculty	Initial
Design Of Analog & Mixed Mode VLSI	16EVE21	MANJULA V K	VKM
VLSI Testing	16EVE22	SANGEETHYA B G	SBG
Advances in VLSI Design	16EVE23	M S OHILESHWARI	MSO
Real Time Operating System	16EVE24	B S USHA	BSU
SOC Design	16EVE254	ARCHANA R KULAKARNI	ARK
VLSI and ES Lab-2	16EVEL26		

Signature of HOD (ECE)				Principal				Director							
R N S Institute of Technology															
Department of Electronics and Communication Engineering															
Time Table- Year 2017 (EVEN SEMESTER)															
Semester: IV M.Tech				W.E.F.03-01-2017				ROOM:111							
DAYS	TIME														
	8.40-9.40	9.40-10.40	10.40-11.00	11.00-12.00	12.00 -1.00	1.00 - 1.40	1.40-2.40	2.40-3.40	3.40-4.40						
MONDAY	PROJECT WORK					LU NC H BR EA K	PROJECT WORK								
TUESDAY	PROJECT WORK						PROJECT WORK								
WEDNESDAY	PROJECT WORK						PROJECT WORK								
THURSDAY	PROJECT WORK						PROJECT WORK								
FRIDAY	SODC (LC)	AVLSI (KM)	BR REA K	SODC (LC)	AVLSI (KM)		PROJECT WORK								
SATURDAY	AVLSI (KM)	AVLSI (KM)		SODC (LC)	SODC (LC)										
Subject		Code		Faculty				Initial							
Subject		Code		Name of Faculty				Intials							
Synthesis & Optimization of Digital Circuits		14EVE41		Leena C				LC							

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