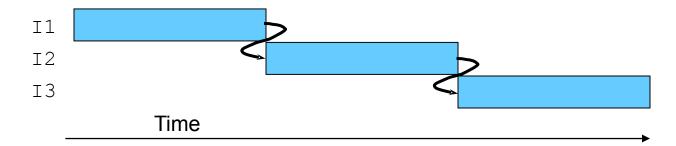
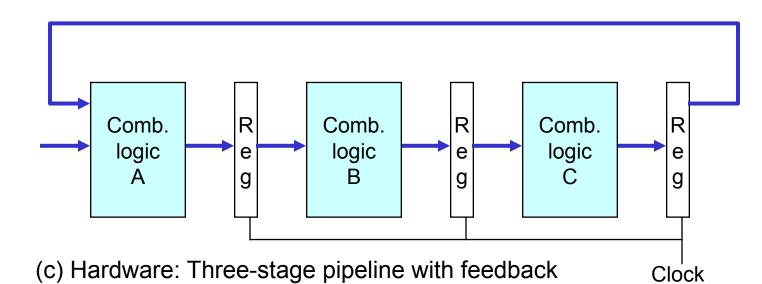


(a) Hardware: Unpipelined with feedback



(b) Pipeline diagram



I1 A B C
I2 A B C
I3 A B C
I4 A B C
Time

(d) Pipeline diagram