















K. K. & T. J.  
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uMudd MarkV.1 Tester  
F155 2018

- POWER
- 19 FPGA\_PROGRAMMED
  - 18 FPGA\_STATUS
  - 17 FPGA\_CONFIG
  - 16 NRST
  - 15 BLINK\_GOOD

- Instructions
- 1) Plug in 5V and GND from a power supply
  - 2) Plug in FPGA and SAM JTAG cables
  - 3) Download uMudd5\_fpga.sof from Quartus
  - 4) Download & run uMudd5\_sam from Keil
  - 5) if BLINK\_GOOD then everything is connected!