

Sheet: /Address Multiplexer/
File: addr_muxiplexer.sch

Title: RAM Address Selector

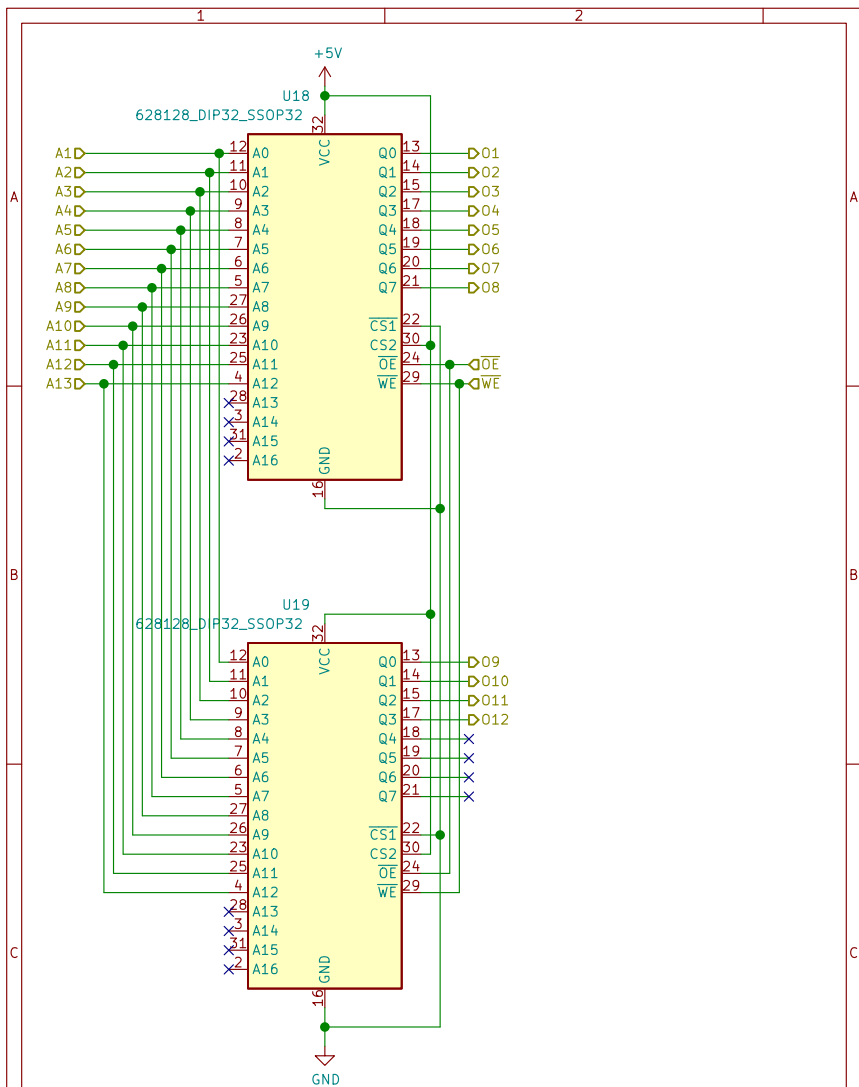
Size: User

Date:

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Id: 6/17



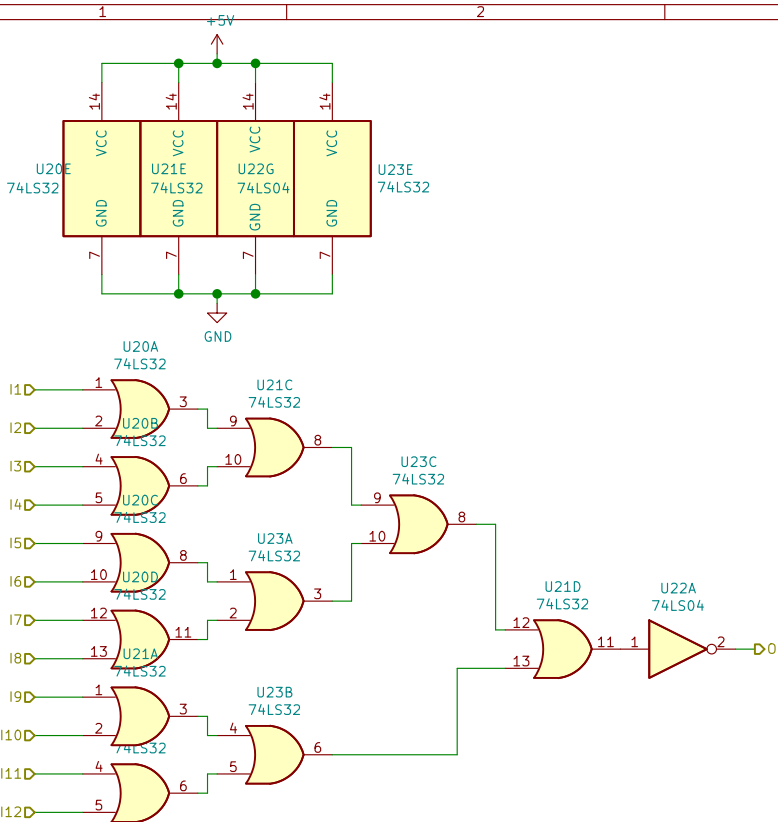
⚠ Not the actual
RAM chip

Sheet: /RAM/
File: ram.sch

Title: RAM

Size: User Date:
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Id: 7/17

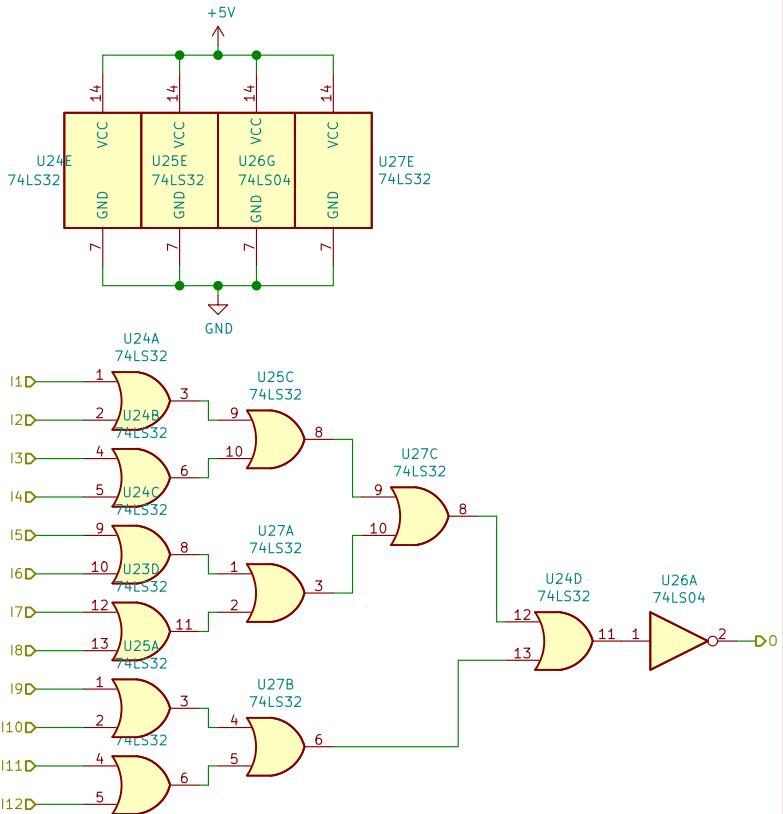


Sheet: /BUS Zero Checker/
File: bus_zerochecker.sch

Title: Bus Zero Checker

Size: User Date:
KiCad E.D.A. eeschema 5.1.6

Rev:
Id: 8/17

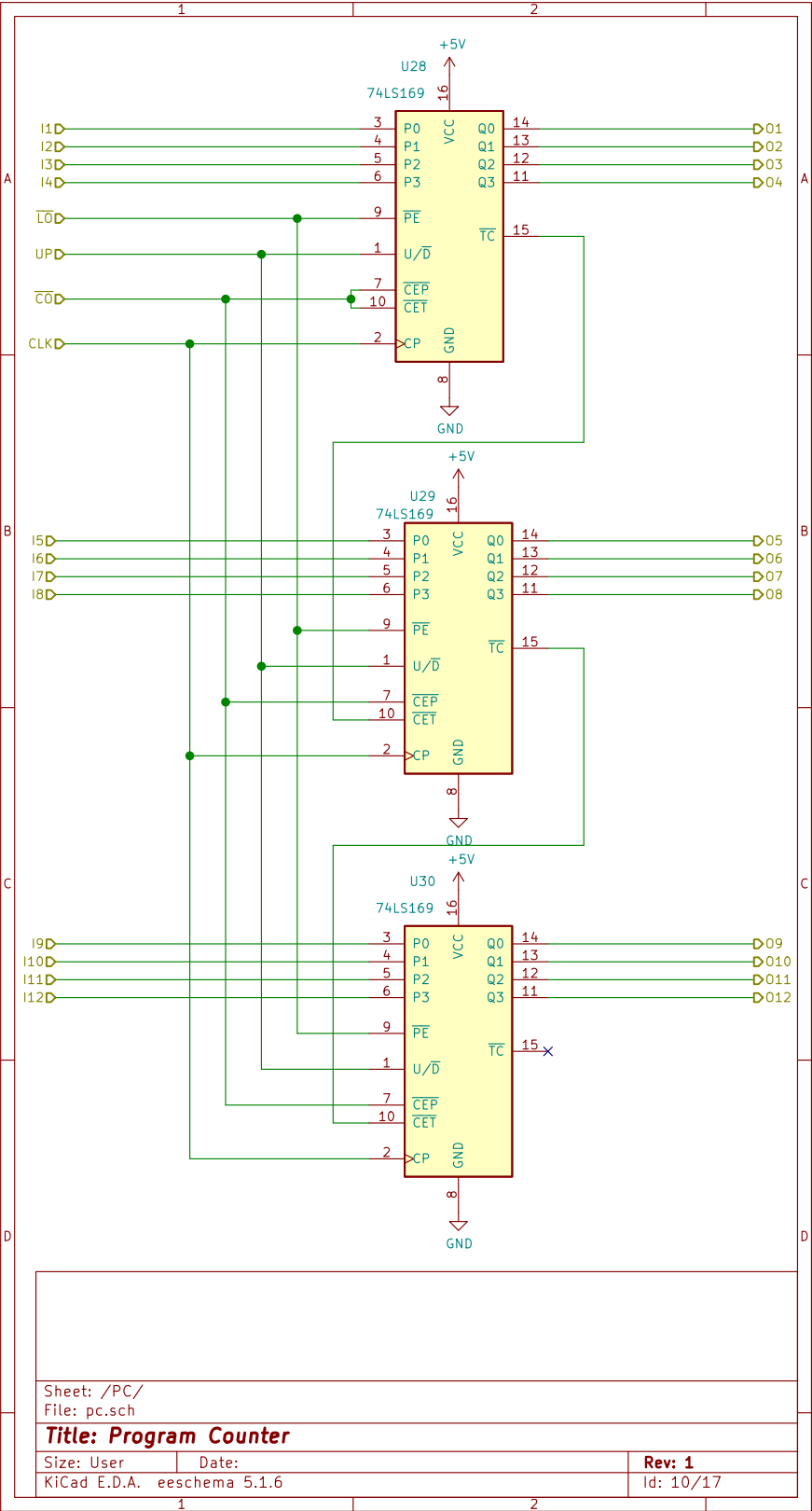


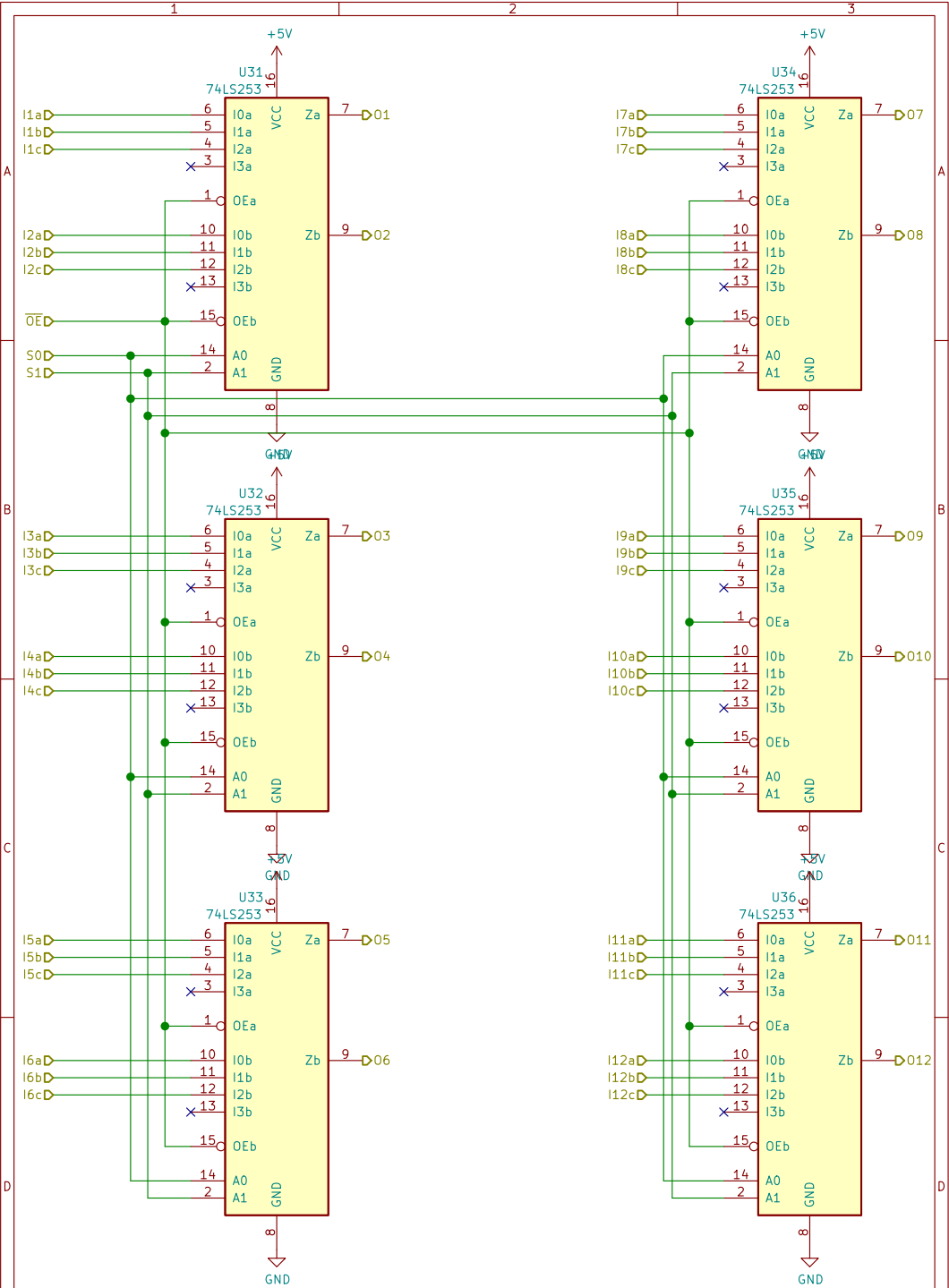
Sheet: /LPC Zero Checker/
File: lpc_zero_checker.sch

Title: Loop Counter Zero Checker

Size: User Date:
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Id: 9/17



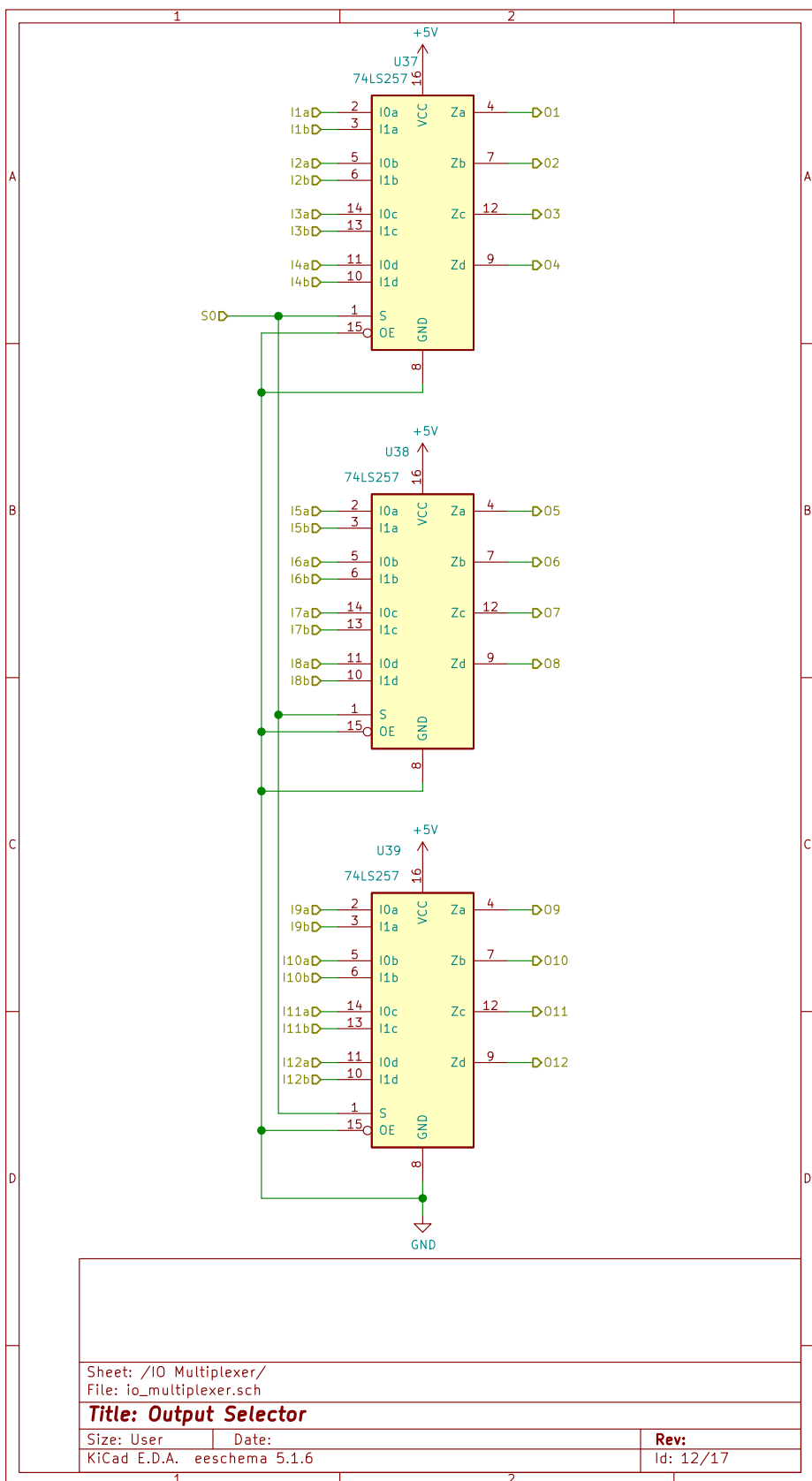


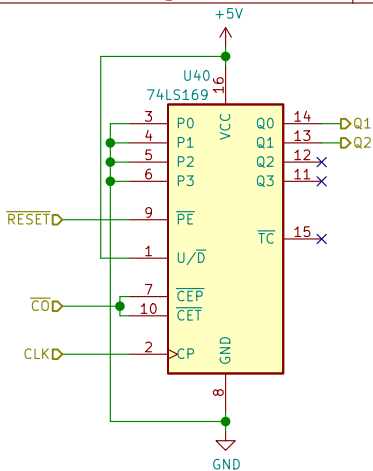
Sheet: /BUS Multiplexer/
File: bus_multiplexer.sch

Title: Bus Data Selector

Size: User Date:
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Rev:
Id: 11/17



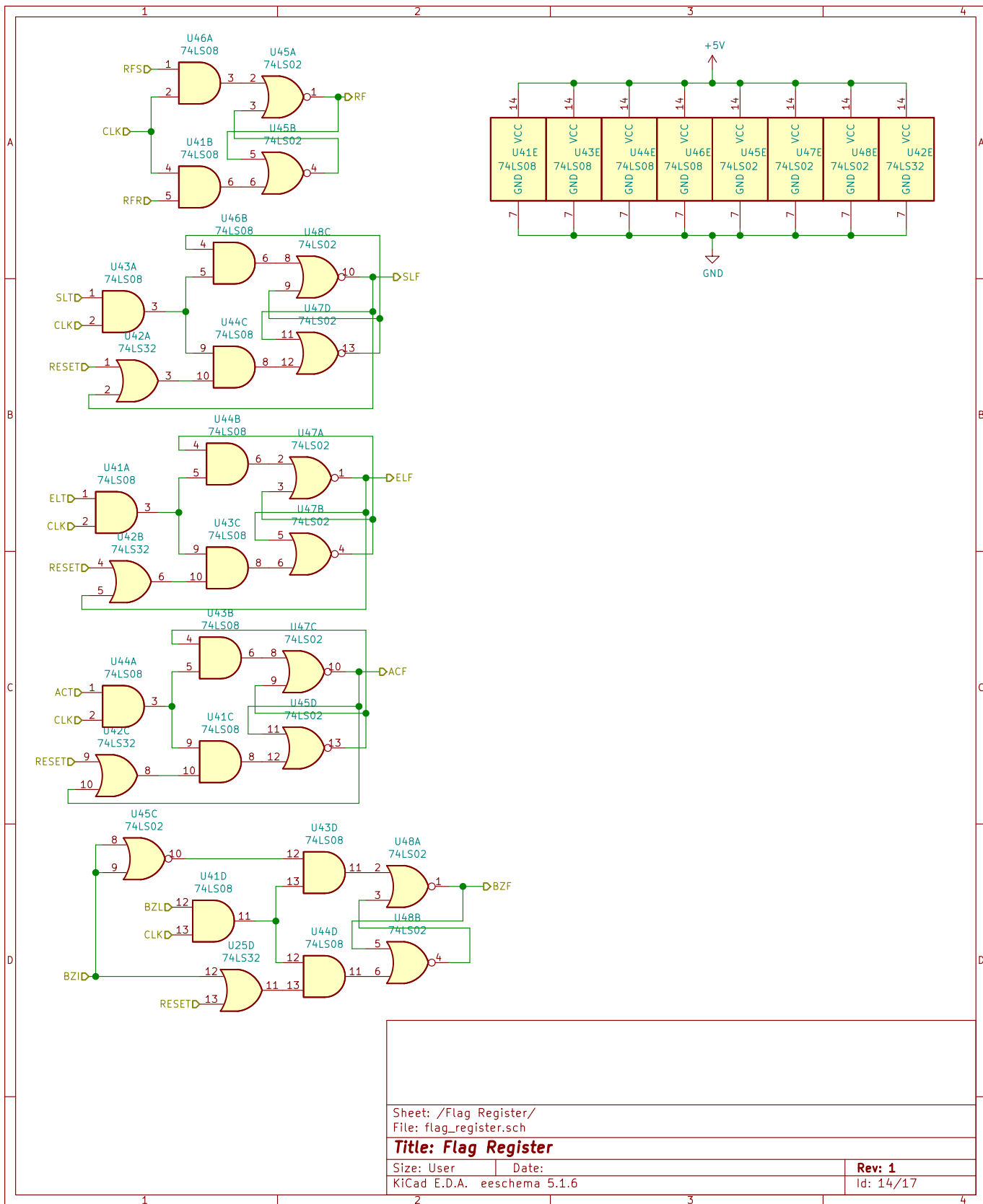


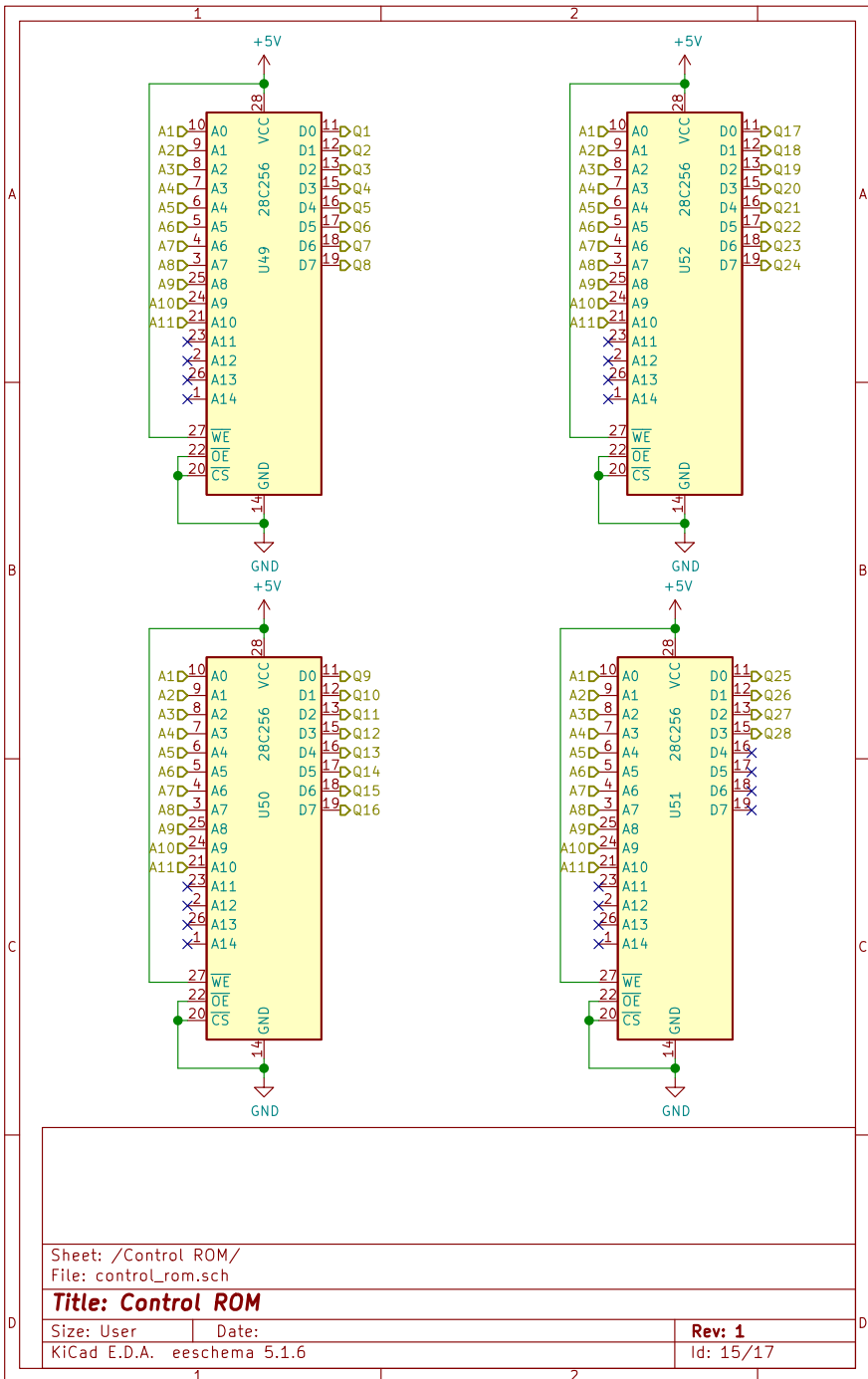
Sheet: /Phase Counter/
File: phase_counter.sch

Title: Phase Counter

Size: User Date:
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Rev: 1
Id: 13/17





TR_0□
TR_1□

SW_0□
SW_1□
SW_2□

▷CLK
▷CLK

C1
10nF

R13
1k

D13

D_Schottky

▷CLK_PULSE

GND

Sheet: /Clock/
File: clock.sch

Title: Clock

Size: User

Date:

Rev: -1

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Id: 16/17

