

1. Simplify the following expressions using Boolean algebraic laws. Give each step of your simplification and denote which laws you're using for each step. Do not skip or combine steps!

(a) $A \cdot (A + B \cdot B) + (B + A) \cdot (A + B)$

$$A \cdot (\overline{A} + BB) + \overline{(B + A)} \cdot (\overline{A} + B) \quad \text{Demorgan's Law}$$

$$A \cdot (\overline{A} + BB) + (\overline{B} \cdot \overline{A}) \cdot (\overline{A} + B) \quad \text{Idempotent Law}$$

$$A \cdot (\overline{A} + B) + (\overline{B} \cdot \overline{A}) \cdot (\overline{A} + B) \quad \text{Distributive Law}$$

$$(\overline{A} + B)(A + \overline{B} \cdot \overline{A}) \quad \text{Distributive Law}$$

$$(\overline{A} + B)(A + \overline{B} \cdot A + \overline{A}) \quad \text{Inverse Law}$$

$$(\overline{A} + B)(A + \overline{B} \cdot 1) \quad \text{Identity Law}$$

$$(\overline{A} + B)(A + \overline{B}) \quad \text{Distributive Law}$$

$$(\overline{A} + B)A + (\overline{A} + B)\overline{B} \quad \text{Distributive Law}$$

$$A\overline{A} + AB + \overline{A} \cdot \overline{B} + \overline{B}B \quad \text{Inverse Law}$$

$$0 + AB + \overline{A} \cdot \overline{B} + 0 \quad \text{Identity Law}$$

$$AB + \overline{A} \cdot \overline{B} + 0 \quad \text{Identity Law}$$

$$\mathbf{AB + \overline{A} \cdot \overline{B}}$$

(b) $\overline{C \cdot B} + A \cdot B \cdot C + \overline{A + C + \overline{B}}$

$$\overline{CB} + ABC + \overline{A + C + \overline{B}} \quad \text{Demorgan's Law}$$

$$(\overline{C} + \overline{B}) + ABC + \overline{A + C + \overline{B}} \quad \text{Demorgan's Law}$$

$$(\overline{C} + \overline{B}) + ABC + \overline{A} \cdot \overline{C}B \quad \text{Commutative Law}$$

$$(\overline{C} + \overline{B}) + ABC + \overline{A}B\overline{C} \quad \text{Absorption Law}$$

$$\mathbf{(\overline{C} + \overline{B}) + A}$$

(c) $(A + B) \cdot (\overline{A} + C) \cdot (\overline{C} + B)$

$(A + B)(\bar{A} + C)(\bar{C} + B)$	Distributive Law
$(A + B)(\bar{A} + C)\bar{C} + (A + B)(\bar{A} + C)B$	Distributive Law
$(A + B)(\bar{A} \cdot \bar{C}) + (A + B)(C\bar{C}) + (A + B)(\bar{A} + C)B$	Inverse Law
$(A + B)(\bar{A} \cdot \bar{C}) + (A + B)(0) + (A + B)(\bar{A} + C)B$	Zero and One Law
$(A + B)(\bar{A} \cdot \bar{C}) + (A + B)(\bar{A} + C)B$	Distributive Law
$(A + B)(\bar{A} \cdot \bar{C}) + (\bar{A} + C)(AB) + (\bar{A} + C)(BB)$	Idempotent Law
$(A + B)(\bar{A} \cdot \bar{C}) + (\bar{A} + C)(AB) + (\bar{A} + C)(B)$	Distributive Law
$A\bar{A} \cdot \bar{C} + \bar{A}B\bar{C} + (\bar{A} + C)(AB) + (\bar{A} + C)(B)$	Inverse Law
$0\bar{C} + \bar{A}B\bar{C} + (\bar{A} + C)(AB) + (\bar{A} + C)B$	Zero and One Law
$\bar{A}B\bar{C} + (\bar{A} + C)(AB) + (\bar{A} + C)B$	Distributive Law
$\bar{A}B\bar{C} + \bar{A}AB + ABC + (\bar{A} + C)B$	Inverse Law
$\bar{A}B\bar{C} + 0B + ABC + (\bar{A} + C)B$	Zero and One Law
$\bar{A}B\bar{C} + ABC + (\bar{A} + C)B$	Distributive Law
$\bar{A}B\bar{C} + ABC + \bar{A}B + BC$	Commutative Law
$\bar{A}B + BC + ABC + \bar{A}B\bar{C}$	Absorption Law
$\bar{A}B + BC$	

2. Find all solutions of the following Boolean equations without using the truth tables:

- (a) $(\bar{A} + C) \cdot (\bar{B} + D + A) \cdot (D + A \cdot \bar{C}) \cdot (\bar{D} + A) = 1$
- $(\bar{A} + C)(\bar{B} + D + A)(D + A\bar{C})(\bar{D} + A) = 1$
 - $(\bar{A} \cdot \bar{B} + \bar{A}D + \bar{A}A + C\bar{B} + CD + AC)(D + A\bar{C})(\bar{D} + A) = 1$
 - $(\bar{A}\bar{B} + \bar{A}D + C\bar{B} + CD + AC)(D + A\bar{C})(\bar{D} + A) = 1$
 - $(\bar{A}\bar{B}D + \bar{A}DD + C\bar{B}D + CDD + ACD + \bar{A}\bar{B}A\bar{C} + \bar{A}DA\bar{C} + C\bar{B}A\bar{C} + CDA\bar{C} + ACAC)(\bar{D} + A) = 1$
 - $(\bar{A}\bar{B}D + \bar{A}D + C\bar{B}D + CD + ACD + \bar{A}\bar{B} \cdot \bar{C})(\bar{D} + A) = 1$
 - $(\bar{A}\bar{B}D\bar{D} + \bar{A}D\bar{D} + C\bar{B}D\bar{D} + CDD + ACDD + \bar{A}\bar{B} \cdot \bar{C} \cdot \bar{D} + \bar{A}\bar{B}D + \bar{A}AD + AC\bar{B}D + ACD + AACD + \bar{A}\bar{B} \cdot \bar{C} = 1$
 - $\bar{A}\bar{B} \cdot \bar{C} \cdot \bar{D} + \bar{A}\bar{B}D + AC\bar{B}D + ACD + ACD + \bar{A}\bar{B} \cdot \bar{C} = 1$
 - $\bar{A}\bar{B} \cdot \bar{C} \cdot \bar{D} + \bar{A}\bar{B}D + AC\bar{B}D + ACD + \bar{A}\bar{B} \cdot \bar{C} = 1$
 - $\bar{B}(A\bar{C}D + AD + ACD + A\bar{C}) + ACD = 1$
 - $\bar{B}(ACD) + ACD = 1$

The equation is true when ALL of ACD is true, or $ACD = 1$

- (b) $((\bar{K} \cdot L \cdot N) \cdot (L + M)) + ((\bar{K} + L + N) \cdot (K \cdot \bar{L} \cdot \bar{M})) \cdot (\bar{K} + \bar{N}) = 1$
- $(\bar{K}LNL + \bar{K}LNM + K\bar{L}M\bar{K} + K\bar{L}L\bar{M} + K\bar{L}N\bar{M})(\bar{K} + \bar{N}) = 1$
 - $(\bar{K}LN + \bar{K}LNM + K\bar{L}N\bar{M})(\bar{K} + \bar{N}) = 1$
 - $\bar{K} \cdot \bar{K}LN + \bar{K}LNM\bar{K} + K\bar{K} \cdot \bar{L} \cdot \bar{M}N + \bar{K}LN\bar{N} + KLN\bar{M}\bar{N} + K\bar{L}M\bar{N}\bar{N} = 1$
 - $\bar{K}LN + \bar{K}LNM = 1$
 - $\bar{K}(LN + LNM) = 1$

- $\overline{K}(LN) = 1$
- $\overline{K}LN = 1$

The equation is true when LN is true and K is false, or $\overline{K}LN = 1$

3. Simplify the following expression by first constructing a truth table, using that truth table to construct a K-map, and then using that K-map to simplify.

$$Q = \overline{X} \cdot \overline{Y} \cdot Z + X \cdot Y \cdot \overline{Z} + \overline{X} \cdot Y \cdot \overline{Z} + X \cdot \overline{Y} \cdot \overline{Z}$$

Truth Table			
x	y	z	result
0	0	0	T
0	0	1	F
0	1	0	T
0	1	1	F
1	0	0	T
1	0	1	F
1	1	0	T
1	1	1	F

K-Map		
$\begin{array}{c} \nearrow \\ x \backslash y \end{array}$	0	1
00	1	0
01	1	0
11	1	0
10	1	0

This k-map simplifies the equation to \overline{Z}

4. Convert the following truth table into its sum of products representation:

A	B	C	Output
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	1

- Sums of product: $\overline{A} \cdot \overline{B} \cdot \overline{C} + \overline{A}B\overline{C} + \overline{A}BC + ABC$

5. Draw a logical circuit diagram that represents the above sum of products expression using OpenCircuits (<https://opencircuits.io/>). Clearly label all inputs/outputs and all components. Make sure you connect appropriate input components (e.g., buttons, switches, clocks, etc.) and output components (e.g., LEDs, displays, etc.) to facilitate testing of your circuit. Download your diagram using OpenCircuits' "Download" feature, rename it to hw4_SOP.circuit, and submit on Submittity along with your hw4.pdf file.

The answer to this question is in file hw4_SOP.circuit.

6. Test you circuit by supplying appropriate inputs and observing the expected values of the output. Explain why your set of tests is sufficient to prove that your logical circuit does in fact implement the required Boolean function. For each test, provide a picture (snapshot) of your circuit. Insert all such pictures in the hw4.pdf PDF file. You can download pictures (PNG, JPEG, or PDF) of your circuit diagram using OpenCircuits' "Export Image" feature.

The images of the circuit diagram is submitted as A_B_C.png

0_0_0.png is true as seen in the truth table, the LED light is on
 0_1_0.png is true as seen in the truth table, the LED light is on
 0_1_1.png is true as seen in the truth table, the LED light is on
 1_1_1.png is true as seen in the truth table, the LED light is on
 0_0_1.png is false as seen in the truth table, the LED light is off
 1_0_0.png is false as seen in the truth table, the LED light is off
 1_0_1.png is false as seen in the truth table, the LED light is off
 1_1_0.png is false as seen in the truth table, the LED light is off

7. Given inputs A and B, show that NOR ($A + B$) is functionally complete by giving logical circuits equivalent to AND ($A * B$), OR ($A + B$), and NOT A gates using only NOR gates in their construction.

NOR_and.circuit represents AND

NOR_or.circuit represents OR
NOR_not.circuit represents NOT

8. What is 5ED4 - 07A4 when these values represent unsigned 16-bit hexadecimal numbers? The result should be written in hexadecimal. Show your work.

$$\begin{array}{r}
 5 \quad E(14) \quad D(13) \quad 4 \\
 - \quad 0 \quad 7 \quad A(10) \quad 4 \\
 \hline
 5 \quad 7 \quad 3 \quad 0
 \end{array}$$

Answer: 5730

9. What is 5ED4 - 07A4 when these values represent signed 16-bit hexadecimal numbers stored in sign-magnitude format? The result should be written in hexadecimal. Show your work.

$$\begin{array}{r}
 5 \quad E(14) \quad D(13) \quad 4 \\
 - \quad 0 \quad 7 \quad A(10) \quad 4 \\
 \hline
 5 \quad 7 \quad 3 \quad 0
 \end{array}$$

Answer: 5730

Explanation:

In sign magnitude, the most significant bit is the sign bit. In our given equations, none of the leading bits are 1: 5 = 0101, 0 = 0000, The answer to this problem will be the same as the one prior.

10. Assume 185 and 122 are unsigned 8-bit decimal integers. Calculate 185 - 122. Is there overflow, underflow, or neither?

Decimal to binary conversion

$$\begin{aligned}
 185 &= 1011 \ 1001 \\
 122 &= 0111 \ 1010
 \end{aligned}$$

To perform the binary subtraction, we must convert the number being subtracted to a negative binary number using two's complement, and perform binary addition

Find 122 Two's Complement Number

$$\begin{aligned}
 122 &= 0111 \ 1010 \\
 &= 1000 \ 0101 \leftarrow \text{one's complement (flip all 1's and 0's)} \\
 &= 1000 \ 0110 \leftarrow \text{add 1 to the end (carried over)}
 \end{aligned}$$

Calculate 185 + 122(Two's Complement Number)

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      1 0000 0000 (carry)
      1011 1001 (185)
+     1000 0110 (122's Two's Complement Number)
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      1 0011 1111

```

The answer is represented in 9 bits, results in an:

OVERFLOW

11. Assume 151 and 214 are signed 8-bit decimal integers stored in two's complement format. Calculate 151 - 214 using saturating arithmetic. The result should be written in decimal. Show your work.

Decimal to binary conversion

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151 = 1001 0111
214 = 1101 0110

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151 in Two's Complement to obtain original value

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      0000 0000 (carry)
      0110 1000 (flip 1's and 0's)
+           1 (add 1)
-----
      0110 1001

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214 in Two's Complement to obtain original value

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      0000 0010 (carry)
      0010 1001 (flip 1's and 0's)
+           1 (add 1)
-----
      0010 1010

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Convert numbers back to decimal

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(151) 0110 1001 = 105
(214) 0010 1010 = 42

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Perform operation

$$105 - 42 = \boxed{63}$$

For 8-bit signed integers, saturation limits are from -128 to 127 , since $-128 \leq 63 \leq 127$, no saturation is required.

12. Assume 151 and 214 are unsigned 8-bit integers. Calculate $151 + 214$ using saturating arithmetic. The result should be written in decimal. Show your work.

Simple Addition

$$151 + 214 = 365$$

Saturation

For 8 bit unsigned integers, saturation limits are from 0 to 256, since $365 > 256$, the final answer will be saturated to the bounds of 256.

$$151 + 214 = \boxed{256}$$

13. What decimal number does the bit pattern $0 \times 0C000000$ represent if it is a two's complement integer? An unsigned integer?
14. If the bit pattern $0 \times 0C000000$ is placed into the Instruction Register, what MIPS instruction will be executed
15. Give a reason why we use two's complement representation for negative numbers in computer arithmetic. Give an example of its usage.