

# Computer Organization Review

- You should understand:
  - von Neumann Model.
  - Buses, Bridges, Transfer Rate, Clock.
  - Memory: DDR, DDR-2, ...
  - Cache, Buffer, Spool.
  - Direct Memory Access (DMA).
  - Port & Memory Mapped I/O.
  - CPU: privilege/kernel/supervisor mode and user mode.
  - Hardware Limitation.
  - Priority: Read vs Write.
  - Interrupts: Polling & Vectored.
  - Multiprocessors: Symmetric vs. Asymmetric.
  - Multicore & Multithreading.
  - Clustered Systems.
  - Numbers: base 2, base 8, base 10, base 16.
    - Base 2:  $110010101010_2$
    - Base 8:  $01234567_8 = 000\ 001\ 010\ 011\ 100\ 101\ 110\ 111_2$
    - Base 10:  $012\ 345\ 679$
    - Base 16:  $9AB\ CDEF_{16} = 1001\ 1010\ 1011\ 1100\ 1101\ 1110\ 1111_2$

# Block Diagram

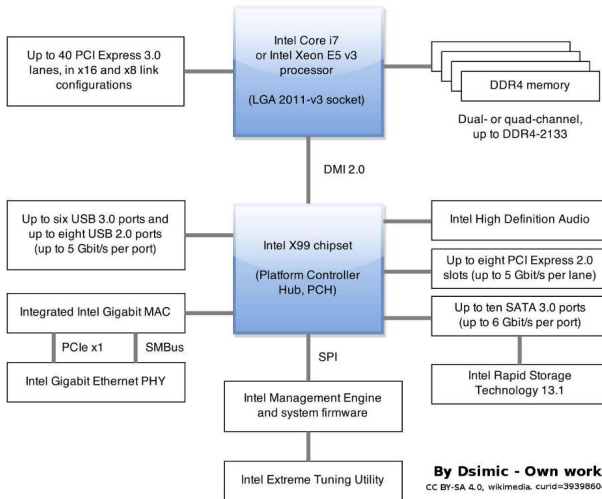


Figure: Block Diagram

# APIC (Advanced Programmable Interrupt Controller)

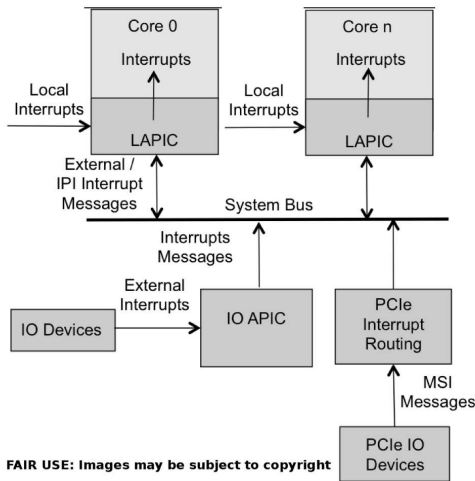


Figure: APIC (Advanced Programmable Interrupt Controller)