CSGE602055 Operating Systems CSF2600505 Sistem Operasi Week 05: Virtual Memory

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University of Indonesia

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Operating Systems 2020-1

A [08-10, Rm 3113, Mo/We] — B/M [10-12, Rm 3113, Mo/We] — C [13-15, Rm 3113, Mo/We]

D [10-12, Rm 2307(Mo), Rm 3113(We)] — E [08-10, Rm 2307(Mo), Rm 3113(We)]

Week	Schedule	Topic	OSC10
Week 00	27 Jan - 02 Feb 2020	Overview 1, Virtualization & Scripting	Ch. 1, 2, 18.
Week 01	03 Feb - 09 Feb 2020	Overview 2, Virtualization & Scripting	Ch. 1, 2, 18.
Week 02	10 Feb - 16 Feb 2020	Security, Protection, Privacy,	Ch. 16, 17
		& C-language	
Week 03	17 Feb - 23 Feb 2020	File System & FUSE	Ch. 13, 14, 15
Week 04	24 Feb - 01 Mar 2020	Addressing, Shared Lib, & Pointer	Ch. 9
Week 05	02 Mar - 08 Mar 2020	Virtual Memory	Ch. 10
Reserved	09 Mar - 13 Mar 2020	Q & E	
MidTerm	14-21 Mar 2020 (TBA)	MidTerm (UTS)	Subject to change.
Week 06	23 Mar - 31 Mar 2020	Concurrency: Processes & Threads	Ch. 3, 4
Week 07	01 Apr - 07 Apr 2020	Synchronization & Deadlock	Ch. 6, 7, 8
Week 08	08 Apr - 14 Apr 2020	Scheduling + W06/W07	Ch. 5
Week 09a	15 Apr - 19 Apr 2020	Storage, Firmware, Bootldr, & Systemd	Ch. 11
OCL	20 Apr - 26 Apr 2020	OnLine & CoLearnIng	
Week 09b	27 Apr - 28 Apr 2020	Storage, Firmware, Bootldr, & Systemd	Ch. 11
Week 10	29 Apr - 05 May 2020	I/O & Programming	Ch. 12
Reserved	06 May - 10 May 2020	Q & A	
Final	11-18 May 2020 (TBA)	Final (UAS)	This schedule is
Extra	25 Jun 2020	Extra assignment confirmation	subject to change.

STARTING POINT — https://os.vlsm.org/

Week 05: Memory

- Start
- Schedule
- 3 Week 05
- 4 Week 05
- Virtual Memory
- Memory Allocation Algorothm
- 7 TOP
- 8 06-memory
- The End

Week 05 Virtual Memory: Topics¹

- Review of physical memory and memory management hardware
- Virtual Memory
- Caching
- Memory Allocation
- Memory Performance
- Working sets and thrashing

¹Source: ACM IEEE CS Curricula 2013

Week 05 Virtual Memory: Learning Outcomes¹

- Explain memory hierarchy and cost-performance trade-offs.
 [Familiarity]
- Summarize the principles of virtual memory as applied to caching and paging. [Familiarity]
- Describe the reason for and use of cache memory (performance and proximity, different dimension of how caches complicate isolation and VM abstraction). [Familiarity]
- Defend the different ways of allocating memory to tasks, citing the relative merits of each. [Assessment]
- Evaluate the trade-offs in terms of memory size (main memory, cache memory, auxiliary memory) and processor speed. [Assessment]
- Discuss the concept of thrashing, both in terms of the reasons it occurs and the techniques used to recognize and manage the problem. [Familiarity]

¹Source: ACM IEEE CS Curricula 2013

Virtual Memory

- Reference: (OSC10-ch10 demo-w05)
- Virtual Memory: Separation Logical from Physical.
- Virtual Address Space: logical view.
- Demand Paging
- Page Flags: Valid / Invalid
- Page Fault
- Demand Paging Performance
- Copy On Write (COW)
- Page Replacement Algorithm
 - Reference String
 - First-In-First-Out (FIFO)
 - Belady Anomaly
 - Optimal Algorithm
 - Least Recently Used (LRU)
 - LRU Implementation
 - Lease Frequently Used (LFU)
 - Most Frequently Used (MFU)

Allocation Algorothm

- Page-Buffering Algorithms
- Allocation of Frames
- Fixed Allocation
- Priority Allocation
- Global vs. Local Allocation
- Non-Uniform Memory Access (NUMA)
- Thrashing
- Working-Set Model
- Shared Memory via Memory-Mapped I/O
- Kernel
 - Buddy System Allocator
 - Slab Allocator

TOP



Figure: top

TOP (2)

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top - 18:37:28 up 14:07, l user, load average: 2.77, 2.71, 2.74 Tasks: 128 total, l running, 127 sleeping, 0 stopped, 0 zombie %Cpu(s): 14.6 us, 17.2 sy, 0.0 ni, 68.1 id, 0.0 wa, 0.0 hi, 0.0 si, 0.0 st											
KiB Mem: 8197060 total, 935152 used, 7261908 free, 191512 buffers KiB Swap: 683004 total, 0 used, 683004 free. 639140 cached Mem											
	USER			VIRT	RES					COMMAND	
	root	PR 20		162032	112	SHR S		0.0	1882:33		
3448		20	0	0	0	0 5		0.0		kworker/0:2	
3198		20		0	0	0 5		0.0		kworker/4:0	
3062		20		0	0	0 5		0.0		kworker/1:2	
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2	root	20	0	0	0	0 9	0.0	0.0		kthreadd .	
3	root	20	0	0	0	0 9	0.0	0.0	0:15.26	ksoftirgd/0	
5	root	0	- 20	0	0	0 9	0.0	0.0	0:00.00	kworker/0:+	
8	root	20	0	0	0	0 9	0.0	0.0	0:00.00	rcu bh	
9	root	rt	0	0	0	0 9	0.0	0.0	0:00.00	migration/0	
10	root	rt	0	0	0	0 5		0.0	0:00.25	watchdog/0	
11	root	rt	0	0	0	0 5	0.0	0.0		watchdog/1	
12	root	rt	0	0	0	0 5	0.0	0.0	0:00.00	migration/1	
13	root	20	0	0	0	0 9	0.0	0.0	0:06.80	ksoftirqd/1	

Figure: "h" = help

TOP (3)

```
@rmsbase: ~
      @r... × | @r... × |
Fields Management for window 1:Def, whose current sort field is %CPU
  Navigate with Up/Dn, Right selects for move then <Enter> or Left commits,
   'd' or <Space> toggles display, 's' sets sort. Use 'q' or <Esc> to end!
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                                    = Tty Process G
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         = Priority
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                                    = Session Id
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         = Virtual Image
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                            SWAP
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         = Shared Memory
                                    = Swapped Size
         = Process Statu
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         = CPU Usage
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                                    = Data+Stack (K
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```

Figure: Moving Fields: "f"

TOP (4)

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  Navigate with Up/Dn, Right selects for move then <Enter> or Left commits,
   'd' or <Space> toggles display, 's' sets sort. Use 'g' or <Esc> to end!
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         = Resident Size
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 SHR
         = Shared Memory
                            GROUP
                                    = Group Name
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                                                               = NET namespace
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 COMMAND = Command Name/
 UID
                            TGID
         = Effective Use
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Figure: Moving Fields

TOP(5)

	@rmsbase: ~/I	Downloads						
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	2683572		1493/6	0		1856708		
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2841		50860		0	292		50860	
						1474084		
	2047252					1587052		
32501			27960		76	373220	33500	
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2512	685824	74188	36868	0	552	399836	74188	0

Figure: Write Configuration .toprc: "W"

06-memory

```
/* Copyright (C) 2016-2018 Rahmat M. Samik-Ibrahim
 * https://rahmatm.samik-ibrahim.vlsm.org/
 * This program is free script/software. This program is distributed in the
 * hope that it will be useful, but WITHOUT ANY WARRANTY; without even the
 * implied warranty of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE.
 * REVO4 Mon Mar 12 17:33:30 WIB 2018
 * START Mon Oct 3 09:26:51 WIB 2016
 */
#define MSIZEO 0x10000
#define MSIZE1 0x10008
#define MSTZE2 0x10009
#define MSTZE3 0x1000A
#define MSIZE4 0x20978
#define MSIZE5 0x20979
#define MSIZE6 0x2097A
#define MSIZE7 0xF0000
#define MSTZE8 0x10000
#define MSTZE9 0x1000
#define LINE
#define MAXSTR 80
#include <stdio.h>
#include <stdlib.h>
#include <unistd.h>
#include <string.h>
#include <sys/types.h>
void printLine(int line) {
   while(line-- > 0) putchar('x');
  putchar('\n'):
  fflush(NULL):
```

06-memory (2)

```
void main (void) {
   int
        msize[] = {MSIZE0, MSIZE1, MSIZE2, MSIZE3, MSIZE4,
                    MSIZE5, MSIZE6, MSIZE7, MSIZE8, MSIZE97:
   int ii. ii:
   int myPID = (int) getpid();
   char strSYS1[MAXSTR], strOUT[MAXSTR];
   char* chrStr = strSYS1:
   char* chrPTR:
   printLine(LINE):
   sprintf(strSYS1, "top -b -n 1 -p%d | tail -5", myPID);
   system (strSYS1);
   sprintf(strSYS1, "top -b -n 1 -p%d | tail -1", mvPID);
  for (ii=0; ii< (sizeof(msize)/sizeof(int)); ii++){
     chrStr = malloc(msize[ii]);
     fgets(strOUT, sizeof(strOUT)-1, popen(strSYS1, "r"));
     strOUT[(int) strlen(strOUT)-1]='\0':
     printf("%s [%X]\n", strOUT, msize[ii]);
     free(chrStr):
   7
  for (ii=0: ii< (sizeof(msize)/sizeof(int)): ii++){
     chrPTR = chrStr = malloc(msize[ii]):
     for (ii=0:ii<msize[ii]:ii++)
         *chrPTR++='x':
     fgets(strOUT, sizeof(strOUT)-1, popen(strSYS1, "r"));
      strOUT[(int) strlen(strOUT)-1]='\0':
     printf("%s [%X]\n", strOUT, msize[ii]);
     free(chrStr);
  }
}
```

06-memory (2)

>>>> \$./06-memory KiB Mem: 8197060 total, 957928 used, 7239132 free, 192520 buffers KiB Swap: 660108 cached 683004 total, 0 used, 683004 free. Mem PID VIRT RES SHR. SWAP CODE DATA USED nDRT [10000] [10008] Γ100091 [1000A] [20978] [20979] [2097A] [F0000] [10000] [1000]

06-memory (3)

4362	4376	1200	1068	0	4	524	1200	0 [1000]
4362	4376	1200	1068	0	4	524	1200	0 [10000]
4362	4376	1276	1068	0	4	524	1276	0 [10008]
4362	4376	1276	1068	0	4	524	1276	0 [10009]
4362	4376	1284	1068	0	4	524	1284	0 [1000A]
4362	4376	1284	1068	0	4	524	1284	0 [20978]
4362	4376	1352	1068	0	4	524	1352	0 [20979]
4362	4376	1352	1068	0	4	524	1352	0 [2097A]
4362	5340	2144	1068	0	4	1488	2144	0 [F0000]
4362	5340	2324	1068	0	4	1488	2324	0 [10000]
4362	5340	2324	1068	0	4	1488	2324	0 [1000]
>>>>> \$								

The End

- ☐ This is the end of the presentation.
- extstyle ext
- This is the end of the presentation.