CSGE602055 Operating Systems CSF2600505 Sistem Operasi Week 05: Virtual Memory

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University of Indonesia

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REV188 06-Feb-2019

Operating Systems 2019-1

A (Rm 3114) [Tu/Th 10-12] — B (Rm 3114) [Tu/Th 13-15] — C (Rm 3114) [Tu/Th 16-18] — D (Rm 2401) [Tu/Th 10-12] — E (Rm 2306) [Tu/Th 13-15]

Week	Schedule	Topic	OSC10
Week 00	07 Feb - 13 Feb 2019	Overview 1, Virtualization & Scripting	Ch. 1, 2, 18.
Week 01	14 Feb - 20 Feb 2019	Overview 2, Virtualization & Scripting	Ch. 1, 2, 18.
Week 02	21 Feb - 27 Feb 2019	Security, Protection, Privacy,	Ch. 16, 17
		& C-language	
Week 03	28 Feb - 06 Mar 2019	File System & FUSE	Ch. 13, 14, 15
Week 04	12 Mar - 18 Mar 2019	Addressing, Shared Lib, & Pointer	Ch. 9
Week 05	19 Mar - 25 Mar 2019	Virtual Memory	Ch. 10
Mid-Term	23-30 Mar 2019 (tba)	MidTerm (UTS)	
Week 06	02 Apr - 08 Apr 2019	Concurency: Processes & Threads	Ch. 3, 4
Week 07	09 Apr - 15 Apr 2019	Synchronization & Deadlock	Ch. 6, 7, 8
Week 08	16 Apr - 22 Apr 2019	Scheduling	Ch. 5
Week 09	23 Apr - 29 Apr 2019	Storage, BIOS, Loader, & Systemd	Ch. 11
Week 10	30 Apr - 06 May 2019	I/O & Programming	Ch. 12
Reserved	07 May - 17 May 2019		
Final	18-25 May 2019 (tba)	Final (UAS)	This schedule is
Extra	27 Jun 2019	Extra assignment confirmation	subject to change.

The Weekly Check List

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Week 05: Memory

- Start
- Schedule
- 3 Week 05
- 4 Week 05
- Virtual Memory
- Memory Allocation Algorothm
- TOP
- 8 06-memory
- The End

Week 05 Virtual Memory: Topics¹

- Review of physical memory and memory management hardware
- Virtual Memory
- Caching
- Memory Allocation
- Memory Performance
- Working sets and thrashing

¹Source: ACM IEEE CS Curricula 2013

Week 05 Virtual Memory: Learning Outcomes¹

- Explain memory hierarchy and cost-performance trade-offs.
 [Familiarity]
- Summarize the principles of virtual memory as applied to caching and paging. [Familiarity]
- Describe the reason for and use of cache memory (performance and proximity, different dimension of how caches complicate isolation and VM abstraction). [Familiarity]
- Defend the different ways of allocating memory to tasks, citing the relative merits of each. [Assessment]
- Evaluate the trade-offs in terms of memory size (main memory, cache memory, auxiliary memory) and processor speed. [Assessment]
- Discuss the concept of thrashing, both in terms of the reasons it occurs and the techniques used to recognize and manage the problem. [Familiarity]

¹Source: ACM IEEE CS Curricula 2013

Virtual Memory

- Reference: (OSC10-ch10 demo-w05)
- Virtual Memory: Separation Logical from Physical.
- Virtual Address Space: logical view.
- Demand Paging
- Page Flags: Valid / Invalid
- Page Fault
- Demand Paging Performance
- Copy On Write (COW)
- Page Replacement Algorithm
 - Reference String
 - First-In-First-Out (FIFO)
 - Belady Anomaly
 - Optimal Algorithm
 - Least Recently Used (LRU)
 - LRU Implementation
 - Lease Frequently Used (LFU)
 - Most Frequently Used (MFU)

Allocation Algorothm

- Page-Buffering Algorithms
- Allocation of Frames
- Fixed Allocation
- Priority Allocation
- Global vs. Local Allocation
- Non-Uniform Memory Access (NUMA)
- Thrashing
- Working-Set Model
- Shared Memory via Memory-Mapped I/O
- Kernel
 - Buddy System Allocator
 - Slab Allocator

TOP



Figure: top

TOP (2)

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				unning, 1						
				sy, 0.0						si, 0.0 st
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KiB Sv	wap:	683004	tota	ι,	0 used	, 683	8 004 fre	ee.	639140 cad	ined Mem
PTD	USER	PR	NI	VIRT	RES	SHR S	%CPU	%MFM	TIME+	COMMAND
518		20	0	162032	112	0 9		0.0	1882:33	
3448		20	0	0	0	0 5		0.0		kworker/0:2
3198		20	0	ŏ	Õ	0 5		0.0		kworker/4:0
3062	root	20	0	ō	0	0 5	5.0	0.0		kworker/1:2
3289	root	20	0	Ō	0	0 9	2.3	0.0		kworker/6:1
7	root	20	0	0	0	0 9	2.0	0.0	1:08.44	rcu sched
3376	root	20	0	0	0	0 9	1.3	0.0	0:18.73	kworker/5:0
1914	root	20	0	0	0	0 5	0.3	0.0	13:10.69	kworker/2:1
1	root	20	0	28684	4736	3012 5	0.0	0.1	0:02.91	systemd
2	root	20	0	0	0	0 9	0.0	0.0	0:00.01	kthreadd
3	root	20	0	0	0	0 9		0.0		ksoftirqd/0
5	root	0	- 20	0	0	0 5		0.0		kworker/0:+
8	root	20	0	0	0	0 9		0.0	0:00.00	
9	root	rt	0	0	0	0 5		0.0		migration/0
10	root	rt	0	0	0	0 5		0.0		watchdog/0
100000	root	rt	0	0	0	0 5		0.0		watchdog/1
1000	root	rt	0	0	0	0 5		0.0		migration/1
13	root	20	0	0	0	0 5	0.0	0.0	0:06.80	ksoftirqd/1

Figure: "h" = help

TOP (3)

```
@rmsbase: ~
      @r... × | @r... × |
Fields Management for window 1:Def, whose current sort field is %CPU
  Navigate with Up/Dn, Right selects for move then <Enter> or Left commits,
   'd' or <Space> toggles display, 's' sets sort. Use 'q' or <Esc> to end!
 PID
         = Process Id
                            TTY
                                    = Controlling T
                                                       USED
                                                               = Res+Swap Size
 USFR
         = Effective Use
                            TPGTD
                                    = Tty Process G
                                                       nsIPC
                                                               = IPC namespace
 PR
         = Priority
                            SID
                                    = Session Id
                                                       nsMNT
                                                               = MNT namespace
 NI
         = Nice Value
                            nTH
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                                                       nsNET
                                                               = NET namespace
 VIRT
         = Virtual Image
                            P
                                    = Last Used Cpu
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                                                               = PID namespace
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         = Resident Size
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                                    = CPU Time
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 SHR
                            SWAP
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                                                               = UTS namespace
         = Shared Memory
                                    = Swapped Size
         = Process Statu
                            CODE
                                    = Code Size (Ki
 %CPU
         = CPU Usage
                            DATA
                                    = Data+Stack (K
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         = Memory Usage
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                                    = Major Page Fa
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                            nMin
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                            Flags
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                            CGROUPS = Control Group
         = Real User Id
 RUSER
                            SUPGIDS = Supp Groups I
         = Real User Nam
 SUID
         = Saved User Id
                            SUPGRPS = Supp Groups N
 SUSER
         = Saved User Na
                            TGID
                                    = Thread Group
 GID
                            ENVIRON = Environment v
         = Group Id
 GROUP
         = Group Name
                            vMj
                                    = Major Faults
  PGRP
         = Process Group
                            vMn
                                    = Minor Faults
```

Figure: Moving Fields: "f"

TOP (4)

```
@rmsbase: ~
                        × @r... × @je... × @r... × @r... ×
                                                           @r... × @r... × @r... ×
Fields Management for window 1:Def, whose current sort field is %CPU
  Navigate with Up/Dn, Right selects for move then <Enter> or Left commits,
   'd' or <Space> toggles display, 's' sets sort. Use 'g' or <Esc> to end!
 PID
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 RES
         = Resident Size
                            GID
                                                      nsMNT
                                    = Group Id
                                                               = MNT namespace
 SHR
         = Shared Memory
                            GROUP
                                    = Group Name
                                                      nsNET
                                                               = NET namespace
 SWAP
         = Swapped Size
                            PGRP
                                    = Process Group
                                                      nsPID
                                                               = PID namespace
 CODE
         = Code Size (Ki
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                                    = Controlling T
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                                                               = UTS namespace
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         = Res+Swap Size
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                                    = Last Used Cpu
 %MEM
         = Memory Usage
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                                    = CPU Time
 USER
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         = Priority
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         = Nice Value
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         = CPU Time. hun
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 COMMAND = Command Name/
 UID
                            TGID
         = Effective Use
                                    = Thread Group
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         = Real User Id
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```

Figure: Moving Fields

TOP(5)

⊗ ⊜ ⊕	@rmsbase: ~/	Downloads							
гоо ×	@г ×	@r ×	@r × [@)r ×	e × @r.	× Ог	× @r	× @г ×	@r × @r ×
top -	19:57:14	4 up 11	:38, 1	user,	load av	verage: (9.43, 0	.54, 0.5	8
Tasks:	285 to	tal,	2 runni	ng, 283	sleepin	ng, 0 s	stopped	, θ zo	mbie
%Cpu(s	5): 3.8	us, 1	.3 sy,	0.0 ni,	94.6	id, 0.3	wa, 0	.0 hi,	0.0 si, 0.0 st
									buff/cache
KiB Sv	vap: 10	00444 t	otal,	994752	free,	5692	used.	12649780	avail Mem
	**								
PID	VIRT	RES						nDRT	
100000000000000000000000000000000000000	2377296			0		1642748			
1234	278216	87880	59116		2288	25164	87880		
	2683572					1856708			
	1687448					1179008			
2841	679488				292	389096	50860		
	1896812					1474084	The second second		
	2047252					1587052			
32501	630768	33500			76	373220	33500		
	8554396					7954584			
	2391592					1717824			
	2198448	274812		0		1532152	274812		
1292		0	0	0	0	0	0		
2514					36	448864			
	4515228			0	133688	3757984	360812	0	
32495	33488	3380		Θ	96	1264	3380		
2388	44036				212		4424		
2412	423204	11380		0	152		11380		
2512	685824	74188	36868	0	552	399836	74188	0	

Figure: Write Configuration .toprc: "W"

06-memory

```
/* Copyright (C) 2016-2018 Rahmat M. Samik-Ibrahim
 * https://rahmatm.samik-ibrahim.vlsm.org/
 * This program is free script/software. This program is distributed in the
 * hope that it will be useful, but WITHOUT ANY WARRANTY; without even the
 * implied warranty of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE.
 * REVO4 Mon Mar 12 17:33:30 WIB 2018
 * START Mon Oct 3 09:26:51 WIB 2016
 */
#define MSIZEO 0x10000
#define MSIZE1 0x10008
#define MSTZE2 0x10009
#define MSTZE3 0x1000A
#define MSIZE4 0x20978
#define MSIZE5 0x20979
#define MSIZE6 0x2097A
#define MSIZE7 0xF0000
#define MSTZE8 0x10000
#define MSTZE9 0x1000
#define LINE
#define MAXSTR 80
#include <stdio.h>
#include <stdlib.h>
#include <unistd.h>
#include <string.h>
#include <sys/types.h>
void printLine(int line) {
   while(line-- > 0) putchar('x');
  putchar('\n'):
  fflush(NULL):
```

06-memory (2)

```
void main (void) {
   int
        msize[] = {MSIZE0, MSIZE1, MSIZE2, MSIZE3, MSIZE4,
                    MSIZE5, MSIZE6, MSIZE7, MSIZE8, MSIZE97:
   int ii. ii:
   int myPID = (int) getpid();
   char strSYS1[MAXSTR], strOUT[MAXSTR];
   char* chrStr = strSYS1:
   char* chrPTR:
   printLine(LINE):
   sprintf(strSYS1, "top -b -n 1 -p%d | tail -5", myPID);
   system (strSYS1);
   sprintf(strSYS1, "top -b -n 1 -p%d | tail -1", mvPID);
  for (ii=0; ii< (sizeof(msize)/sizeof(int)); ii++){
     chrStr = malloc(msize[ii]);
     fgets(strOUT, sizeof(strOUT)-1, popen(strSYS1, "r"));
     strOUT[(int) strlen(strOUT)-1]='\0':
     printf("%s [%X]\n", strOUT, msize[ii]);
     free(chrStr):
   7
  for (ii=0: ii< (sizeof(msize)/sizeof(int)): ii++){
     chrPTR = chrStr = malloc(msize[ii]):
     for (ii=0:ii<msize[ii]:ii++)
         *chrPTR++='x':
     fgets(strOUT, sizeof(strOUT)-1, popen(strSYS1, "r"));
      strOUT[(int) strlen(strOUT)-1]='\0':
     printf("%s [%X]\n", strOUT, msize[ii]);
     free(chrStr);
  }
}
```

06-memory (2)

>>>> \$./06-memory KiB Mem: 8197060 total, 957928 used, 7239132 free, 192520 buffers KiB Swap: 660108 cached 683004 total, 0 used, 683004 free. Mem PID VIRT RES SHR. SWAP CODE DATA USED nDRT [10000] [10008] Γ100091 [1000A] [20978] [20979] [2097A] [F0000] [10000] [1000]

06-memory (3)

4362	4376	1200	1068	0	4	524	1200	0 [1000]
4362	4376	1200	1068	0	4	524	1200	0 [10000]
4362	4376	1276	1068	0	4	524	1276	0 [10008]
4362	4376	1276	1068	0	4	524	1276	0 [10009]
4362	4376	1284	1068	0	4	524	1284	0 [1000A]
4362	4376	1284	1068	0	4	524	1284	0 [20978]
4362	4376	1352	1068	0	4	524	1352	0 [20979]
4362	4376	1352	1068	0	4	524	1352	0 [2097A]
4362	5340	2144	1068	0	4	1488	2144	0 [F0000]
4362	5340	2324	1068	0	4	1488	2324	0 [10000]
4362	5340	2324	1068	0	4	1488	2324	0 [1000]
>>>>> \$								

The End

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- extstyle ext
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