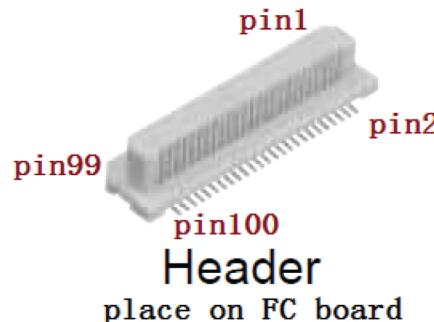
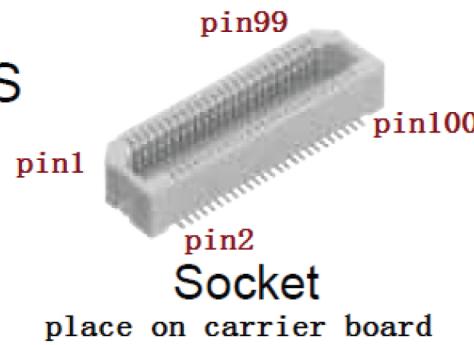


• P5KS



mated height=9mm

99	P99	IO_CH8	Gnd	P100	100
97	P97	IO_CH7	(0.2A max)	IO_VDD_3V3_OUT	98
95	P95	IO_CH6	(0.2A max)	VDD_3V3_SPEKTRUM	96
93	P93	IO_CH5		SPEKTRUM_dsm_IN	94
91	P91	IO_CH4		SAFETY_SWITCH_IN	92
89	P89	IO_CH3		nSAFETY_LED_OUT	90
87	P87	IO_CH2		Buzzer	88
85	P85	IO_CH1		SBUS_OUT	86
83	P83	Gnd		SBUS_RC_IN	84
81	P81	VDD_servo_IN		PPM_RC_IN	82
79	P79	FMU_CH8		RSSI_IN	80
77	P77	FMU_CH7		Gnd	78
75	P75	FMU_CH6	(0.3A max)	VDD_5V_PPM_RC	76
73	P73	FMU_CH5	(0.3A max)	VDD_5V_SBUS_RC	74
71	P71	FMU_CH4		VDD_5V_PERIPH	72
69	P69	FMU_CH3	(1.5A max)	VDD_5V_PERIPH	70
67	P67	FMU_CH2		VDD_5V_PERIPH	68
65	P65	FMU_CH1		VDD_5V_HIPOWER	66
63	P63	TIM5_SPARE_4	(1.5A max)	VDD_5V_HIPOWER	64
61	P61	FMU_CAP3		VDD_5V_HIPOWER	62
59	P59	FMU_CAP2		Gnd	60
57	P57	FMU_CAP1		Reserve	58
55	P55	USART1_TX_GPS1		SPI5_SCK_EXTERNAL1	56
53	P53	USART1_RX_GPS1		SPI5_MISO_EXTERNAL1	54
51	P51	I2C1_SCL_GPS1		SPI5_MOSI_EXTERNAL1	52
49	P49	I2C1_SDA_GPS1		SPI5_CS1_EXTERNAL1	50
47	P47	Gnd		SPI5_CS2_EXTERNAL1	48
45	P45	USART2_TX_TELEM1		SPI5_SYNC_EXTERNAL1	46
43	P43	USART2_RX_TELEM1		nSPI5_RESET_EXTERNAL1	44
41	P41	USART2_CTS_TELEM1		Gnd	42
39	P39	USART2_RTS_TELEM1		ADC1_3V3_IN	40
37	P37	USART3_TX_TELEM2		ADC1_6V6_IN	38
35	P35	USART3_RX_TELEM2		BAT1_I_sense	36
33	P33	USART3_CTS_TELEM2		BAT1_V_sense	34
31	P31	USART3_RTS_TELEM2		BAT2_I_sense	32
29	P29	UART4_TX_I2C2		BAT2_V_sense	30
27	P27	UART4_RX_I2C2		Gnd	28
25	P25	I2C2_SCL_UART4		VDD5V_Brick1	26
23	P23	I2C2_SDA_UART4		VDD5V_Brick1	24
21	P21	I2C4_SCL_EXTERNAL2		VDD5V_Brick1	22
19	P19	I2C4_SDA_EXTERNAL2		VDD5V_Brick1	20
17	P17	CAN1_H	(3A max)	VDD5V_Brick1	18
15	P15	CAN1_L		VDD5V_Brick1	16
13	P13	CAN2_H		VDD5V_Brick1	14
11	P11	CAN2_L		Gnd	12
9	P9	Gnd	J6	VDD5V_Brick2	10
7	P7	USB_D+		VDD5V_Brick2	8
5	P5	USB_D-		VDD5V_Brick2	6
3	P3	VBUS (0.5A max)	(3A max)	VDD5V_Brick2	4
1	P1	Gnd		VDD5V_Brick2	2

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