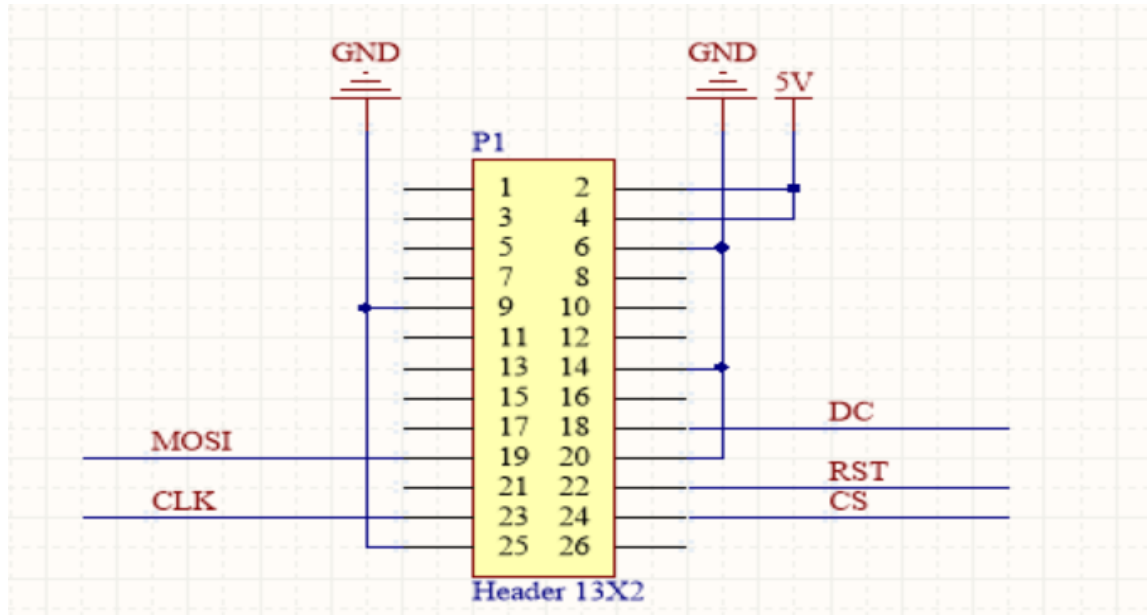# Hardware conection

Schematic diagram：