Argenis Jimenez Aguirre

CPE 403

TIVAC LAB 05

Task00: Execute the supplied code, no submission required.

```
1 // Argenis Jimenez Aguirre
 2 // CPE 403
3 // TIVAC LAB 05
 5 #include <stdint.h>
 6#include <stdbool.h>
 7 #include "inc/hw_memmap.h"
 8 #include "inc/hw_types.h"
 9#include "driverlib/debug.h"
10 #include "driverlib/sysctl.h"
11 #include "driverlib/adc.h"
12 #define TARGET_IS_BLIZZARD_RB1
13 #include "driverlib/rom.h"
14 #include "driverlib/gpio.h"
16 int main(void)
17 {
18
      // ADC FIFO data stored in array
19
      uint32_t ui32ADC@Value[4];
20
21
      // Variables for Average, Celsius and Fahrenheit Temperatures
22
      volatile uint32_t ui32TempAvg;
23
      volatile uint32_t ui32TempValueC;
24
      volatile uint32_t ui32TempValueF;
25
26
      // Run 40MHz System Clock
27
      SysCtlClockSet(SYSCTL_SYSDIV_5|SYSCTL_USE_PLL|SYSCTL_OSC_MAIN|SYSCTL_XTAL_16MHZ);
28
29
      // Enable ADC0 peripheral
      SysCtlPeripheralEnable(SYSCTL PERIPH ADC0);
30
      ADCHardwareOversampleConfigure(ADCO BASE, 64);
31
32
      // Use highest priority and set ADC0 and SS1
33
34
      ADCSequenceConfigure(ADC0_BASE, 1, ADC_TRIGGER_PROCESSOR, 0);
35
36
      // Sample steps 0-2 on Sequencer 1
      ADCSequenceStepConfigure(ADC0 BASE, 1, 0, ADC CTL TS);
37
38
      ADCSequenceStepConfigure(ADC0_BASE, 1, 1, ADC_CTL_TS);
39
      ADCSequenceStepConfigure(ADC0 BASE, 1, 2, ADC CTL TS):
```

```
40
41
      // Configure Interrupt flag and sample final step in Sequencer 1
42
      ADCSequenceStepConfigure(ADC0_BASE,1,3,ADC_CTL_TS|ADC_CTL_IE|ADC_CTL_END);
43
      // Enable Sequencer 1
44
45
      ADCSequenceEnable(ADC@_BASE, 1);
46
47
      while(1)
48
         // ADC conversion complete when status flag is cleared
49
50
         ADCIntClear(ADC0 BASE, 1);
         // Trigger ADC conversion
51
52
         ADCProcessorTrigger(ADC@_BASE, 1);
53
54
         // Wait for end of conversion
55
         while(!ADCIntStatus(ADC0_BASE, 1, false))
56
57
         // Copy samples available in FIFO to buffer
58
59
         ADCSequenceDataGet(ADC0_BASE, 1, ui32ADC0Value);
60
         // Calculate Average, Celsius and Fahrenheit temperature
         ui32TempAvg = (ui32ADC@Value[0] + ui32ADC@Value[1] + ui32ADC@Value[2] + ui32ADC@Value[3] + 2)/4;
61
         ui32TempValueC = (1475 - ((2475 * ui32TempAvg)) / 4096)/10;
62
63
         ui32TempValueF = ((ui32TempValueC * 9) + 160) / 5;
64
65
      }
66 }
```

Task 01: Change the ADC Sequencer to SS3. Turn on the LED at PF2 if the temperature is greater that 72 degF. Use internal temperature sensor for all SS2 sequence. Display the temperature in the built-in graph tool.

```
1// Argenis Jimenez Aguirre
2 // CPE 403
3 // TIVAC LAB 05
4
 5 #include <stdint.h>
 6 #include <stdbool.h>
 7#include "inc/hw_memmap.h"
 8 #include "inc/hw_types.h"
 9#include "driverlib/debug.h"
10 #include "driverlib/sysctl.h"
11 #include "driverlib/adc.h"
12 #define TARGET IS BLIZZARD RB1
13 #include "driverlib/rom.h"
14 #include "driverlib/gpio.h"
16 int main(void)
17 {
18
      // ADC FIFO data stored in array
19
      uint32_t ui32ADC@Value[4];
20
21
      // Variables for Average, Celsius and Fahrenheit Temperatures
22
      volatile uint32_t ui32TempAvg;
23
      volatile uint32_t ui32TempValueC;
24
      volatile uint32_t ui32TempValueF;
25
26
      // Run 40MHz System Clock
27
      SysCt1ClockSet(SYSCTL_SYSDIV_5|SYSCTL_USE_PLL|SYSCTL_OSC_MAIN|SYSCTL_XTAL_16MHZ);
28
29
      // Enable PortF and set Pin 2 to output
      SysCtlPeripheralEnable(SYSCTL PERIPH GPIOF);
30
31
      GPIOPinTypeGPIOOutput(GPIO_PORTF_BASE, GPIO_PIN_2);
32
      // Enable ADC0 peripheral
33
      SysCtlPeripheralEnable(SYSCTL PERIPH ADC0);
34
35
      ADCHardwareOversampleConfigure(ADCO_BASE, 64);
36
37
      // Use highest priority and set ADC0 and SS2
      ADCSequenceConfigure(ADC@_BASE, 2, ADC_TRIGGER_PROCESSOR, 0);
38
```

```
39
40
       // Sample steps 0-2 on Sequencer 2
41
       ADCSequenceStepConfigure(ADC@_BASE, 2, 0, ADC_CTL_TS);
       ADCSequenceStepConfigure(ADC0_BASE, 2, 1, ADC_CTL_TS);
ADCSequenceStepConfigure(ADC0_BASE, 2, 2, ADC_CTL_TS);
42
43
44
45
       // Configure Interrupt flag and sample final step in Sequencer 2
46
       ADCSequenceStepConfigure(ADC0_BASE,2,3,ADC_CTL_TS|ADC_CTL_IE|ADC_CTL_END);
47
48
       // Enable Sequencer 2
49
       ADCSequenceEnable(ADC0 BASE, 2);
50
51
       while(1)
52
       {
53
           // ADC conversion complete when status flag is cleared
          ADCIntClear(ADC0_BASE, 2);
54
55
           // Trigger ADC conversion
          ADCProcessorTrigger(ADC0_BASE, 2);
56
57
58
          // Wait for end of conversion
59
          while(!ADCIntStatus(ADC0_BASE, 2, false))
60
           {
61
62
           // Copy samples available in FIFO to buffer
63
          ADCSequenceDataGet(ADC0_BASE, 2, ui32ADC0Value);
64
           // Calculate Average, <u>Celsius</u> and <u>Fahrenheit</u> temperature
          ui32TempAvg = (ui32ADC@Value[0] + ui32ADC@Value[1] + ui32ADC@Value[2] + ui32ADC@Value[3] + 2)/4;
ui32TempValueC = (1475 - ((2475 * ui32TempAvg)) / 4096)/10;
65
66
67
          ui32TempValueF = ((ui32TempValueC * 9) + 160) / 5;
68
           // If temperature is greater than 72 turn on Blue LED
69
          if(ui32TempValueF > 72)
               GPIOPinWrite(GPIO_PORTF_BASE, GPIO_PIN_2, 4);
70
71
           // Turn off Blue LED
72
          else
73
               GPIOPinWrite(GPIO_PORTF_BASE, GPIO_PIN_2, 0);
74
       }
75 }
```

Task 02: Introduce hardware averaging to 32. Using the timer TIMER1A conduct an ADC conversion on overflow every 0.5 sec. Use the Timer1A interrupt. Display the temperature in the built-in graph tool.

```
1 // Argenis Jimenez Aguirre
 2 // CPE 403
 3 // TIVAC LAB 05
 5 #include <stdint.h>
 6#include <stdbool.h>
 7#include "inc/hw_memmap.h"
 8 #include "inc/hw_types.h"
 9#include "driverlib/debug.h"
10 #include "driverlib/sysctl.h"
11 #include "driverlib/adc.h"
12 #define TARGET_IS_BLIZZARD_RB1
13 #include "driverlib/rom.h"
14 #include "driverlib/gpio.h"
15 #include "driverlib/timer.h"
16 #include "driverlib/interrupt.h"
17 #include "driverlib/pin_map.h"
18 #include "inc/tm4c123gh6pm.h"
19 #include "driverlib/rom_map.h"
20
21 int main(void)
22 {
23
      uint32_t ui32Period;
24
25
      // Run 40MHz System Clock
26
      SysCt1ClockSet(SYSCTL_SYSDIV_5|SYSCTL_USE_PLL|SYSCTL_OSC_MAIN|SYSCTL_XTAL_16MHZ);
27
      // Enable Clock to Peripheral Timer1
28
29
      SysCtlPeripheralEnable(SYSCTL PERIPH TIMER1);
30
      // Configure Timer 1 to Periodic Mode
31
      TimerConfigure(TIMER1_BASE, TIMER_CFG_PERIODIC);
32
33
      // Create 0.5 Sec delay
34
      ui32Period = SysCtlClockGet() / 2;
35
      TimerLoadSet(TIMER1_BASE, TIMER_A, ui32Period-1);
36
      // Enable Vector in Timer1A
37
      IntEnable(INT_TIMER1A);
38
      // Enable event in Timer and generate interrupt
39
      TimerIntEnable(TIMER1 BASE. TIMER TIMA TIMEOUT):
```

```
40
41
      // Enable ADC0 peripheral
      SysCtlPeripheralEnable(SYSCTL PERIPH ADC0);
42
      ADCHardwareOversampleConfigure(ADC0 BASE, 32);
43
44
45
      // Enable PortF and set Pin 2 to output
46
      SysCtlPeripheralEnable(SYSCTL PERIPH GPIOF);
47
      GPIOPinTypeGPIOOutput(GPIO_PORTF_BASE, GPIO_PIN_2);
48
49
      // Use highest priority and set ADC0 and SS2
50
      ADCSequenceConfigure(ADC0_BASE, 2, ADC_TRIGGER_PROCESSOR, 0);
51
52
      // Sample steps 0-2 on Sequencer 2
53
      ADCSequenceStepConfigure(ADC0_BASE, 2, 0, ADC_CTL_TS);
54
      ADCSequenceStepConfigure(ADC0_BASE, 2, 1, ADC_CTL_TS);
55
      ADCSequenceStepConfigure(ADC0_BASE, 2, 2, ADC_CTL_TS);
56
57
      // Configure Interrupt flag and sample final step in Sequencer 2
58
      ADCSequenceStepConfigure(ADC0_BASE,2,3,ADC_CTL_TS|ADC_CTL_IE|ADC_CTL_END);
59
60
      // Enable Sequencer 2
61
      ADCSequenceEnable(ADC0_BASE, 2);
62
      // Master interrupt enable API for all interrupts
63
64
      IntMasterEnable();
65
      // Start timer and trigger interrupts in timeouts
66
      TimerEnable(TIMER1_BASE, TIMER_A);
67
68
      while(1)
69
      {
70
      }
71 }
73 void Timer1IntHandler(void)
74 {
75
      // Clear the timer interrupt
76
      TimerIntClear(TIMER1_BASE, TIMER_TIMA_TIMEOUT);
```

```
77
78
      // ADC FIFO data stored in array
79
      uint32_t ui32ADC@Value[4];
80
81
      // Variables for Average, Celsius and Fahrenheit Temperatures
      volatile uint32_t ui32TempAvg;
82
      volatile uint32_t ui32TempValueC;
83
84
      volatile uint32_t ui32TempValueF;
85
86
      // ADC conversion complete when status flag is cleared
87
      ADCIntClear(ADC0 BASE, 2);
      // Trigger ADC conversion
88
89
      ADCProcessorTrigger(ADC@_BASE, 2);
90
91
      // Wait for end of conversion
92
      while(!ADCIntStatus(ADC0_BASE, 2, false))
93
      {
94
      // Copy samples available in FIFO to buffer
95
96
      ADCSequenceDataGet(ADC0_BASE, 2, ui32ADC0Value);
97
      // Calculate Average, Celsius and Fahrenheit temperature
98
      ui32TempAvg = (ui32ADCØValue[0] + ui32ADCØValue[1] + ui32ADCØValue[2] + ui32ADCØValue[3] + 2)/4;
99
      ui32TempValueC = (1475 - ((2475 * ui32TempAvg)) / 4096)/10;
      ui32TempValueF = ((ui32TempValueC * 9) + 160) / 5;
00
01
      // If temperature is greater than 72 turn on Blue LED
      if(ui32TempValueF > 72)
02
03
          GPIOPinWrite(GPIO_PORTF_BASE, GPIO_PIN_2, 4);
04
      // Turn off Blue LED
05
06
          GPIOPinWrite(GPIO_PORTF_BASE, GPIO_PIN_2, 0);
07 }
```