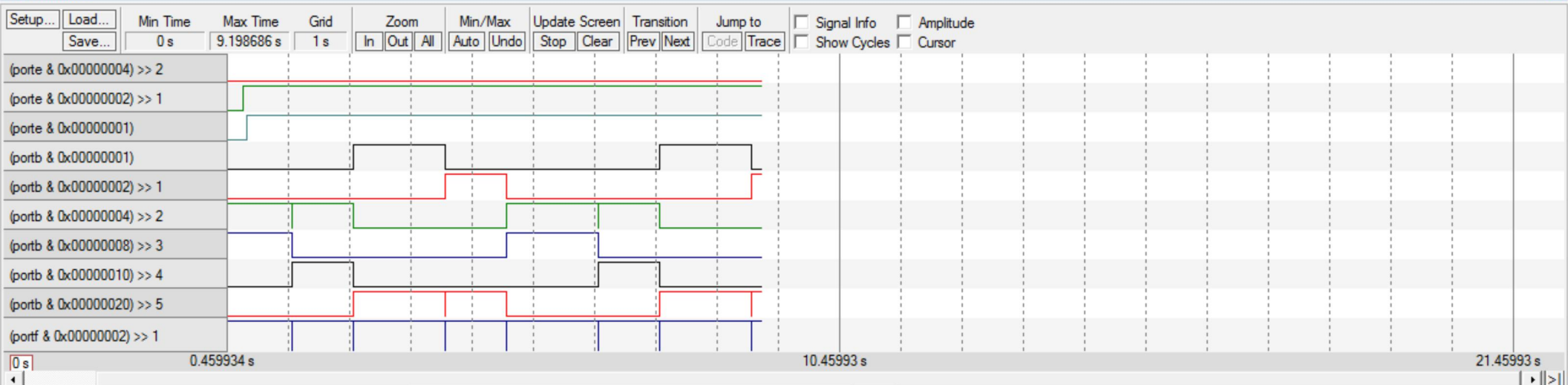


Logic Analyzer



Disassembly Logic Analyzer

startup.s TrafficLight.c

Command

LA (port & 0x00000001)

*** error 41: incompatible operand type

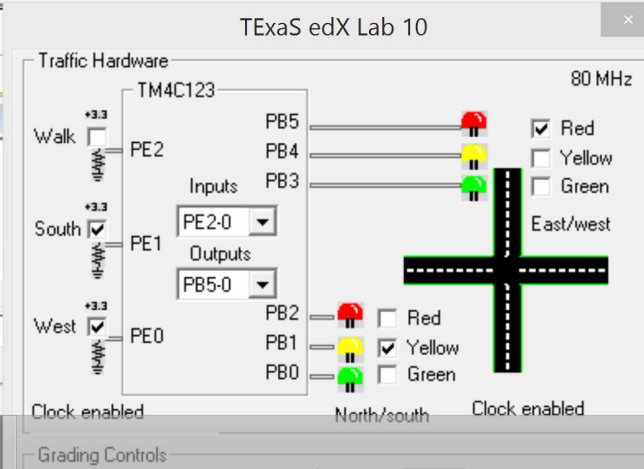
LA (portf & 0x00000002) >> 1

Error: attempting to change PDEN while CR

>

ASSIGN BreakDisable BreakEnable BreakKill

Magnifier



TEaX edX Lab 10

Port F Hardware

TM4C123

SW1 SW2

PF3 PF2 PF1 LED LED Red

Port F Registers

DATA: 0x13 PUR: 0x00 LOCK: 0x01

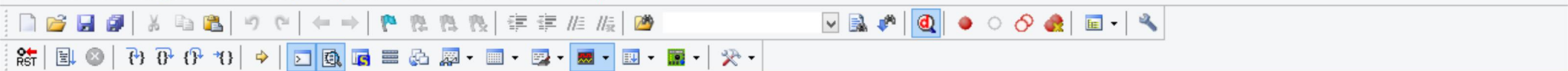
DIR: 0x0F PDR: 0x00 CR: 0xFF

DEN: 0x0F RCGC2: 0x0000003A Clock enabled

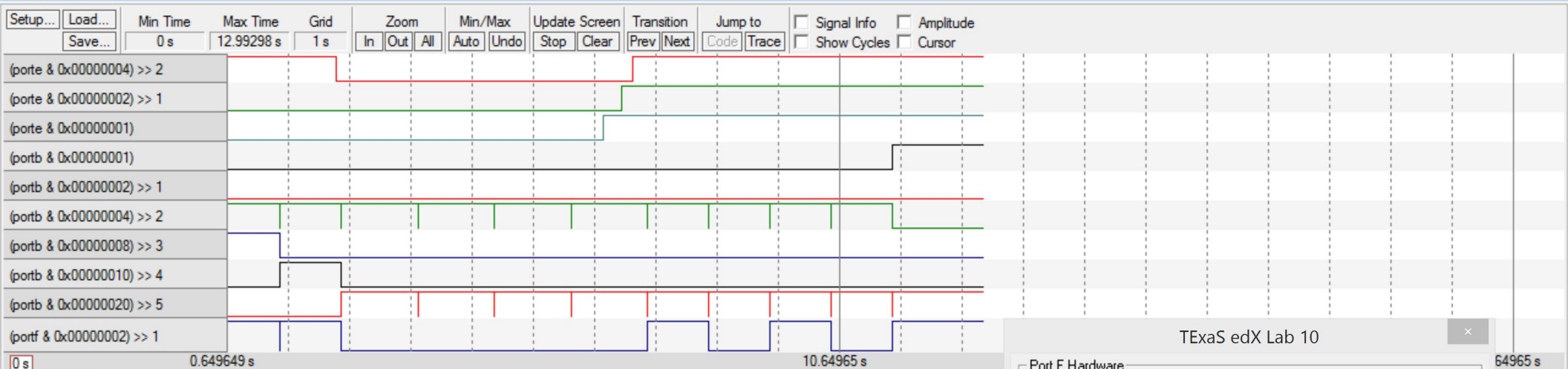
ILL

L OVR R/W

3:44 PM 3/10/2015



Logic Analyzer



0 s 0.649649 s 10.64965 s

Disassembly Logic Analyzer

startup.s TrafficLight.c

Command

LA (port & 0x00000001)

*** error 41: incompatible operand type

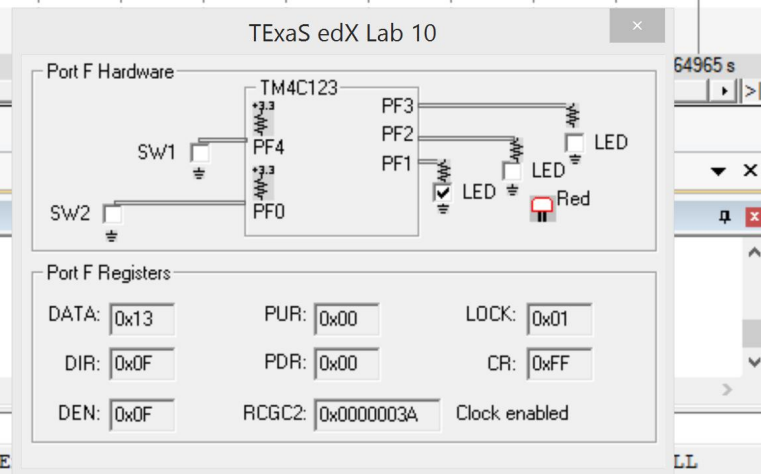
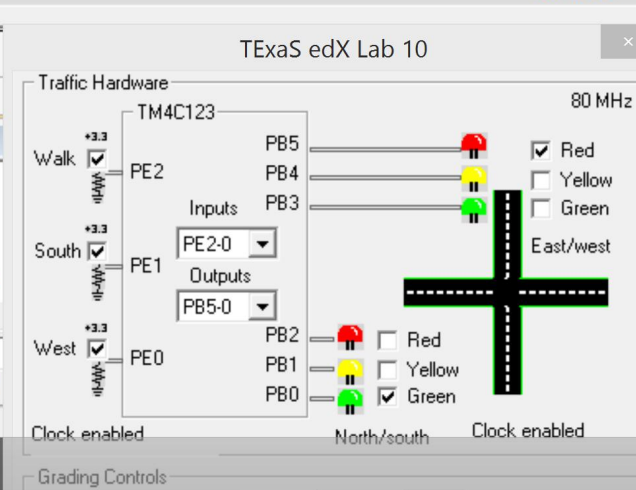
LA (portf & 0x00000002) >> 1

Error: attempting to change PDEN while CR

<

>

ASSIGN BreakDisable BreakEnable BreakKill



Port F Registers

DATA: 0x13 PUR: 0x00 LOCK: 0x01

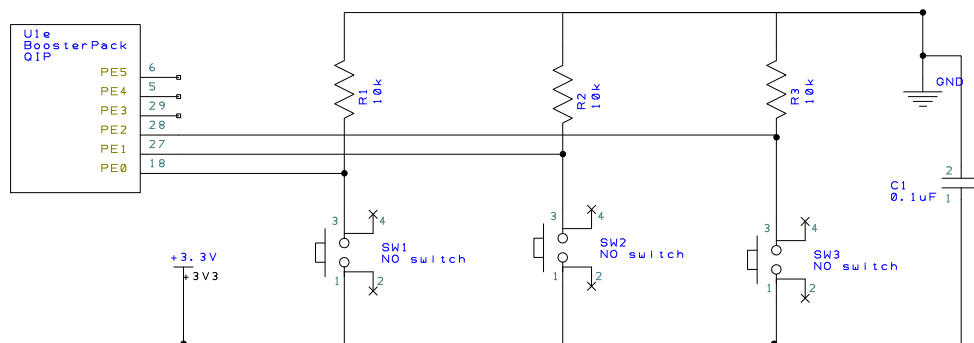
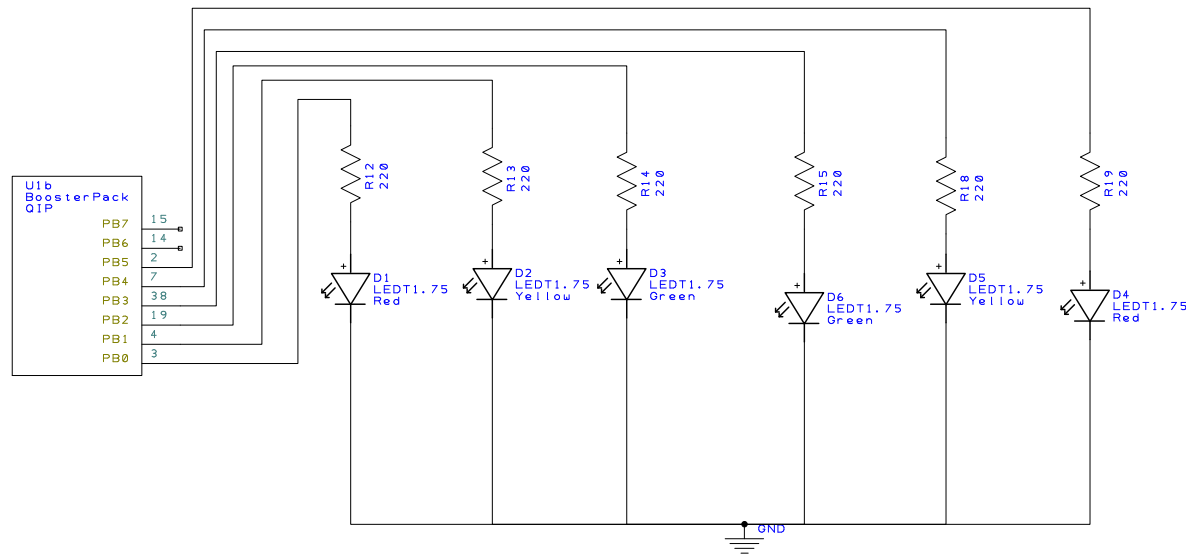
DIR: 0x0F PDR: 0x00 CR: 0xFF

DEN: 0x0F RCGC2: 0x0000003A Clock enabled

Simulation t1: 12.99328944 sec L:38 C:1 CAP NUM SCRL OVR R/W

Grading Controls

3:47 PM 3/10/2015



All resistors are 1/4 watt 5% carbon composition
 C1 is ceramic Z5U
 Switches are www.BGMicro.com SWT1043
 Red LEDs, T1 3/4, 20mA Digikey 160-1087-ND
 Yellow LEDs, T1 3/4, 20mA Digikey 160-1088-ND
 Green LEDs, T1 3/4, 20mA Digikey 160-1089-ND
 Slide pot, Bourns SSHA20B20300 www.AllElectronics.com SP-20K

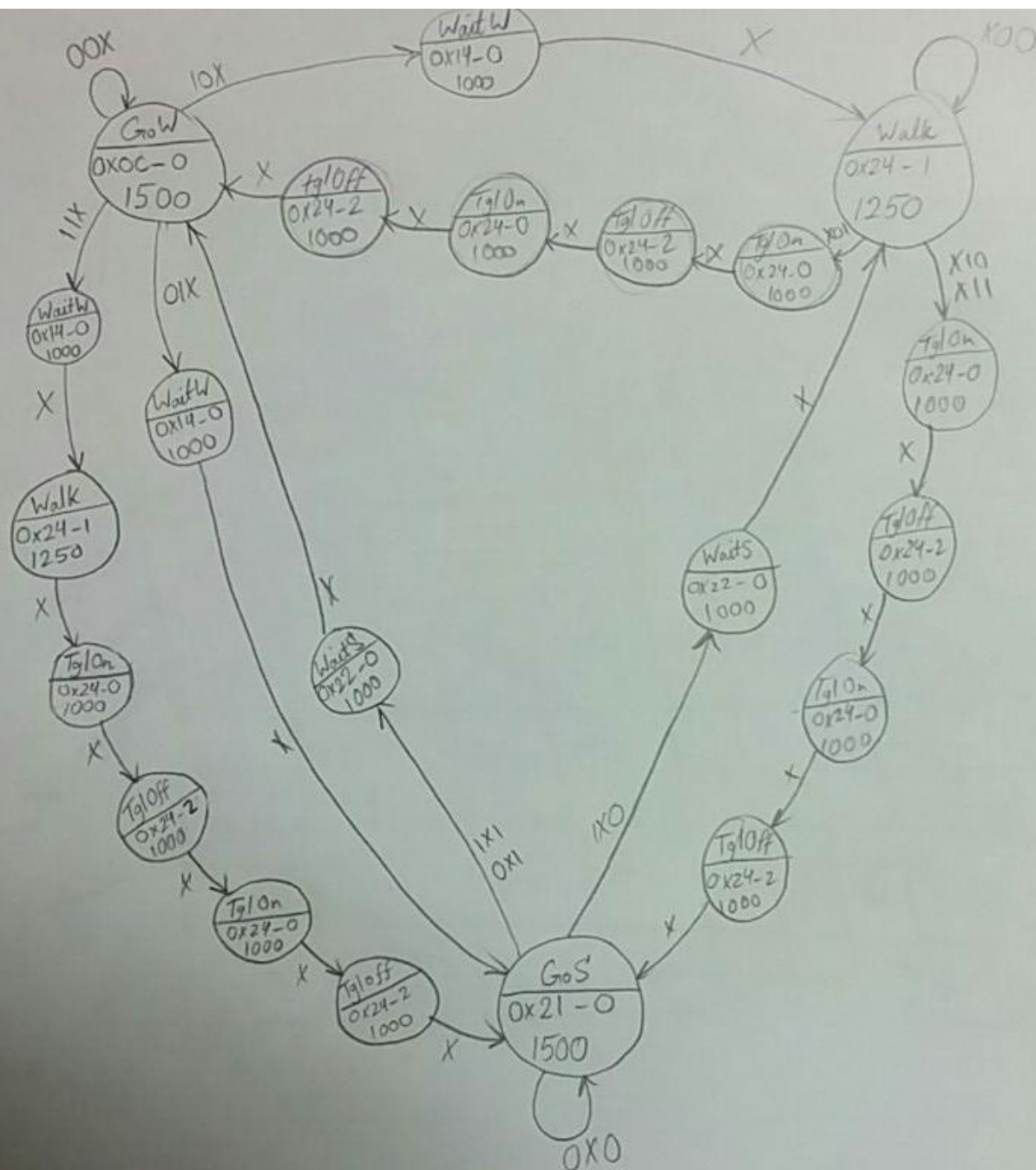
University Of Texas At Austin

Schematic Name: EK-LM4F120XL or EK-TM4C123GXL

Name(s): Aria Pahlavan and Khalid Qarryzada

Date: March 10, 2015

Semester: Spring 2015



PE

2	1	0
Walk	S	W

PF

Walk	Toggle	Don't
3	2	1
off		Walk

PB

W	W	S	S
5	4	3	2
			1
			0