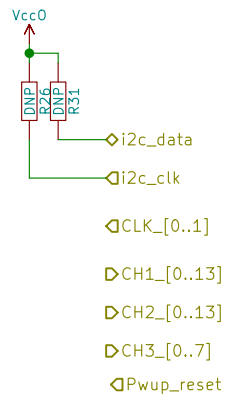
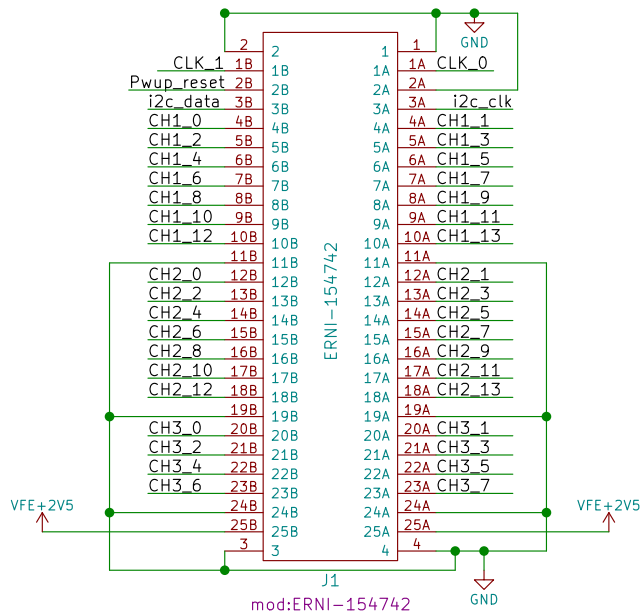


Sheet: /A_out/		
File: A_out.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad no-vcs-found-product		Id: 2/12



Sheet: /A_in/
File: A_in.sch

Title:

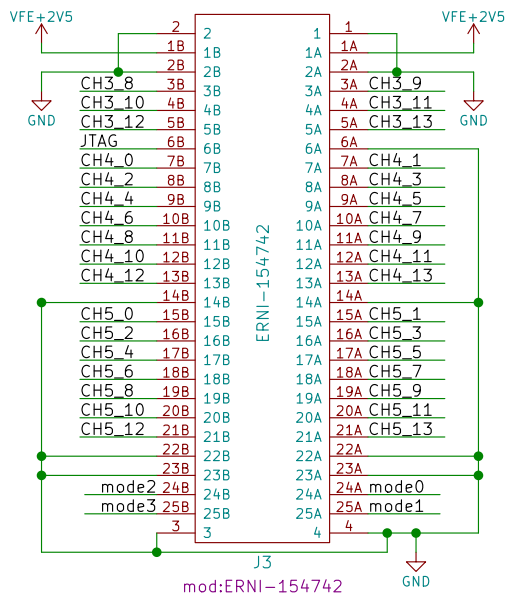
Size: A4

Date:

KiCad E.D.A. kicad no-vcs-found-product

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JTAG → JTAG

→ CH4_[0..13]

→ CH5_[0..13]

→ CH3_[8..13]

mode[0..3] → mode[3..0]

Sheet: /B_in/
File: B_in.sch

Title:

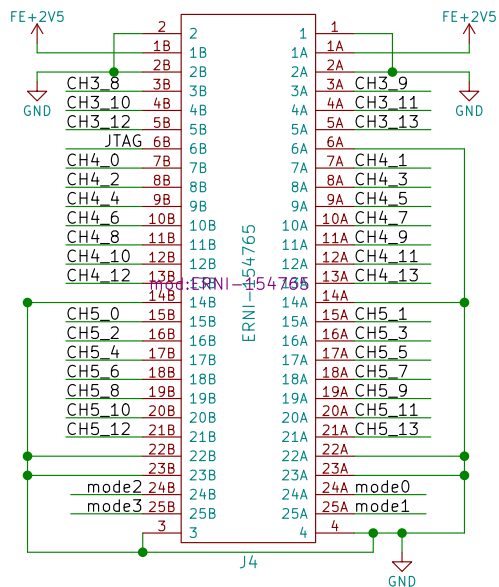
Size: A4

Date:

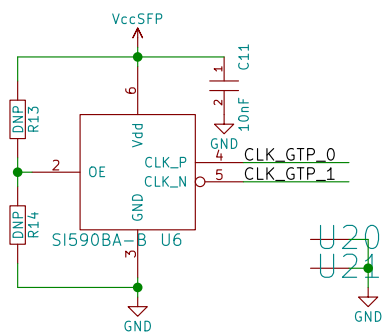
KiCad E.D.A. kicad no-vcs-found-product

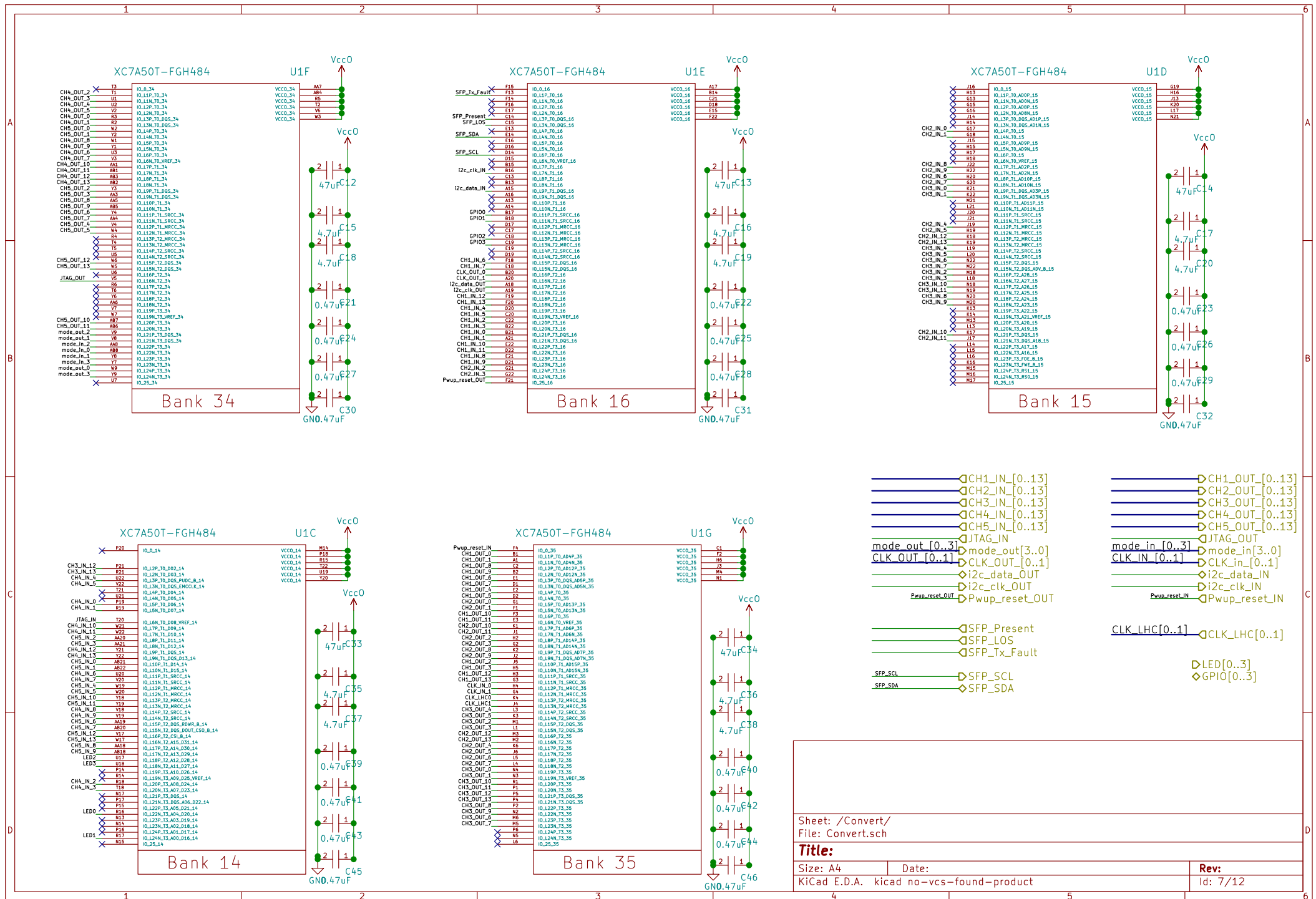
Rev:

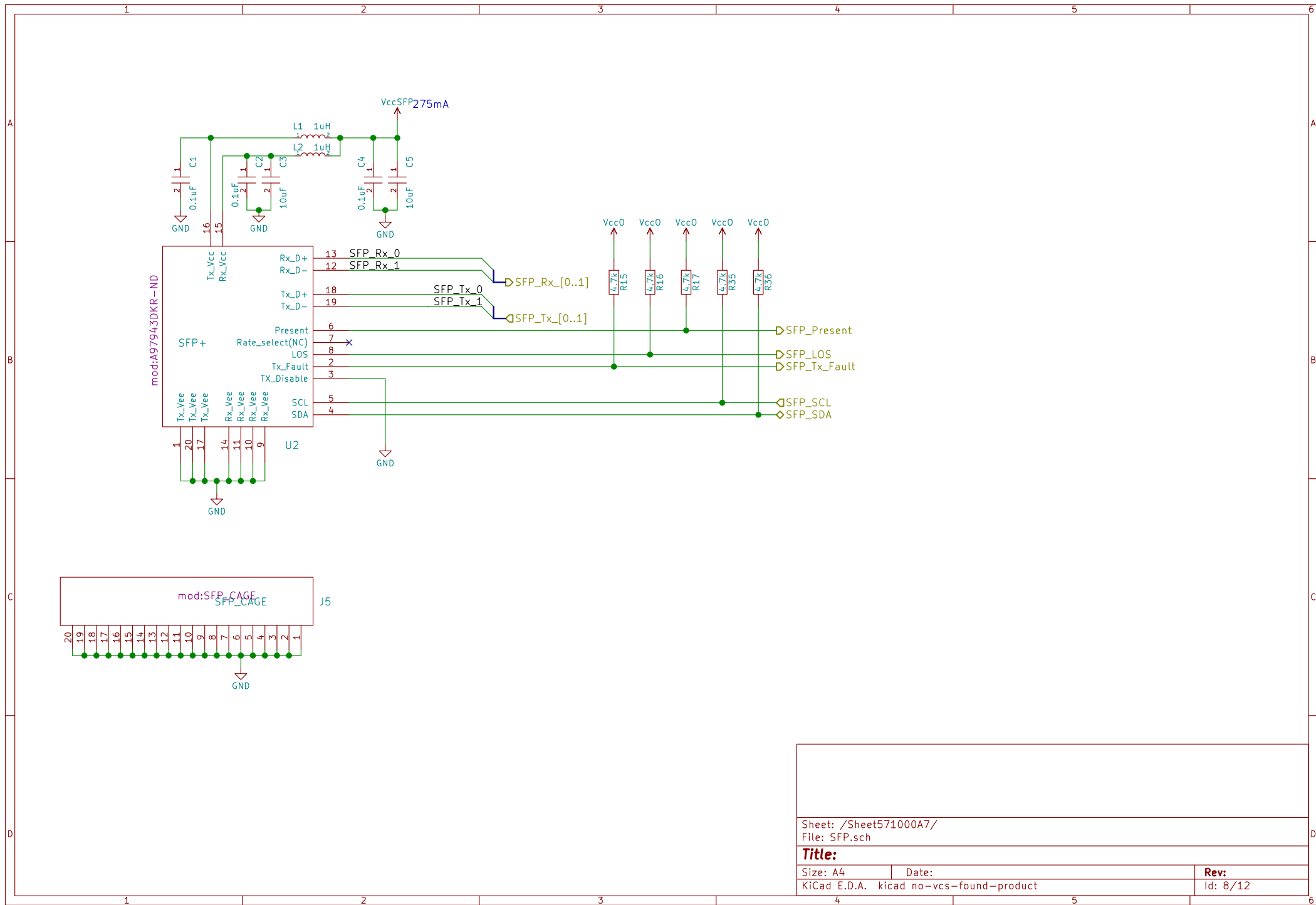
Id: 4/12



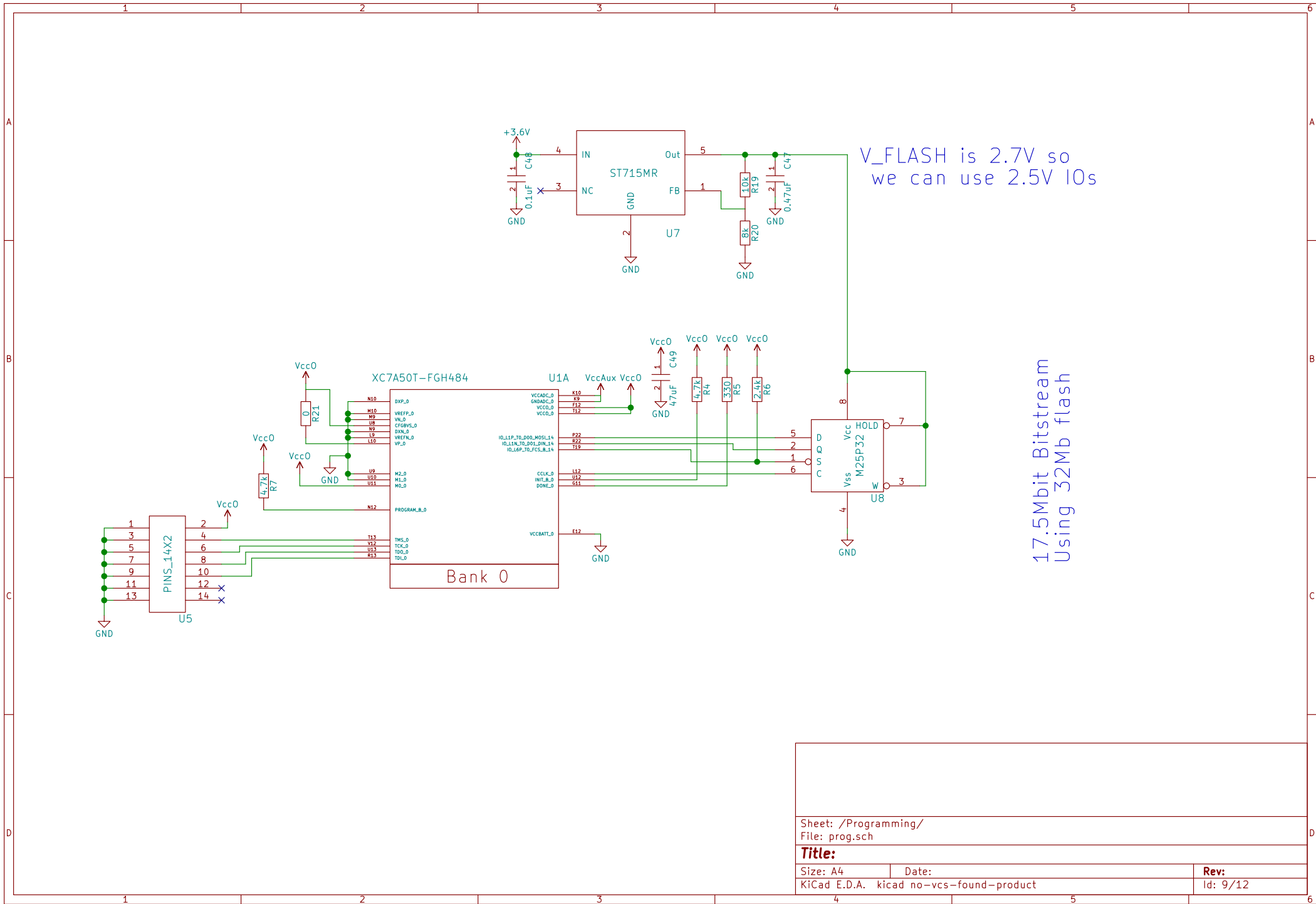
JTAG
CH4_[0..13]
CH5_[0..13]
CH3_[8..13]
mode[3..0]



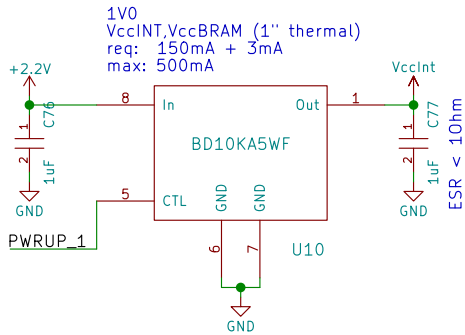




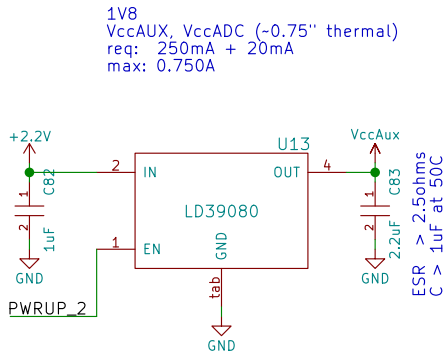
Sheet: /Sheet571000A7/ File: SFP.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad no-vcs-found-product		Id: 8/12



Seq 1

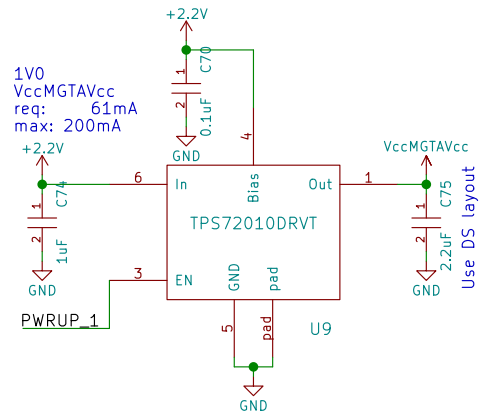
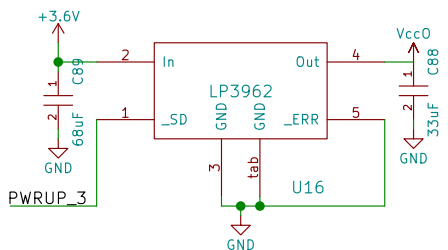


Seq 2

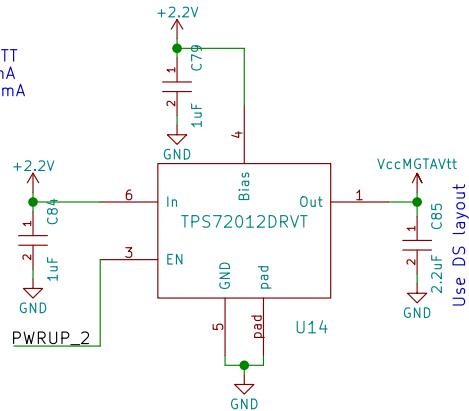


Seq 3

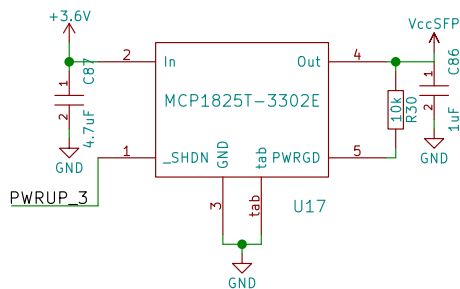
2V5
VccO (1" thermal pad)
req: 650mA
max: 1.5A



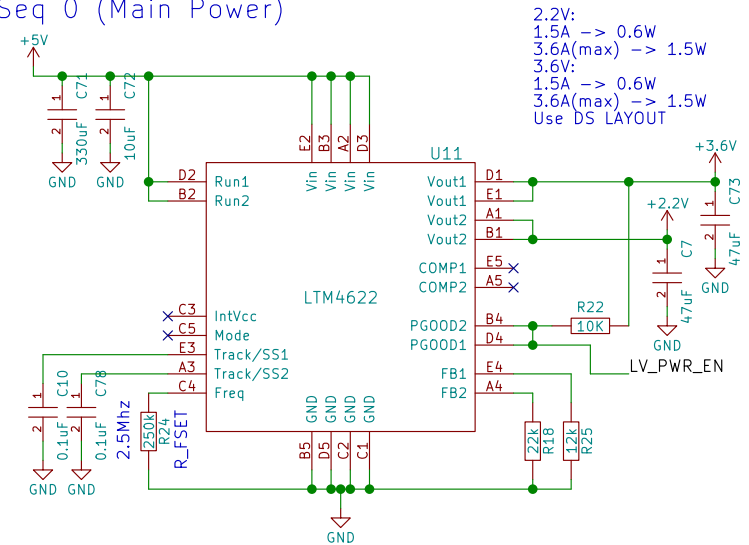
1V2
VccMGTA VTT
req: 50mA
max: 150mA



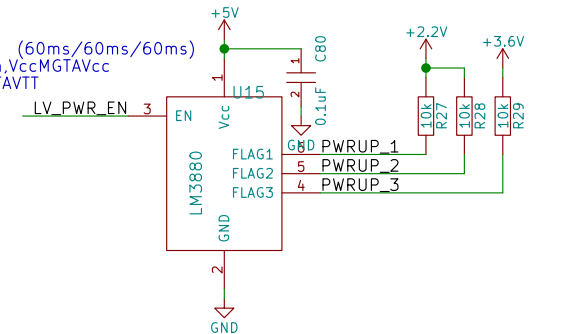
3V3
SFP (0.5" thermal pad)
req: 275mA + 0 to 30mA (FLASH)
max: 500mA



Seq 0 (Main Power)



Control: 2V5 good (60ms/60ms/60ms)
1) VccINT, VccBram, VccMGTA Vcc
2) VccAUX, VccMGTA VTT
3) VccO, VccSFP



Sheet: /Power/
File: power.sch

Title:

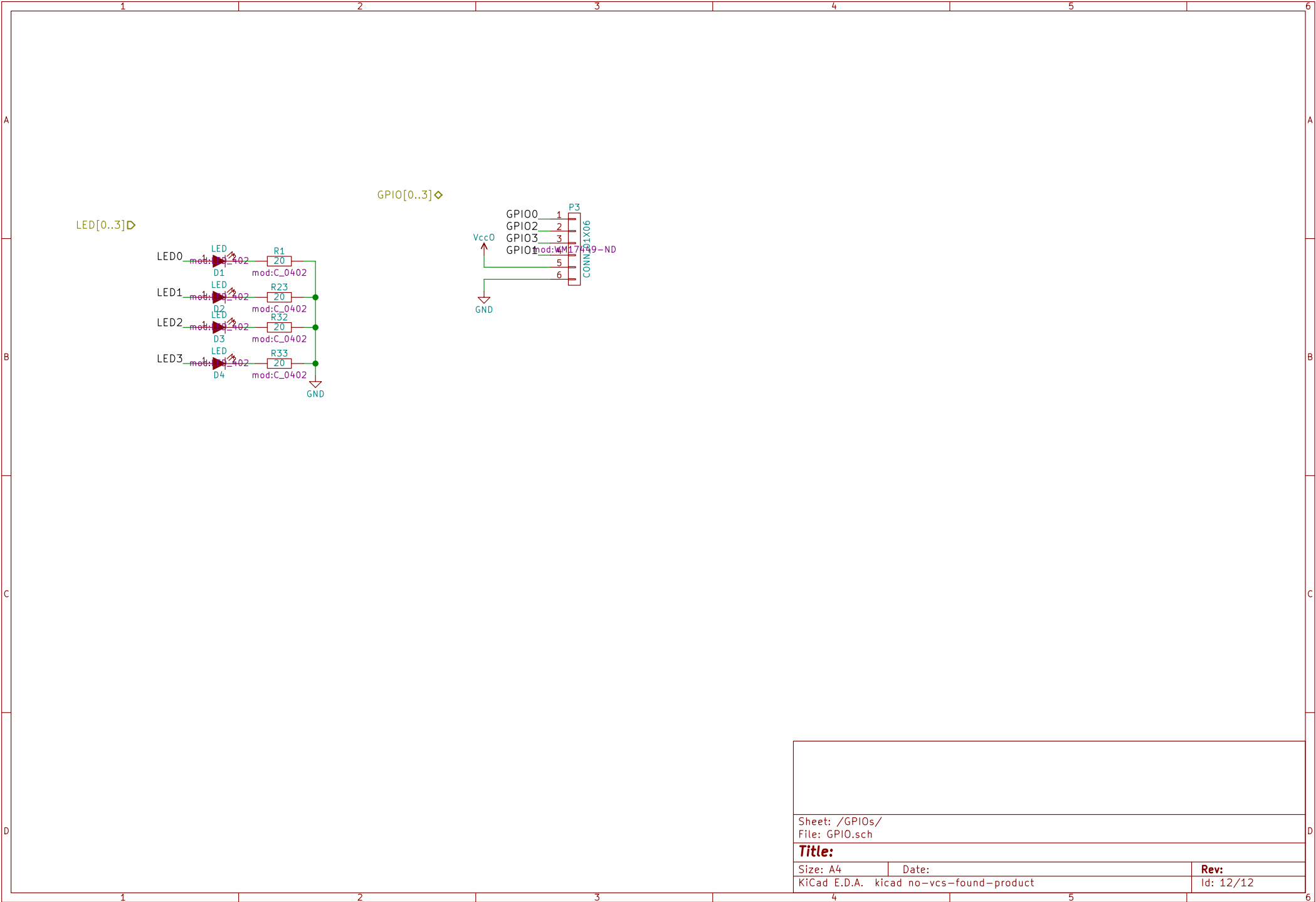
Size: A4

Date:

KiCad E.D.A. kicad no-vcs-found-product

Rev:

Id: 11/12



Sheet: /GPIOs/ File: GPIO.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad no-vcs-found-product		Id: 12/12