SDD Review Document

Summary

Date	06/09/2021
Effort	1 hour
Room/Location	Online
Review Status	Open
Review name	SDD_Button. doc
Method	WT
Release	1.0.0
Responsible	Ariel Antonio Gonzalez Padilla
Project	Door Control module
Reason of Review	First Revision

Comment List

No.	Reference	Comments / Actions	Classification (E)rror/Risk / (R)emark	Responsible person/Planned date for completion	Completion(Name/Date)
1	Abbreviations	More abbreviations could be added to make the document more accessible.	R	Jose Garibay - 14/09/21	
2	Reference	The traceability matrix template is not referenced on the document.	R	Jose Garibay - 14/09/21	
3	SW Conceptual design	No conceptual design is described on point 4	E	Jose Garibay - 14/09/21	
4	SW Internal breakdown	Detail could be added on the internal breakdown, additional information could help first time readers to understand the SWC dynamics better	R	Jose Garibay - 14/09/21	
5	Function – Button_Init	The information initialized on button_init shall be to a certain level explained to implement the idea better	E	Jose Garibay - 14/09/21	
6	Function – Button_Run	The description of the Button_Run function, gives no example to the periodic operations that this interface gives access to.	E	Jose Garibay - 14/09/21	
7	Function – Button_Run	Finishing state condition of the function diagram is not stated, this could create confusion	E	Jose Garibay - 14/09/21	

Check List

No	Description	OK / NOK / NR	Comment	Responsible person / Planned date for completion	Status
1	Does the design comply to the SW architecture? (interfaces, scheduling...)	OK			Closed
2	Are all requirements allocated to Desing elements?	OK			Closed
3	Are all operations described in an adequate detail and with the adequate notation?	NOK	A wider explanation on the complex functions is missign	Jose Garibay - 21/09/21	Open
4	Is the coupling level between SW parts (internal or externals) reduced to the minimum? Is the justification of all global data written in the design document?	OK	They are to the minimum, but the lack of detail indicates possible further divition	Jose Garibay - 21/09/21	Open
5	Is each data owned by one unit? If a data is public (for read and/or for write operations), is its access made using a method provided by the owner? (if a method is provided for read and write operations on the same pubilc data, the data has to be private)	OK			Closed
6	How are the variables initialized? If not initialized, is the reason explained?	OK			Closed
7	Is the mechanism to initialise the functionality (when needed) described? (eg: function calls, data acquisition …)	NOK	The presence of this component is there, but it lacks an explanation	Jose Garibay - 21/09/21	Open
8	In case of global variable (shared or not shared) used in reentrance function (reentrance raised by an ISR), is there a mechanism to avoid data modification during its treatment?	NOK	There are no global variables stated on the design,	Jose Garibay - 21/09/21	Open

			clarification is needed		
9	Are Tasks, ISRs and event notification function kept as short as possible?	NR	This SWC does not have interrupts		Closed
10	Is the state variable only used in one single module? (If the state variable needs to be visible from another module (to be avoided), indicate it in the design and use the mechanism of read copy on that variable).	OK	Information sufficiently separated		Closed
11	Is the event memorization (ex: flag) consumed at the end of each reccurence of a state machine? Otherwise, the risk is to use an obsolete event (ex: event memorization consumption conditionned by a state transition).	NOK	This aspect is not considered in the state machine	Jose Garibay - 21/09/21	Open
12	In case of asynchronous reception of the same event by several objects (ex: state machine, C function called periodicly…), has each object its own memorization mechanism (ex: separate flags).	NOR	This aspect is not considered in the state machine	Jose Garibay - 21/09/21	Open