

## SDD Review Document

### Summary

<b>Date</b>	06/09/2021
<b>Effort</b>	1 hour
<b>Room/Location</b>	Online
<b>Review Status</b>	Open
<b>Review name</b>	SDD_Door. doc
<b>Method</b>	WT
<b>Release</b>	1.0.0
<b>Responsible</b>	Ariel Antonio Gonzalez Padilla
<b>Project</b>	Door Control module
<b>Reason of Review</b>	First Revision

## Comment List

No.	Reference	Comments / Actions	Classification (E)rror/Risk / (R)emark	Responsible person/Planned date for completion	Completion(Name/Date)
1	Abbreviations	More abbreviations could be added to make the document more accessible.	R	Jose Garibay - 14/09/21	
2	Reference	The traceability matrix template is not referenced on the document.	R	Jose Garibay - 14/09/21	
3	SW Conceptual design	No conceptual design is described on point 4	E	Jose Garibay - 14/09/21	
4	SW Internal breakdown	Agglomerating everything into one .c, when is something as complicated as the Door functions, could mean a risk to the code quality	E	Jose Garibay - 14/09/21	
5	Function – Door_Run	The description of the Button_Run function, gives no example to the periodic operations, a text could help complement the state diagram,	R	Jose Garibay - 14/09/21	
6	Function – Door_Set_Req uest	Setter functions work with a parameter, this change should be applied	E	Jose Garibay - 14/09/21	
7	Functions	The lack of contitions indicates flexibility, but this attribute could compromise integrity	R	Jose Garibay - 14/09/21	

## Check List

No	Description	OK / NOK / NR	Comment	Responsible person / Planned date for completion	Status
1	Does the design comply to the SW architecture? (interfaces, scheduling...)	OK			Closed
2	Are all requirements allocated to Desing elements?	OK			Closed
3	Are all operations described in an adequate detail and with the adequate notation?	NOK	A wider explanation on the complex functions is missing	Jose Garibay - 21/09/21	Open
4	Is the coupling level between SW parts (internal or externals) reduced to the minimum? Is the justification of all global data written in the design document?	OK	Additional details should be added	Jose Garibay - 21/09/21	Open
5	Is each data owned by one unit? If a data is public (for read and/or for write operations), is its access made using a method provided by the owner? (if a method is provided for read and write operations on the same pubilc data, the data has to be private)	OK			Closed
6	How are the variables initialized? If not initialized, is the reason explained?	OK			Closed
7	Is the mechanism to initialise the functionality (when needed) described? (eg: function calls, data acquisition ...)	OK	More detail to the states could improve readability	Jose Garibay - 21/09/21	Open
8	In case of global variable (shared or not shared) used in reen trance function (reen trance raised by an ISR), is there a mechanism to avoid data modification during its treatment?	NR	No global variables are used on this SWC		Closed

9	Are Tasks, ISRs and event notification function kept as short as possible?	NR	This SWC does not have interrupts		Closed
10	Is the state variable only used in one single module? (If the state variable needs to be visible from another module (to be avoided), indicate it in the design and use the mechanism of read copy on that variable).	OK	Information sufficiently separated		Closed
11	Is the event memorization (ex: flag) consumed at the end of each recurrence of a state machine? Otherwise, the risk is to use an obsolete event (ex: event memorization consumption conditioned by a state transition).	NOK	This aspect is not considered in the state machine	Jose Garibay - 21/09/21	Open
12	In case of asynchronous reception of the same event by several objects (ex: state machine, C function called periodically...), has each object its own memorization mechanism (ex: separate flags).	NOR	This aspect is not considered in the state machine	Jose Garibay - 21/09/21	Open