

## SHREYAS SRIVASTAVA (07EC1016)

### ACADEMIC QUALIFICATIONS

YEAR	EXAMINATION	BOARD/UNIVERSITY	%/CGPA
2011	B.Tech(E & ECE)	IIT KHARAGPUR	8.15/10
2006	CLASS-12	CBSE	88.4
2004	CLASS-10	CBSE	91

### ACADEMIC DISTINCTIONS

- Among the **top 0.2 percent** of 300,000 students who appeared for the IIT JEE with **All India Rank 444**.
- Held an All India Rank 293(**top 0.05 percent**) in the AIEEE 2007 for which 600,000 students appeared.
- Received *AIEEE merit scholarship* 2007 for outstanding performance in AIEEE 2007

### SUMMER INTERNSHIP AND ACADEMIC PROJECTS

NAME OF ORGANISATION/DURATION	PROJECT OUTLINE/ACHIEVEMENTS
<b>Bharat Dynamics Limited(BDL), Hyderabad (May-July 2010)</b>	<ul style="list-style-type: none"><li>• Digitization &amp; miniaturization of guidance electronics unit of weapon system using mathematical modeling &amp; PIC based microcontroller circuitry</li><li>• Unifying and centralization of control unit through microcontroller based circuitry.</li><li>• Programming the PIC18F4525 microcontroller to meet the stringent timing requirements with respect to command pulse.</li><li>• Successful demonstration of the Unified PCB card on the test setup. Implemented circuit sent for manufacturing</li><li>• Miniaturization of circuitry results in a <i>significant reduction in the missile weight</i> which translates into a major advantage on the battle turf.</li></ul>
<b>University of Applied Sciences, Giessen Germany (May-July 2009)</b>	<ul style="list-style-type: none"><li>• Development of Matlab based data processing software which had the capability of interfacing the complex data obtained from simulation software TCAD to easily processable useful data</li><li>• Built features for 3-D and 2-D MOSFET data along with visual representation of data</li></ul>
<b>Ongoing B.Tech Thesis Project Supervisor: Prof. M Chakrabarty</b>	<ul style="list-style-type: none"><li>• SPT treatment to the Bit Serial Realization of the Sign-LMS based Adaptive Filter</li><li>• Designing novel algorithms for bit-serial canonic SPT numbers to reduce hardware complexity.</li><li>• Software Implementation of bit-serial adaptive filter based on bit-serial canonic SPT adder/multiplier</li></ul>

### POSITION OF RESPONSIBILITY

- 1) Head , Space Technology Student's Society(spAts) IIT Kharagpur( *Founded in 2008*)
  - (a) Conducted and coordinated innovative and unique workshops in Kshitij (annual techno-management fest IIT Kharagpur) like iSputnik(2009) and Water-rocketry(2010)
  - (b) Interviewed over 300 students for the team selection.
  - (c) Helped in securing annual funding from ISRO.
  - (d) Helped in establishing spAts as one of the most promising and vibrant societies in IIT Kharagpur.
- 2) Member of the Decoration team in the Spring fest 2007 edition.

### CO-CURRICULAR & EXTRA-CURRICULAR ACTIVITIES

- 1) Finalist in the GE Edison Challenge -2009 held at JFWTC GE Center. Only 18 teams from over the country selected for the finale.
  - a. Designed a nano-turbine to improve the efficiency of agricultural water pumps.
- 2) Developed "Facial expression Recognition system" using adaptive fuzzy neuro inference system , as course project in Soft-computing.