Time In: 3:44

Time Out : 5:46

## SET A

From Boolean Equation below draw the Boolean Diagram and write the variables that gives the value of 1.

$$AC\overline{B} + CAD\overline{B} = A$$

$$X\overline{YZ} + \overline{X} = X$$

$$\overline{GFH} \; \overline{HGF} = F$$

$$C\overline{AB} + AB = A$$

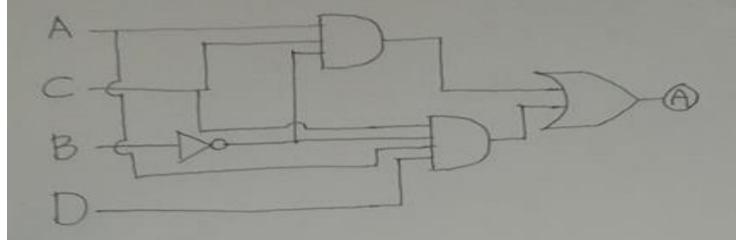
Pick one out of five Boolean expression and implement in the form of a digital logic circuit:

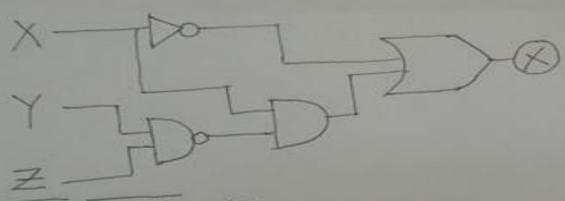


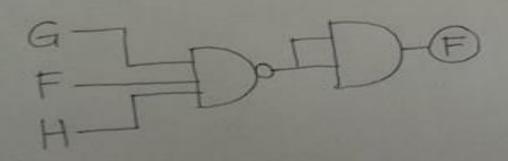
Boolean Diagram

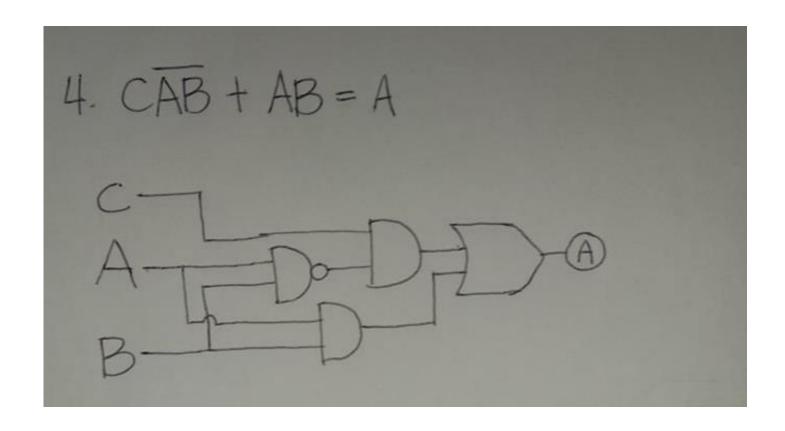
## Ariston L. Catipay BECB-2A

## 1. ACB+ CADB = A

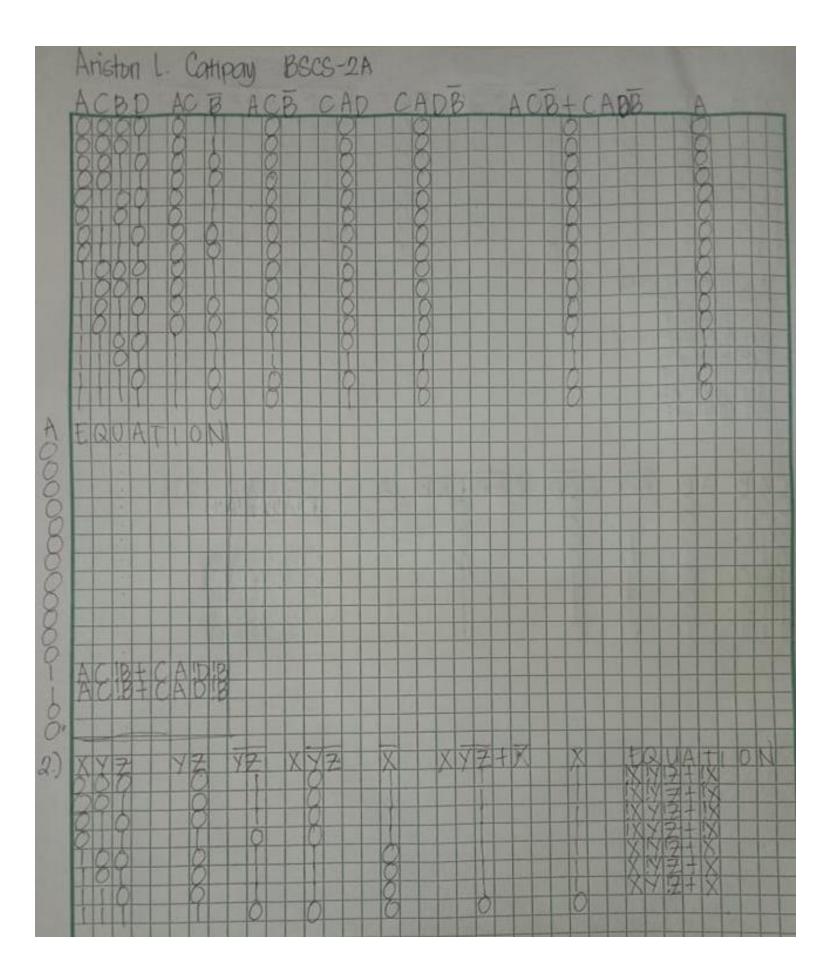








Variables that give the value of 1.



	GPH	GF	GEH	GFH	HG	HGF	HGF	GFH	HGF
	886	18	8		8	8			
	184	1	8			8			
		H	M			Ý	0		
F			ON						
	S T								
	0								
0									
4	CAIS	AB	AG (	A D	CABLA	BA	Eig	UAITI	ION
	910	N N	6	8	1	Ĭ	107	8 + A F	
		18					CI	6 - A 6 6 - A 6 - A 6 - A	9
			0	0			CP	BFA!	