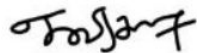


CERTIFICATE

BIT - Centre for PCB Design and Manufacturing

This is to certify that Kavya C has attended Two Days Training on **“Cadence VLSI EDA Tool”** Organized by the Printed Circuit Board (PCB) Lab and Department of Electronics and Communication Engineering, Bannari Amman Institute of Technology, Sathyamangalam in Collaboration with Entuple Technologies Private Limited, Bengaluru. The training was held on 07.04.2025 and 08.04.2025 at the PCB Lab, Bannari Amman Institute of Technology.



Dr. S P. PRAKASH
HoD / ECE - BIT



Mr. G S SANJAY
Field Application Engineer / Entuple
Technologies, Bengaluru



Dr. C. PALANISAMY
Principal BIT