Outputs	States							
	AB2	ABC2	ACB2	ACBC2	APB2	APBC2	B3AC	B3AS
Sw1	1	1	1	1	1	1	1	1
Sw2	1	1	1	1	1	1	1	1
Sw3	1	1	1	1	1	1	1	1
Sw4	1	1	1	1	1	1	1	1
DA[1..0]	10	10	01	01	01	01	01	00
DB[1..0]	10	01	10	01	10	01	01	01

Outputs	States							
	AB2	ABC2	ACB2	ACBC2	APB2	APBC2	B3AC	B3AS
Sw1								
Sw2								
Sw3								
Sw4								
DA[1..0]								
DB[1..0]								

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	AB2	ABC2	ACB2	ACBC2	APB2	APBC2	B3AC	B3AS
Sw1								
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DA[1..0]								
DB[1..0]								