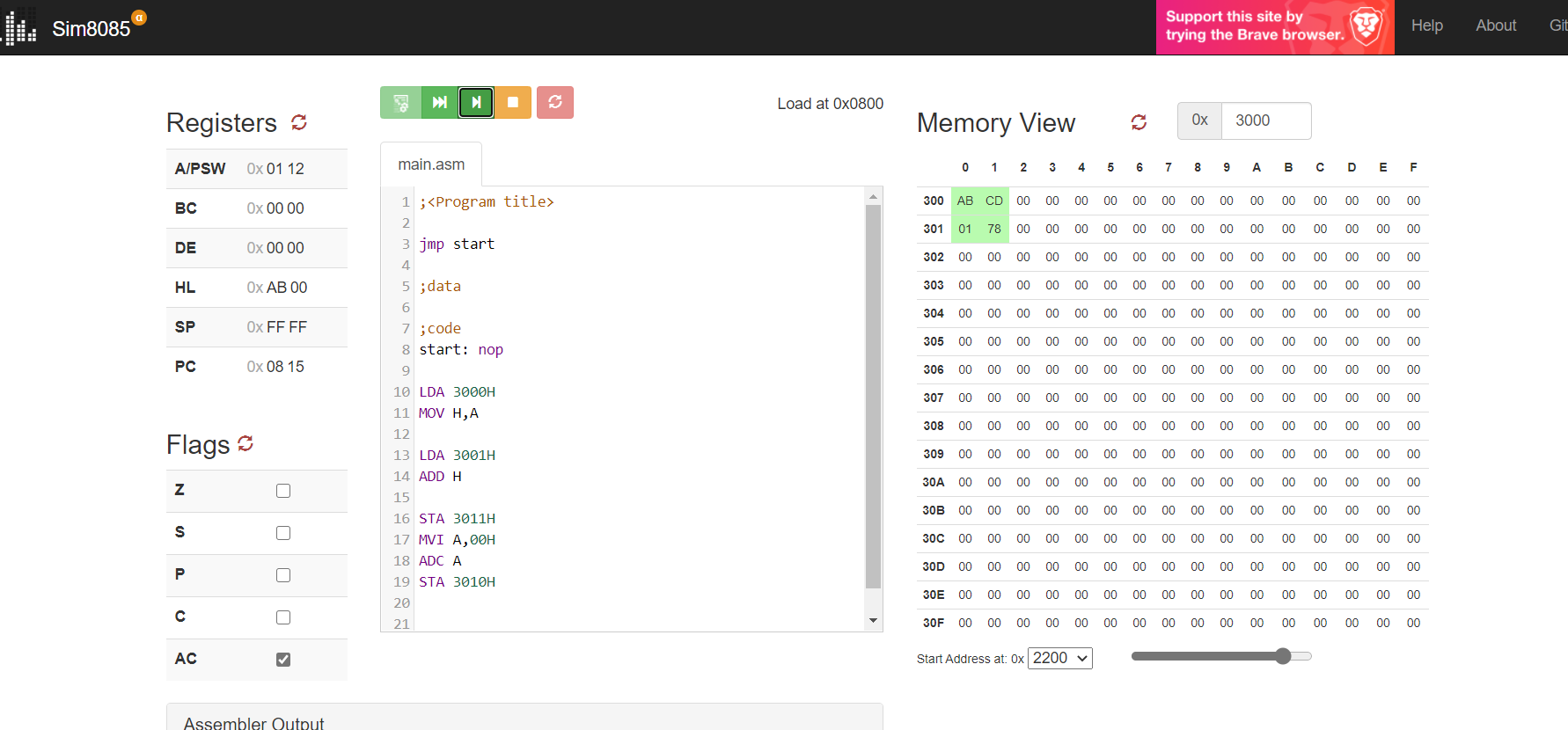
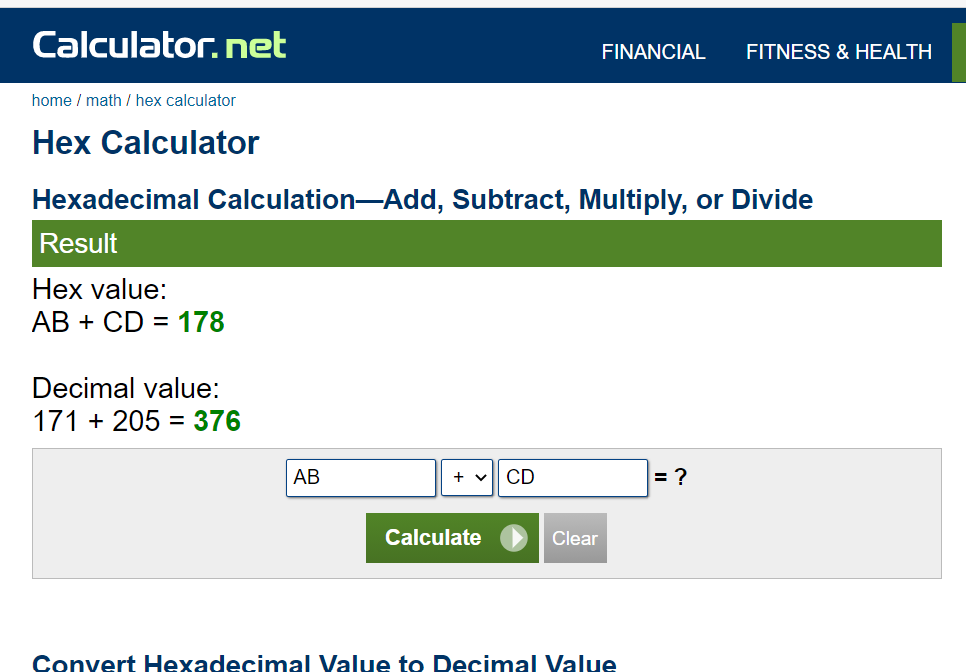
**Microprocessor & Interfacing**

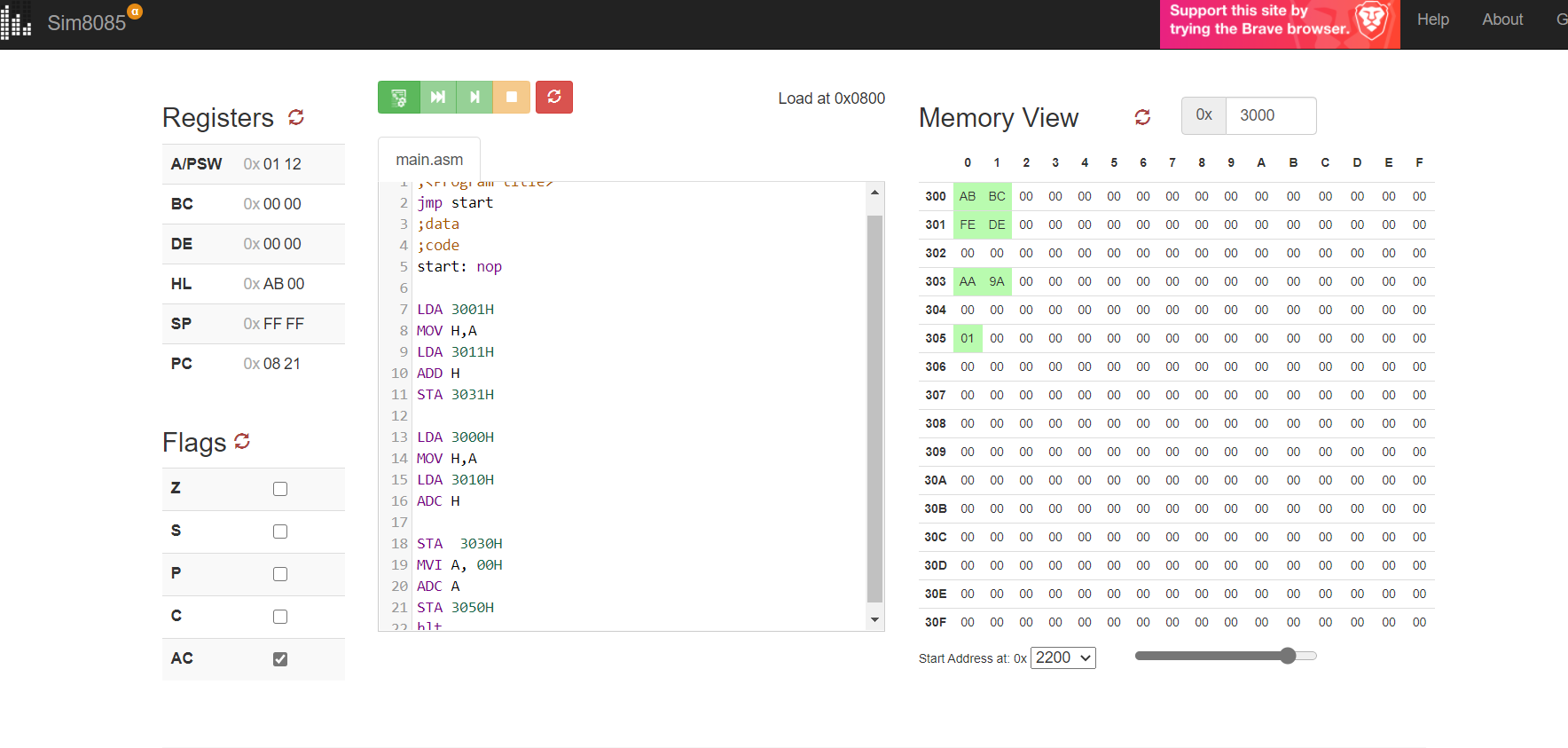
**(Lab session-02)**

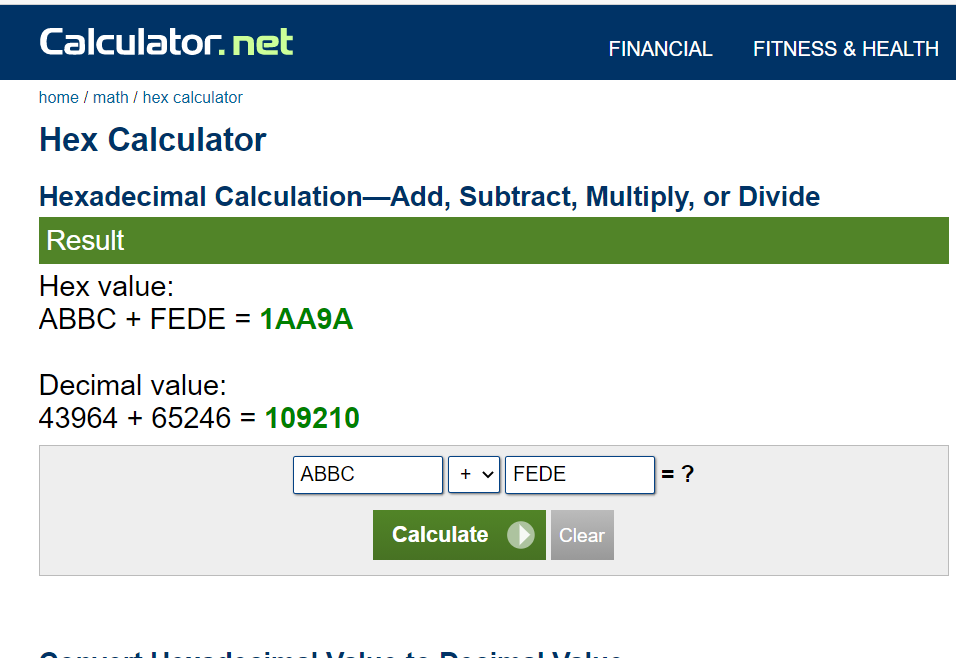
* **Addition of 8 bit**

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* **Addition of 16 bit**

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