TPS65217 Test B'd (Rev.A)

[1] Index

Rev	Date	Designer	Description
A	25.07.09	Ganghyeok Lim	Create design project

Index

Title	TPS65217_Test_Bd.PrjPcb
Rev	A
Doc	Index.SchDoc
Sheet # 1 of 5	Engineer Ganghyeok Lim
Date	2025-01-26

TPS65217 Test B'd (Rev.A)

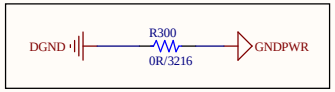
[2] Overview

Title	**TPS65217_Test_Bd.PrjPcb**	Rev *****
Doc	**Overview.SchDoc**	
Sheet # **2** of **5**	Engineer *****	
Date *****		

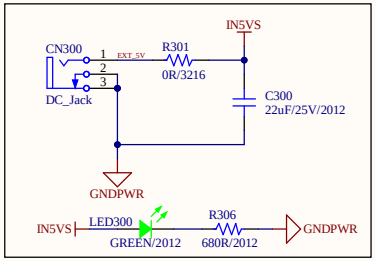
TPS65217 Test B'd (Rev.A)

[3] TPS65217DRSLR

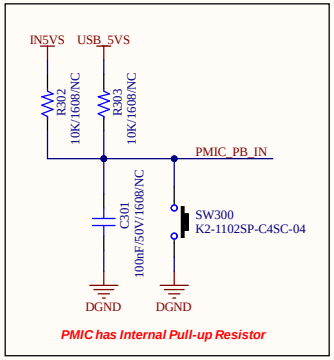
Digital GND



Power Connector (5V)

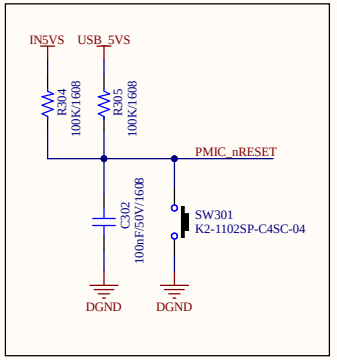


PMIC Power-On Button

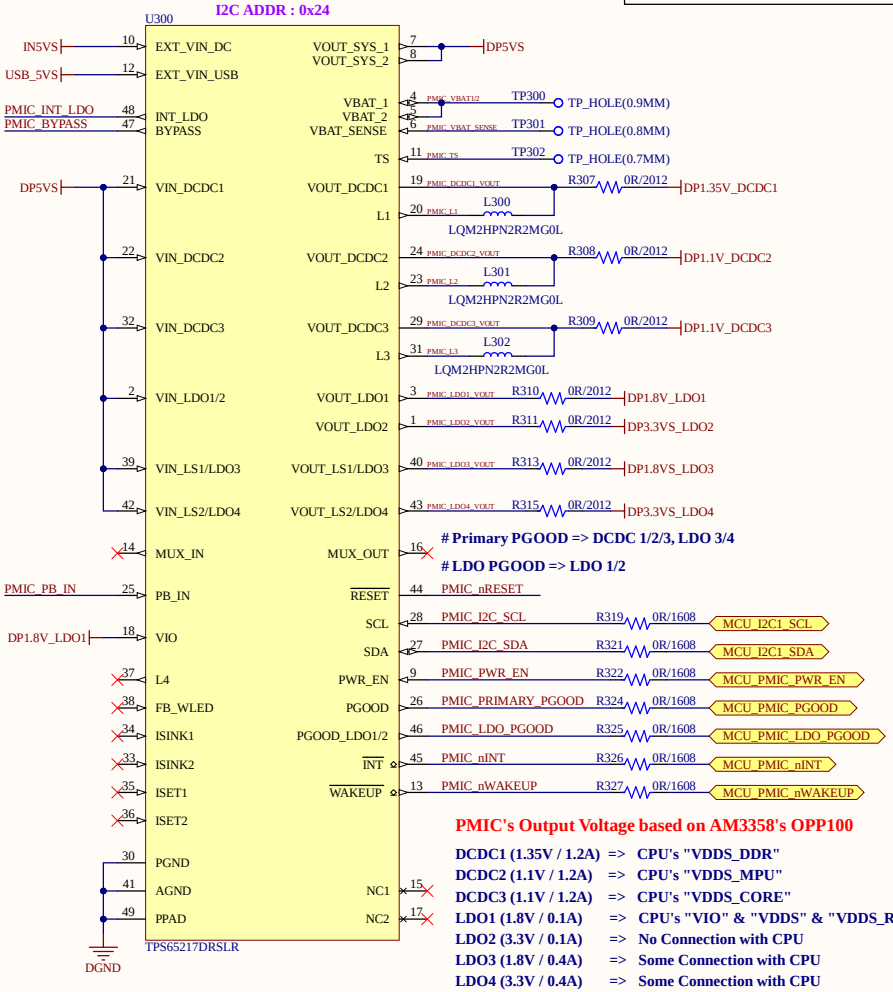


PMIC has Internal Pull-up Resistor

PMIC Reset Button



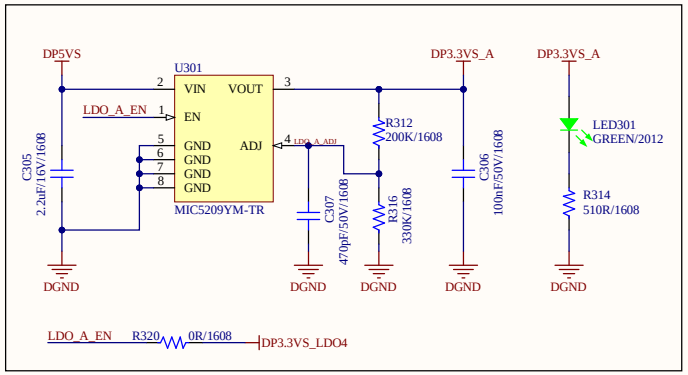
I2C ADDR : 0x24



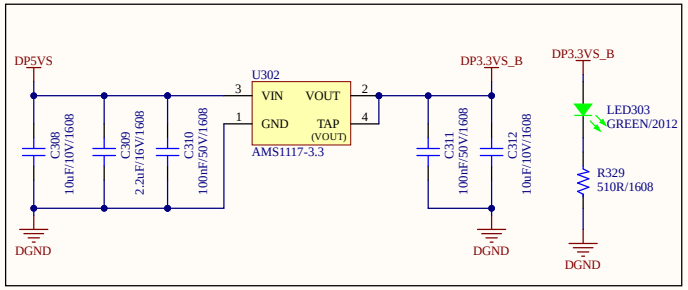
PMIC's Output Voltage based on AM3358's OPP100

DCDC1 (1.35V / 1.2A)	=>	CPU's "VDDS_DDR"
DCDC2 (1.1V / 1.2A)	=>	CPU's "VDDS_MPU"
DCDC3 (1.1V / 1.2A)	=>	CPU's "VDDS_CORE"
LDO1 (1.8V / 0.1A)	=>	CPU's "VIO" & "VDDS" & "VDDS_RTC"
LDO2 (3.3V / 0.1A)	=>	No Connection with CPU
LDO3 (1.8V / 0.4A)	=>	Some Connection with CPU
LDO4 (3.3V / 0.4A)	=>	Some Connection with CPU

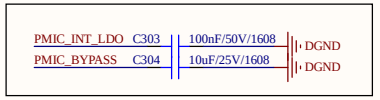
LDO (3.3V / 500mA)



LDO (3.3V / 1A)

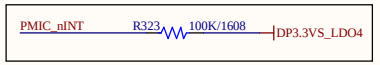


INT LDO / BYPASS Pin

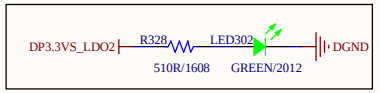


I2C Pull-up Resistor

nINT Pull-up



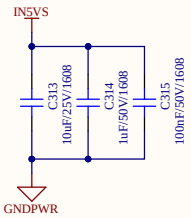
PMIC Power LED



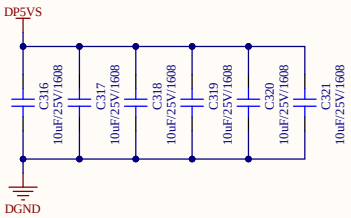
Rail	Output [V]	Sequence (Strobe)
DCDC1	1.35 [V] / 1.2 [A]	1
DCDC2	1.1 [V] / 1.2 [A]	5
DCDC3	1.1 [V] / 1.2 [A]	5
LDO1	1.8 [V] / 0.1 [A]	15
LDO2	3.3 [V] / 0.1 [A]	3
LS1 or LDO3	1.8 [V] / 0.4 [A]	2
LS2 or LDO4	3.3 [V] / 0.4 [A]	4

Decoupling Capacitors

Decap for IN5VS

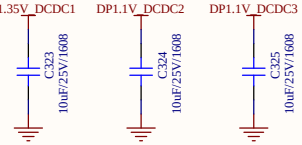


Decap for DP5VS

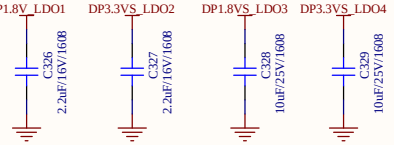


Decap for USB_5VS

Decap for DCDC 1/2/3



Decap for LDO 1~4

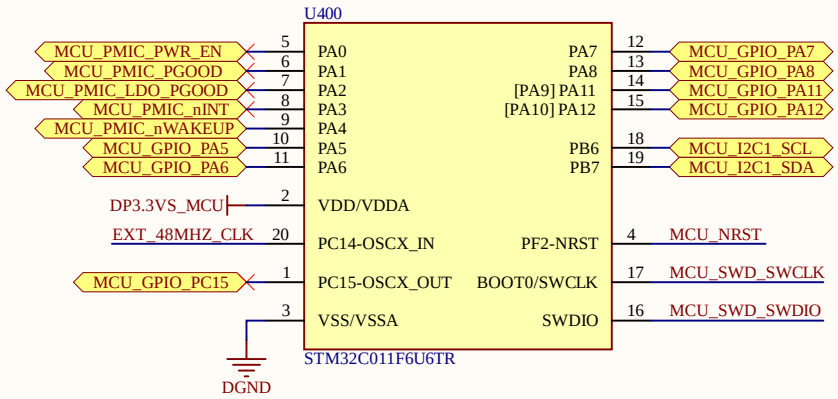


Place Decaps Closely to Each Pin of PMIC

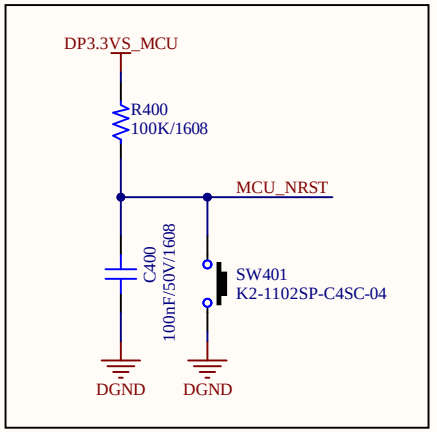
Title	TPS65217_Test_Bd.PrjPcb
Rev	*
Doc	TPS65217DRSLR.SchDoc
Sheet # 3 of 5	Engineer *
Date *	

TPS65217 Test B'd (Rev.A)

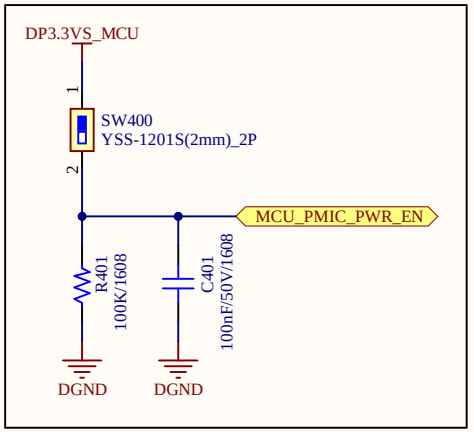
[4] MCU



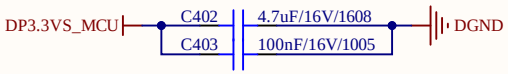
MCU RESET



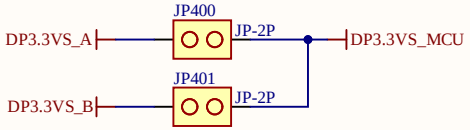
PMIC Power Enable



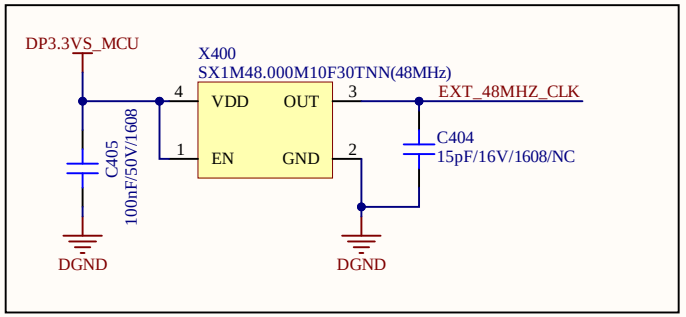
Decoupling Capacitors



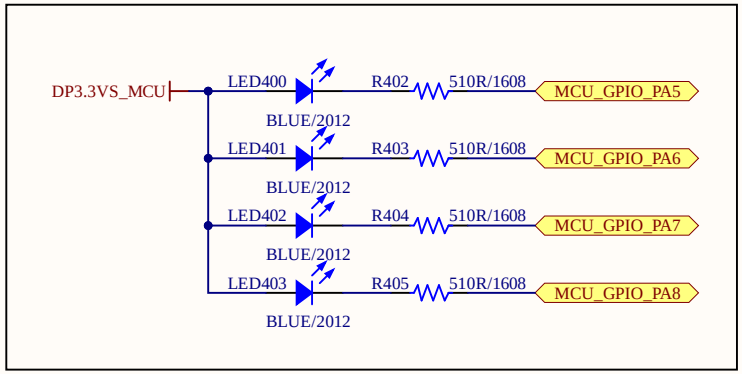
LDO Selection



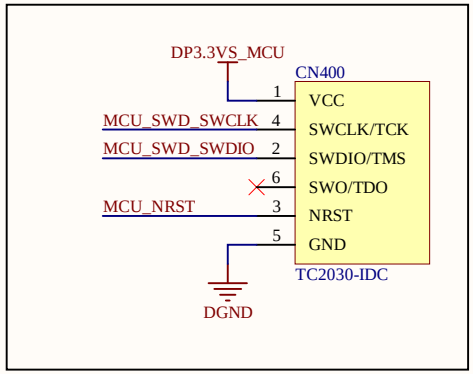
Oscillator (48MHz)



MCU STATUS LED



ST-LINK Connector

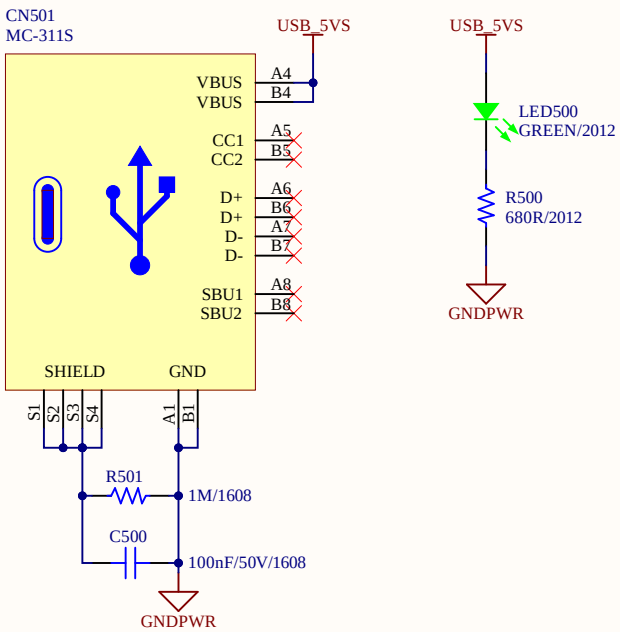


Title	**TPS65217_Test_Bd.PrjPcb**	Rev *
Doc	**MCU.SchDoc**	
Sheet # **4** of **5**	Engineer *	
Date *		

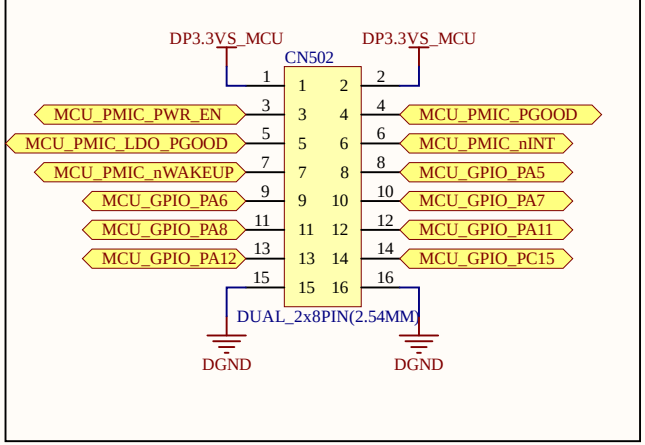
TPS65217 Test B'd (Rev.A)

[5] Connector

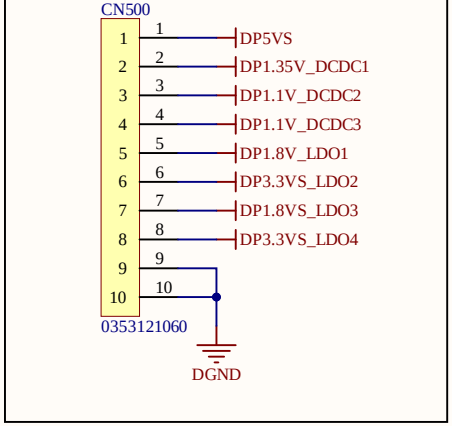
USB Connector (Type-C)



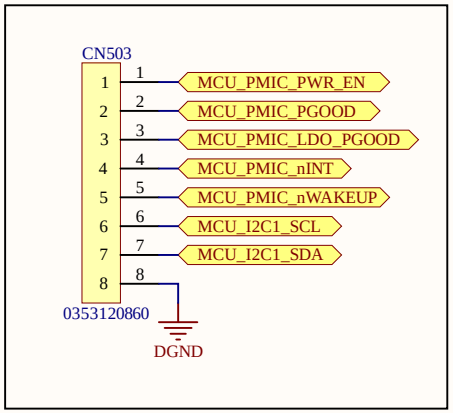
MCU GPIO Connector



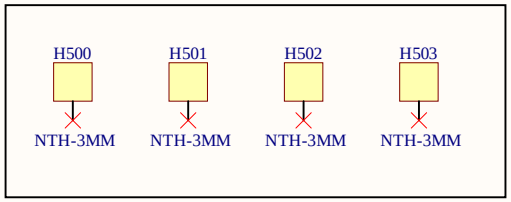
PMIC Output Rails



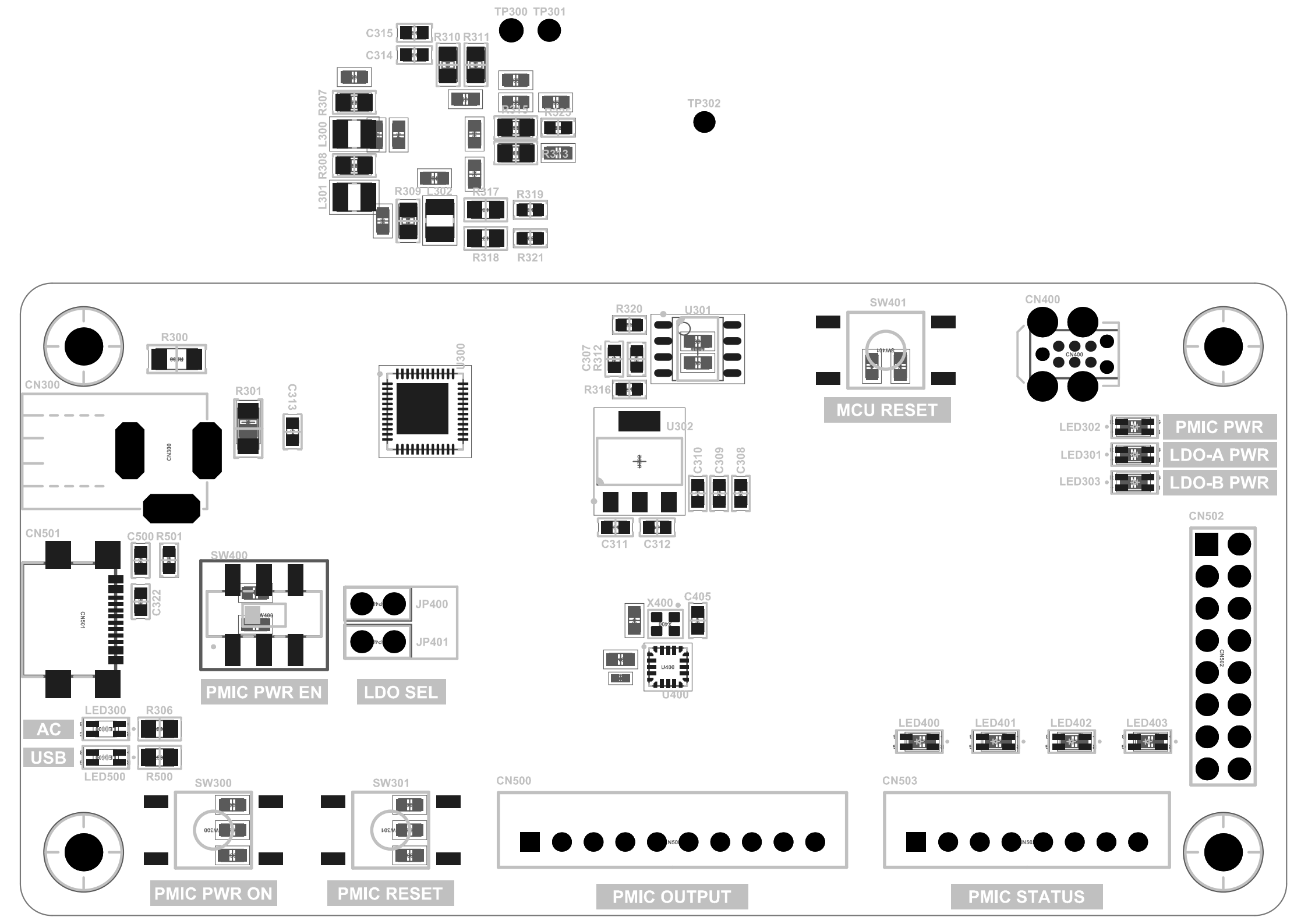
PMIC Status Signals



Mount Holes



Title	**TPS65217_Test_Bd.PrjPcb**	Rev *
Doc	**Connector.SchDoc**	
Sheet # **5** of **5**	Engineer *	
Date *		


TP300
TP301
C315
R310
R311
C314
TP302
R307
L300
R308
L301
R309
L302
R317
R319
R318
R321
R320
U301
C307
R312
R316
SW401
MCU RESET
CN400
LED302
PMIC PWR
LED301
LDO-A PWR
LED303
LDO-B PWR
R300
CN300
R301
C313
U300
U302
C310
C309
C308
C311
C312
CN501
C500
R501
C322
SW400
PMIC PWR EN
JP400
JP401
LDO SEL
X400
C405
U400
CN502
AC
LED300
R306
USB
LED500
R500
LED400
LED401
LED402
LED403
SW300
SW301
CN500
CN503
PMIC PWR ON
PMIC RESET
PMIC OUTPUT
PMIC STATUS