



[1] Index

Rev	Date	Designer	Description
A	25.07.09	Ganghyeok Lim	Create design project

Index

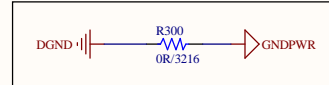
#1 Index	#8 ??
#2 Overview	#9 ??
#3 ??	
#4 ??	
#5 ??	
#6 ??	
#7 ??	

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Doc			 		
Index.SchDoc					
Sheet # 1 of 5	Engineer	Ganghyeok Lim			
Date	2025-01-26				
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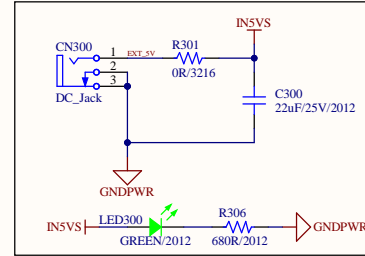
	1	2	3	4																								
A	<div>TPS65217 Test B'd (Rev.A)</div> <div>[2] Overview</div>																											
B																												
C																												
D	<table><tr><td colspan="3">Title</td><td>Rev</td></tr><tr><td colspan="3">TPS65217_Test_Bd.PrjPcb</td><td>*</td></tr><tr><td colspan="2">Doc</td><td colspan="2">Overview.SchDoc</td></tr><tr><td colspan="2">Sheet # 2 of 5</td><td colspan="2">Engineer *</td></tr><tr><td colspan="2">Date</td><td colspan="2">*</td></tr><tr><td colspan="4">This document is copyright of ArkX and shall not be revealed, produced, copied, in whole or in part, nor used for any purpose other than submitted.</td></tr></table>				Title			Rev	TPS65217_Test_Bd.PrjPcb			*	Doc		Overview.SchDoc		Sheet # 2 of 5		Engineer *		Date		*		This document is copyright of ArkX and shall not be revealed, produced, copied, in whole or in part, nor used for any purpose other than submitted.			
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[3] TPS65217DRSLR

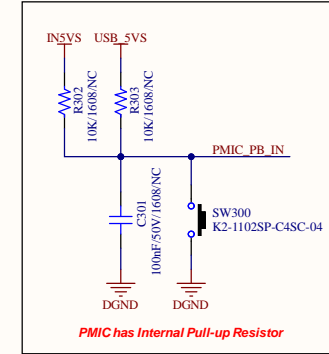
Digital GND



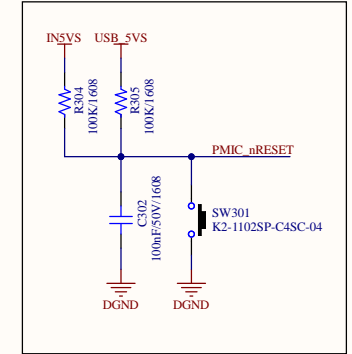
Power Connector (5V)



PMIC Power-On Button



PMIC Reset Button



A

B

C

D

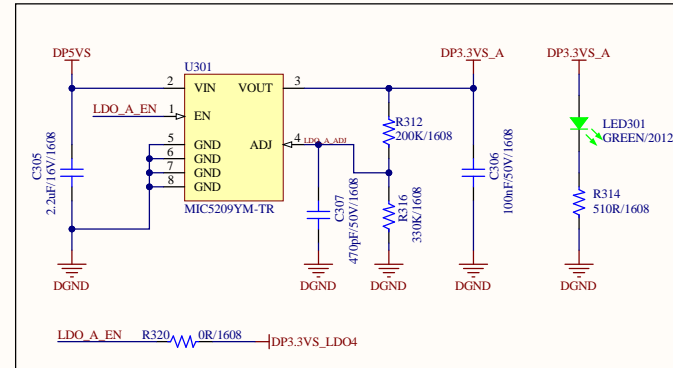
A

B

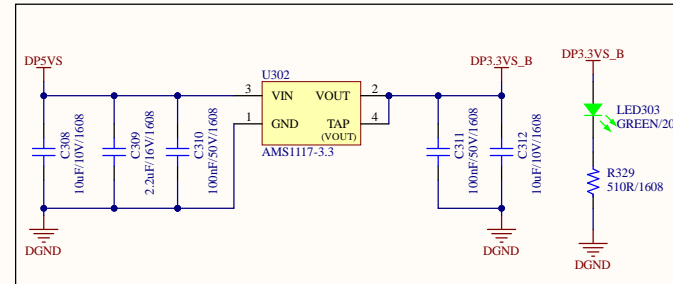
C

D

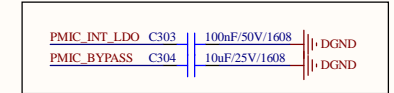
LDO (3.3V / 500mA)



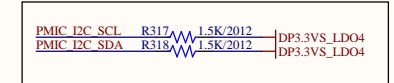
LDO (3.3V / 1A)



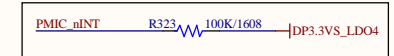
INT LDO / BYPASS Pin



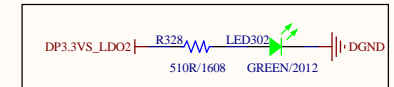
I2C Pull-up Resistor



nINT Pull-up



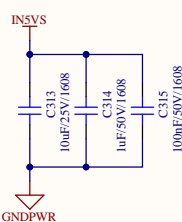
PMIC Power LED



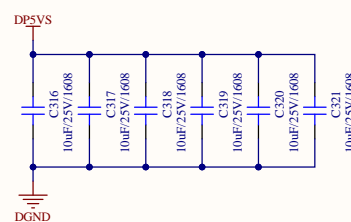
Rail	Output [V]	Sequence (Strobe)
DCDC1	1.35 [V] / 1.2 [A]	1
DCDC2	1.1 [V] / 1.2 [A]	5
DCDC3	1.1 [V] / 1.2 [A]	5
LDO1	1.8 [V] / 0.1 [A]	15
LDO2	3.3 [V] / 0.1 [A]	3
LS1 or LDO3	1.8 [V] / 0.4 [A]	2
LS2 or LDO4	3.3 [V] / 0.4 [A]	4

Decoupling Capacitors

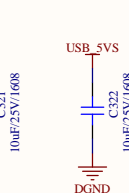
Decap for IN5VS



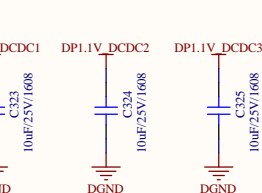
Decap for DP5VS



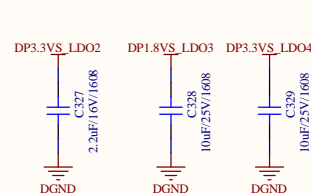
Decap for USB_5VS



Decap for DCDC 1/2/3



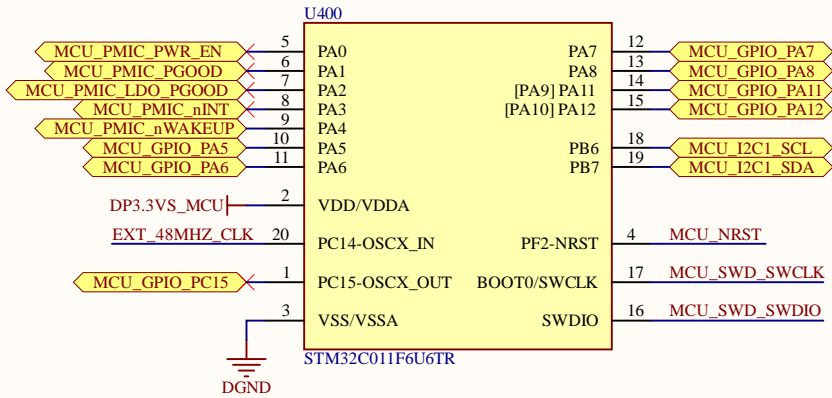
Decap for LDO 1-4



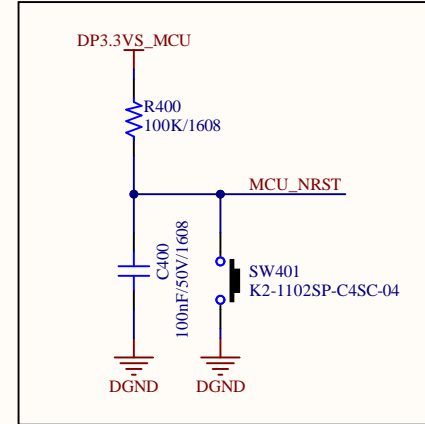
Place Decaps Closely to Each Pin of PMIC

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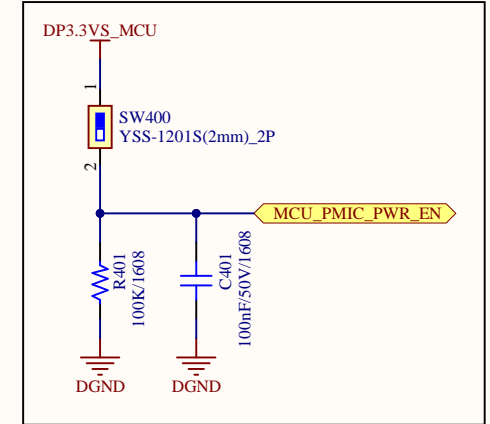
[4] MCU



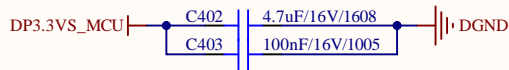
MCU RESET



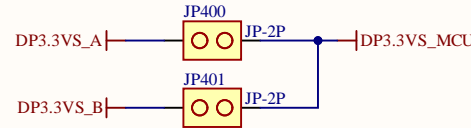
PMIC Power Enable



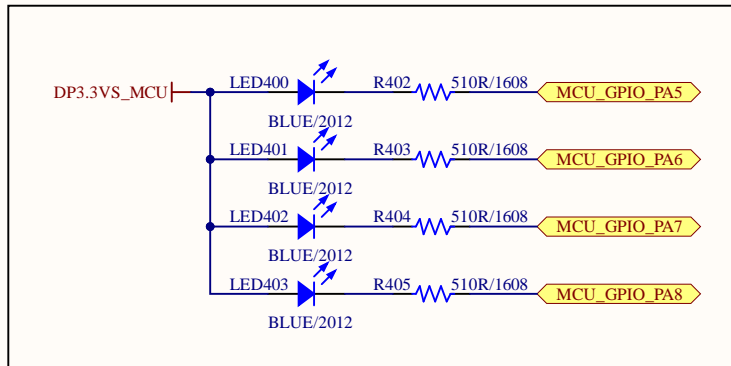
Decoupling Capacitors



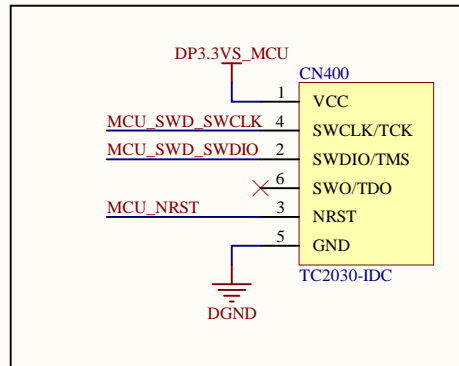
LDO Selection



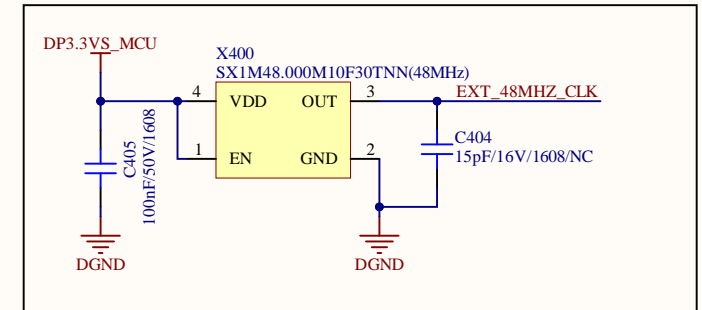
MCU STATUS LED



ST-LINK Connector



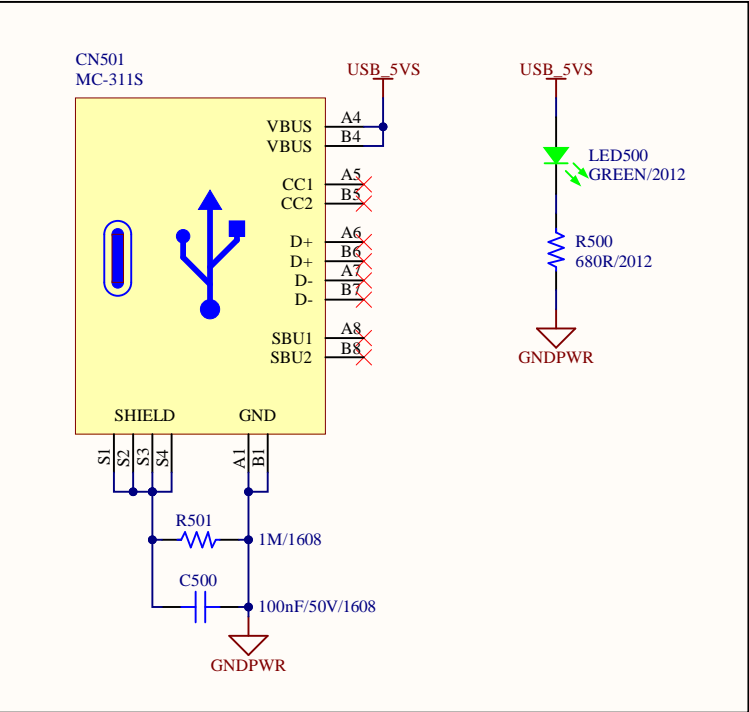
Oscillator (48MHz)



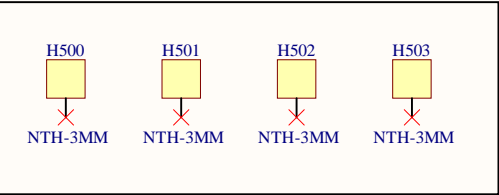
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[5] Connector

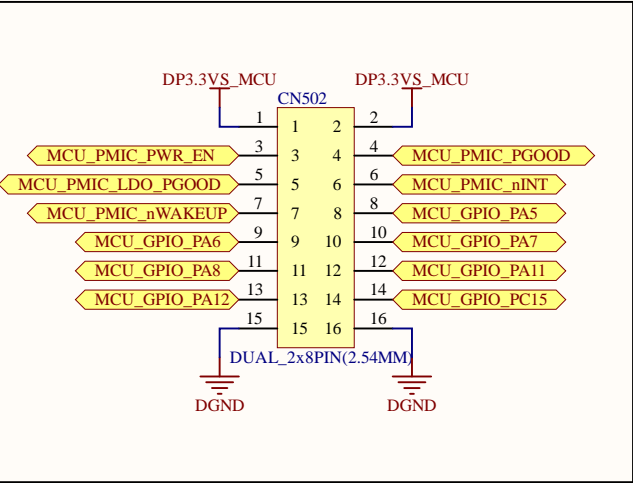
USB Connector (Type-C)



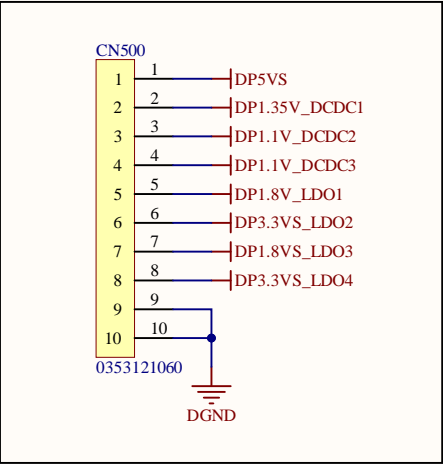
Mount Holes



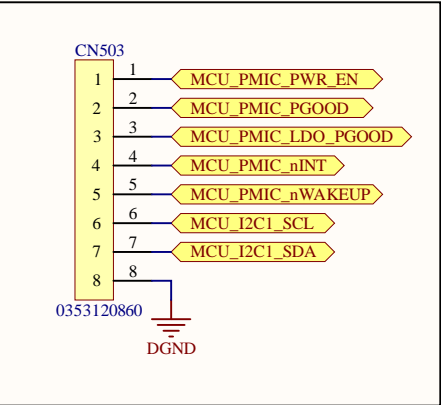
MCU GPIO Connector



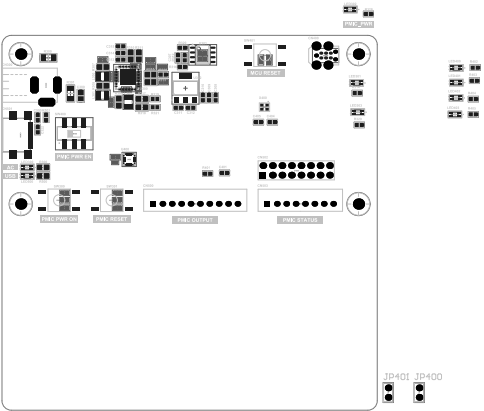
PMIC Output Rails



PMIC Status Signals



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