



ESP32 Strapping Pins
GPIO0, GPIO5 and GPIO15 have pull-up resistors enabled during power up.
GPIO2 and GPIO12 have pull-down resistors enabled during power up.
These pull-up and pull-down resistors can be disabled via software once the device is running.

The ESP32 will enter the serial bootloader when GPIO0 is held low on reset. Otherwise it will run the program in flash.

GPIO0 has an internal pullup resistor, so if it is left unconnected then it will pull high.

Many boards use a button marked "BOOT" that pulls GPIO0 low when pressed.

GPIO2 must also be either left unconnected/floating, or driven Low, in order to enter the serial bootloader.

VSPI CLK=IO18
VSPI MISO=IO19
VSPI MOSI=IO23

HSPI CLK=IO14
HSPI MISO=IO12
HSPI MOSI=IO13

RESET/EN line is held high by resistor on ESP32 module/board

The pins GPIO16 and GPIO17 are available for use only on the boards with the modules ESP32-WROOM and ESP32-SOLO-1. The boards with ESP32-WROVER modules have the pins reserved for internal use.

PIN MAPPING

Pin	Function
1	AVRISP_RESET
2	MODULE_RX1
3	TOUCH_CS
4	SD_CS
5	CANBUS_TX
6	CANBUS_RX
7	DISPLAY_DC
8	RS485_RX
9	RS485_TX
10	RS485_ENABLE
11	i2c_SCK
12	i2c_SDA
13	MODULE_TX1
14	I033
15	I034_INPUT
16	I035_INPUT
17	TOUCH_IRQ
18	TCA6416A_INTB
19	DISPLAY_CS
20	GND
21	RESET
22	i2c_SCK
23	i2c_SDA
24	EXT_IO_A
25	EXT_IO_B
26	EXT_IO_C
27	EXT_IO_D
28	RELAY1
29	RELAY2
30	RELAY3_SSR
31	RELAY4_SSR
32	BLUE_LED
33	RED_LED
34	GREEN_LED
35	DISPLAY_LED
36	SW1OUTPUT
37	SW2OUTPUT
38	GND

Display is only item on bus so no CS needed

