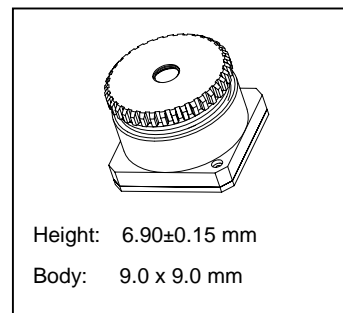


C1M1 Compact Camera Cube

Color SXGA and Fixed Focus

Raw RGB Data Output



1. Features

- CMOS image sensor with SXGA resolution (1280 x 1024) and 3.6µm x 3.6µm pixel size
- Embedded 10 bit ADC
- Raw RGB output data format.
- Fully automatic Xenon and LED-type flash support. *
- Simple two-wire serial programming interface.
- Optical aperture of F/2.8
- Focusing range of 50 cm to infinity
- Compact form factor of 9.0 x 9.0 x 6.90±0.15 mm

* Upon request.

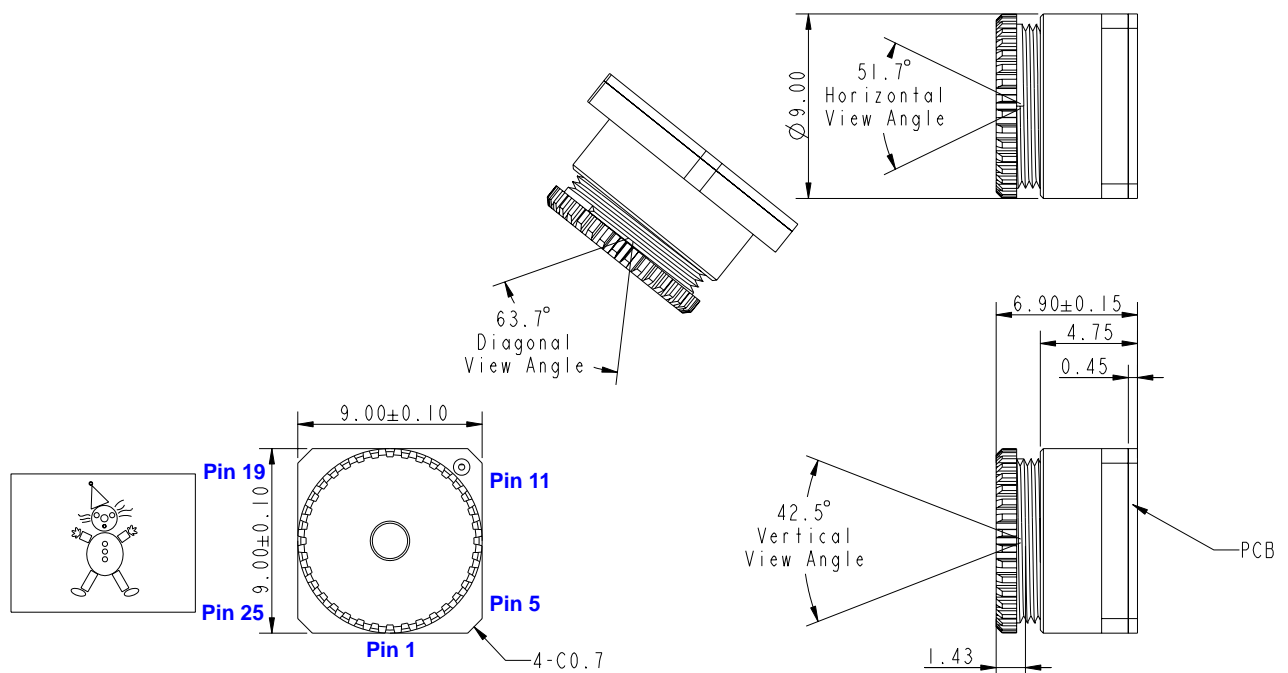
Features of the CMOS Sensor Core	
Optical Format	1/3 inch
Sensor Array Format	1280 x 1024
Pixel Size	3.6 µm x 3.6 µm
Frame Rate	SXGA 15 fps; VGA 60 fps; CIF 150 fps.
Sensor ADC Resolution	10 bit, on-chip
Features of the Image Signal Processor	
Programmable Control I/F	Simple two-wire serial interface
Output Format	Raw Data Output
Major Electric Characteristics	
Power Consumption	129 mW (full resolution mode) 70 mW (preview mode)
Operating Voltage	Digital: 2.5V – 3.1V (2.8V normal) Analog: 2.5V – 3.1V (2.8V normal) I/O: 1.7V – 3.6V

The products and specifications specified herein are for reference purpose only and are subject to change without notice.

2. Optical Lens Sepcification

Specification	Description
Effective Focal Length	4.8 mm
F/No.	2.8
Focusing Range	50 cm to infinity
IR Cut Filter	Cut off wavelength = 650 nm
Lens Construction	3P (Plastic)
View Angle	Horizontal: 51.7° Vertical: 42.5° Diagonal: 63.7°

3. Module Mechanical Dimensions



Note: Macro function upon request.

4. FPC Pin Definition

Pin No.	Symbol	Type	Description	Note
1	PIXCLK	Output	Pixel clock output.	
2	OE#	Input	Output enable.	
3	LINE_VALID	Output	Line valid.	
4	FRAME_VALID	Output	Frame valid.	
5	FLASH	Output	Strobe or turn on flash.	
6	SCLK	Input	Two-wire serial interface clock.	
7	SDATA	I/O	Two-wire serial interface data I/O.	
8	DGND	Ground	Digital ground.	
9	VDD	Power	Digital power. (2.5V – 3.1V)	
10	VDD	Power	Digital power. (2.5V – 3.1V)	
11	VAA	Power	Analog power. (2.5V – 3.1V)	
12	AGND	Ground	Analog ground.	
13	RESET#	Input	Asynchronous reset.	
14	STANDBY	Input	Disables imager.	
15	CLKIN	Input	Master clock into sensor.	
16	D9	Output	Pixel data output 9.	
17	D8	Output	Pixel data output 8.	
18	D7	Output	Pixel data output 7.	
19	D6	Output	Pixel data output 6.	
20	D5	Output	Pixel data output 5.	
21	D4	Output	Pixel data output 4.	
22	D3	Output	Pixel data output 3.	
23	D2	Output	Pixel data output 2.	
24	D1	Output	Pixel data output 1.	
25	D0	Output	Pixel data output 0. (LSB)	
26	NC	NC	NC.	
27	NC	NC	NC.	
28	NC	NC	NC.	