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# 1 INTRODUCTION

In this report, we are designing a Silicon Photo-electron Multiplier (SiPM). This type of circuit is used in

- accelerator-based particle and nuclear physics experiments because of their small size, high photon detection efficiency and insensitivity to magnetic fields[2]. In the course, we examined the use of SiPMs in the CERN where the SiPM allows detection and counting of photons with high resolution and single photon sensitivity[1].
- and also in Positron Emission Tomography (PET) scans. In these scans, the patients ingest glucose tagged with radioactively unstable isotopes that emit positrons in their decay reaction. The glucose will accumulate in regions of the body with high sugar consumption like the brain and the liver, but also tumors. The positrons emitted combine with electrons in a reaction that forms two photons that fly away from one another at a 180° angle. The SiPM are organised in a ring, so the two photons are received by two diametrically opposed detectors[3].

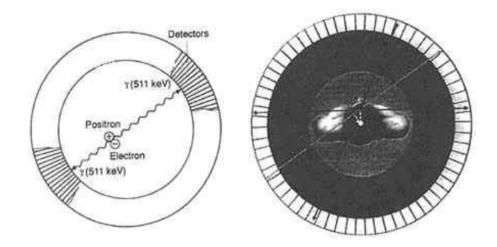


Figure 1: Photon detection in PET scan[3]

For my project, I chose to work with the particle accelerator application context. This means, we are going to model our input stimulus considering single photon detection which is not the case for PET scan applications.



## 2 MODELISATION: PHOTODETECTOR

In our application scenario, we consider our input coming from an array of Single Photon Avalanche Diodes (SPADs). We are going to model the scenario where a single cell emits a pulse current while the remaining cells remain inactive.

We imagine an array of  $100 \times 100$  SPADs functioning independently but connected to a common readout in parallel, with each cell having its own quenching resistor. The parameters considered are

- each cell has dimensions  $50\mu m \times 50\mu m$ , a capacitance of 100 fF and a quenching resistor of  $100 k\Omega$ .
- one photo electron corresponds to a charge of 100fC which is equivalent to a current pulse of 1mA. The duration of the pulse is estimated as 100ps, with a rise time of 1ps and a fall time of 1ps.

When one of the capacitances receives the current pulse, the remaining  $10^4 - 1$  cells can be simplified to a single cell with

$$\begin{cases} C_{eq} &\approx 1nF \\ R_{eq} &\approx 10\Omega \end{cases} \tag{1}$$

To model the noise in the readout bus, we consider a parasitic inductance of  $L_{\text{parasitic}} = 10nH$ .

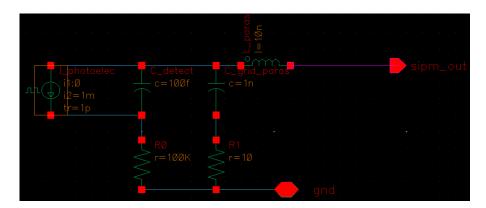


Figure 2: Photodetector block schematic



## 3 THE PREAMPLIFIER

- 3.1 SCHEMATIC
- 3.2 Layout
- 3.3 SIMULATION RESULTS

## 4 THE DISCRIMINATOR

- 4.1 SCHEMATIC
- 4.2 Layout
- 4.3 Simulation Results

#### 5 CONCLUSION

#### **REFERENCES**

- [1] Stefan Gundacker and Arjan Heering. The silicon photomultiplier: fundamentals and applications of a modern solid-state photon detector. *Phys. Med. Biol. 65 17TR01*, 2020.
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[3] National Research Council (US), Institute of Medicine (US) Committee on the Mathematics, and Physics of Emerging Dynamic Biomedical Imaging. **Positron Emission Tomography**. *National Academies Press (US)*, 1996.