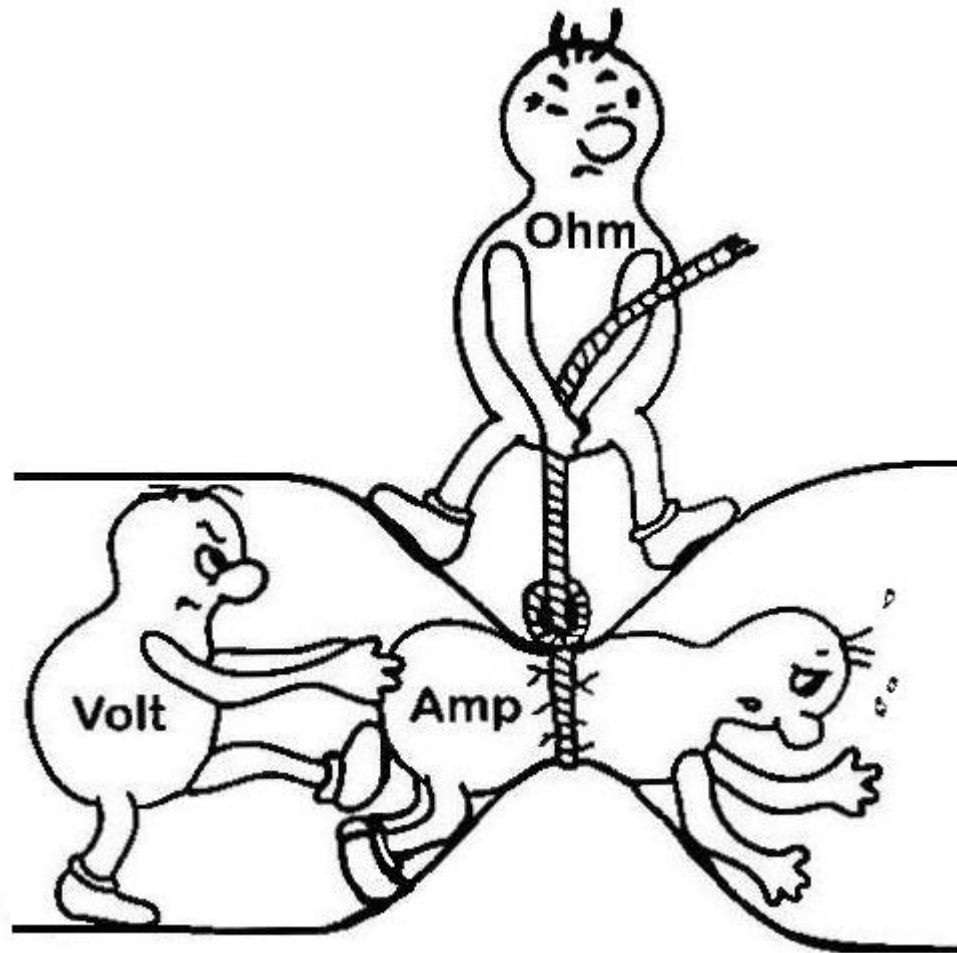


ELECTRONIC'S WORKSHOP

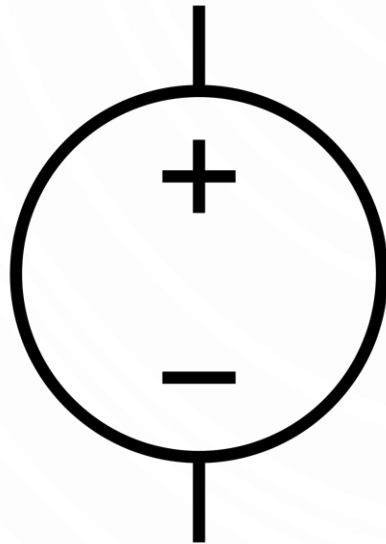
Gonalo Ribeiro
Ricardo Amendoeira
Sara Vieira
Bruno Gonalves



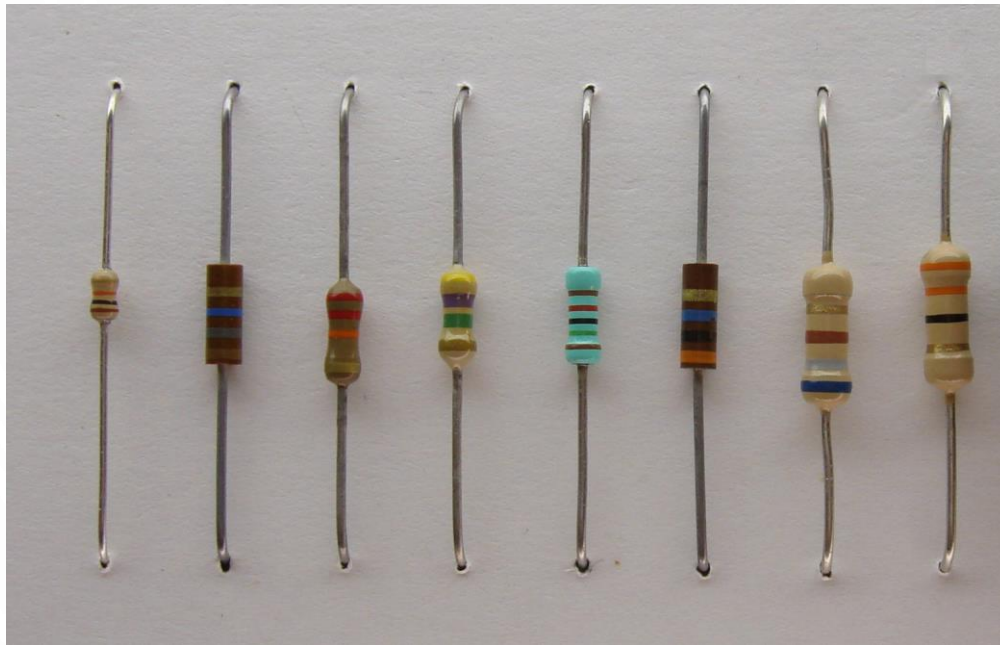
Resistor, Voltage and Current



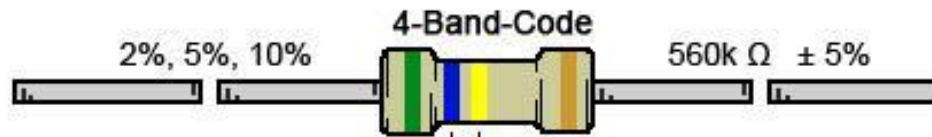
VOLTAGE SOURCE



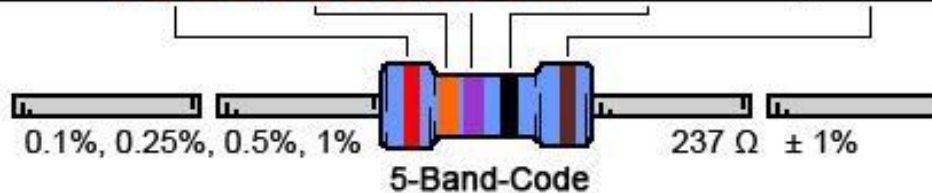
RESISTORS - OHM



COLOUR CODE



COLOR	1 ST BAND	2 ND BAND	3 RD BAND	MULTIPLIER	TOLERANCE
Black	0	0	0	1 Ω	
Brown	1	1	1	10 Ω	\pm 1% (F)
Red	2	2	2	100 Ω	\pm 2% (G)
Orange	3	3	3	1K Ω	
Yellow	4	4	4	10K Ω	
Green	5	5	5	100K Ω	\pm 0.5% (D)
Blue	6	6	6	1M Ω	\pm 0.25% (C)
Violet	7	7	7	10M Ω	\pm 0.10% (B)
Grey	8	8	8		\pm 0.05%
White	9	9	9		
Gold				0.1 Ω	\pm 5% (J)
Silver				0.01 Ω	\pm 10% (K)



OHM'S LAW



$$R = \frac{U}{I}$$

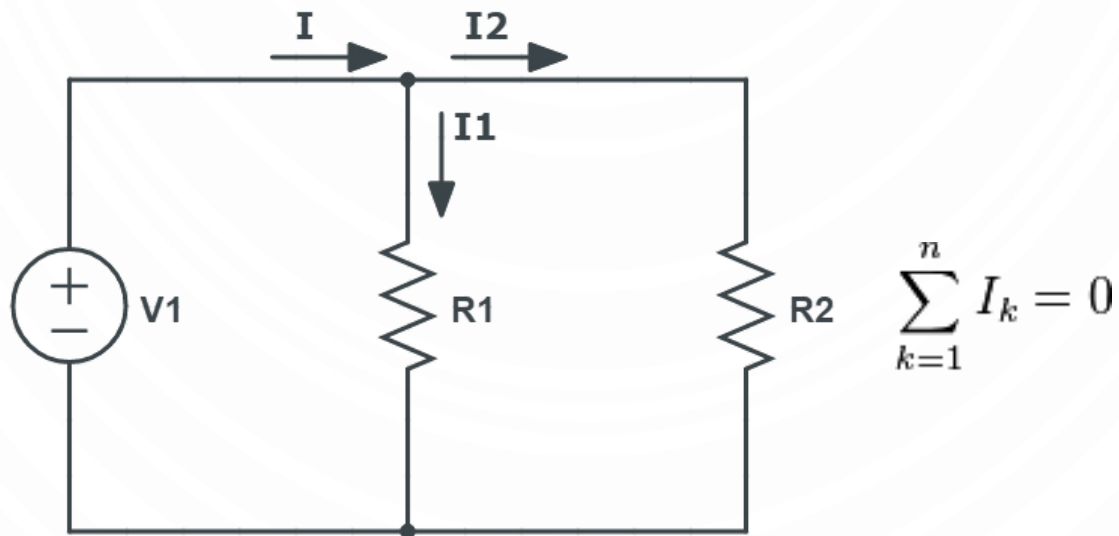
R – ohm (Ω)

U – volt (V)

I – ampere (A)

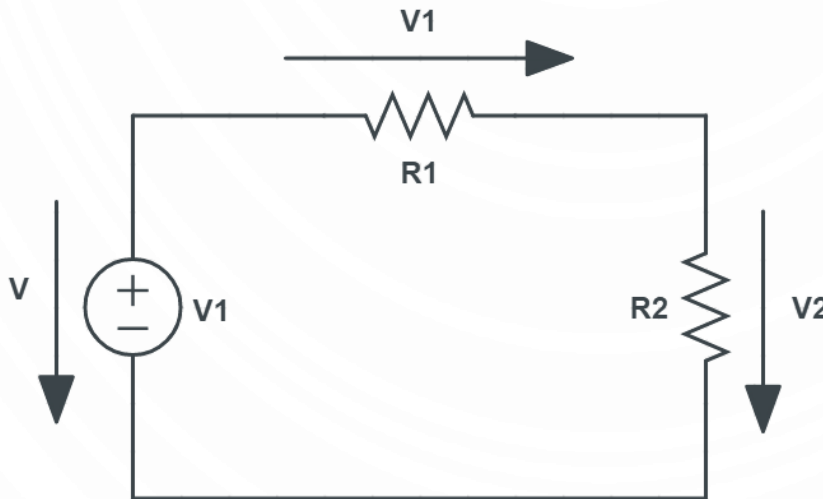
KIRCHOFF'S CURRENT LAW

For each node, the sum of the currents that go in that node is equal to the sum of the currents that leave that same node.



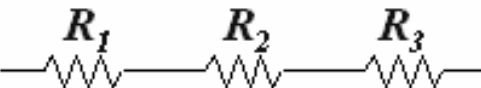

KIRCHOFF'S VOLTAGE LAW

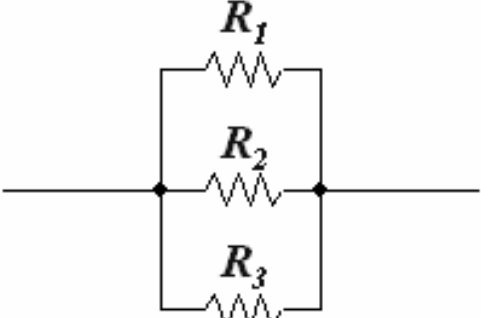

The sum of the voltages around a mesh is zero.



$$\sum_{k=1}^n V_k = 0$$

SERIE AND PARALLEL

Series:  $=$  $R_{eq} = R_1 + R_2 + R_3$

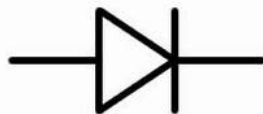
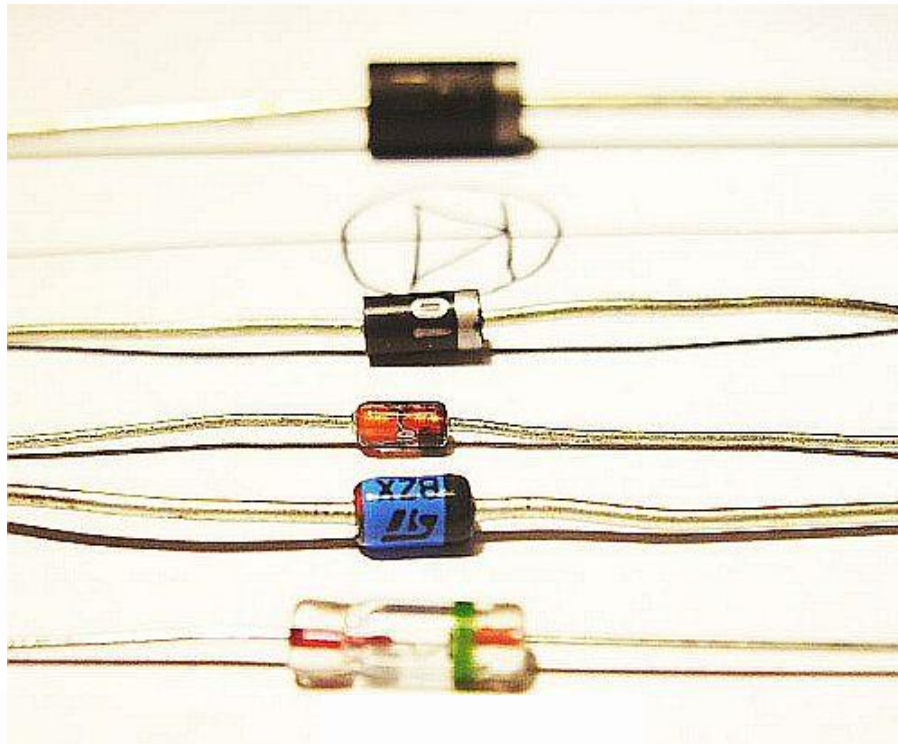
Parallel:  $=$  $R_{eq} = (1/R_1 + 1/R_2 + 1/R_3)^{-1}$

SHORT CIRCUIT

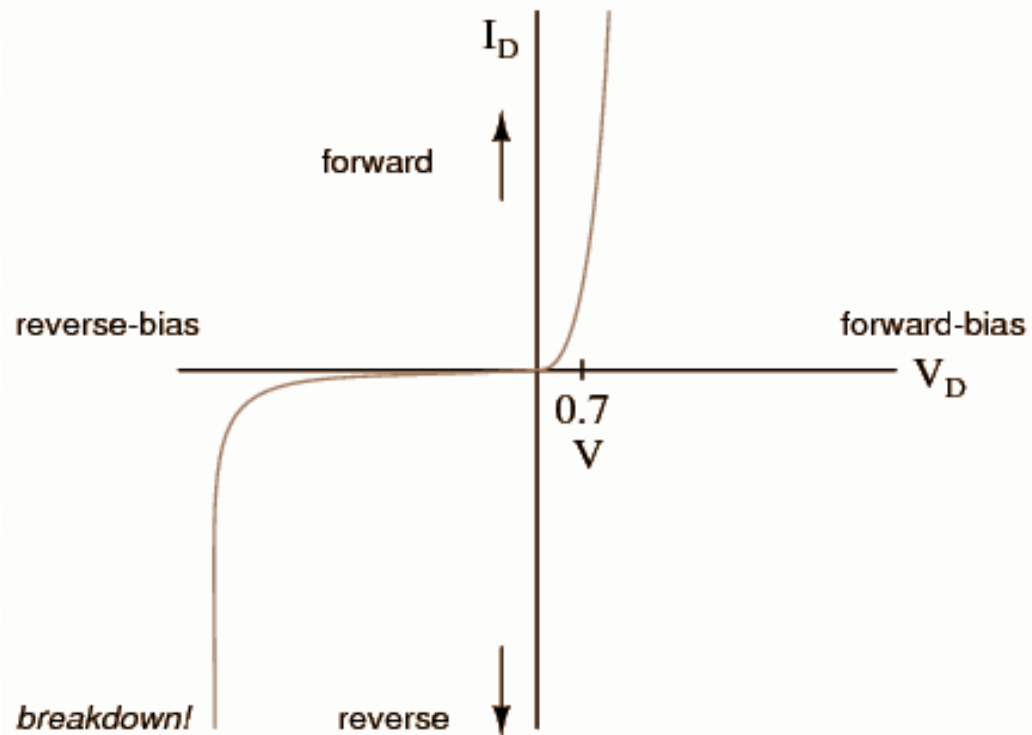
Resistance is futile



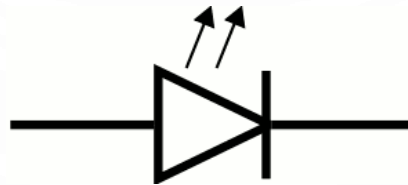
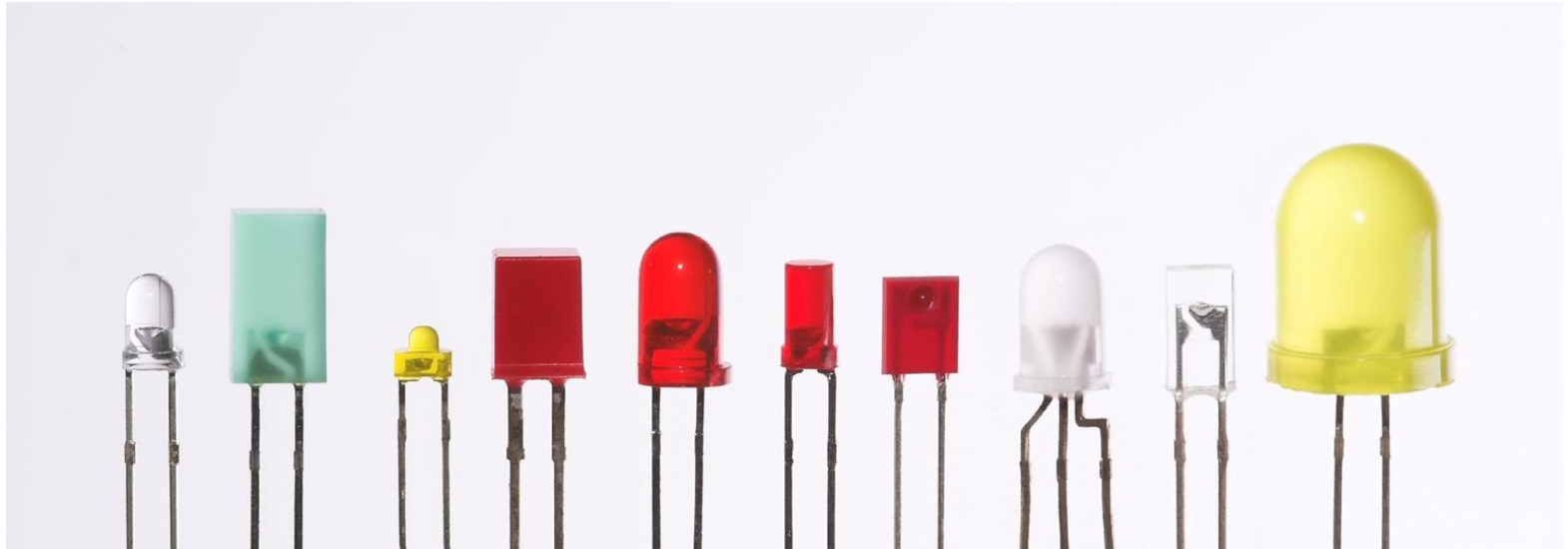
DIODES



DIODE'S CHARACTERISTICS



LED – LIGHT EMITTING DIODE



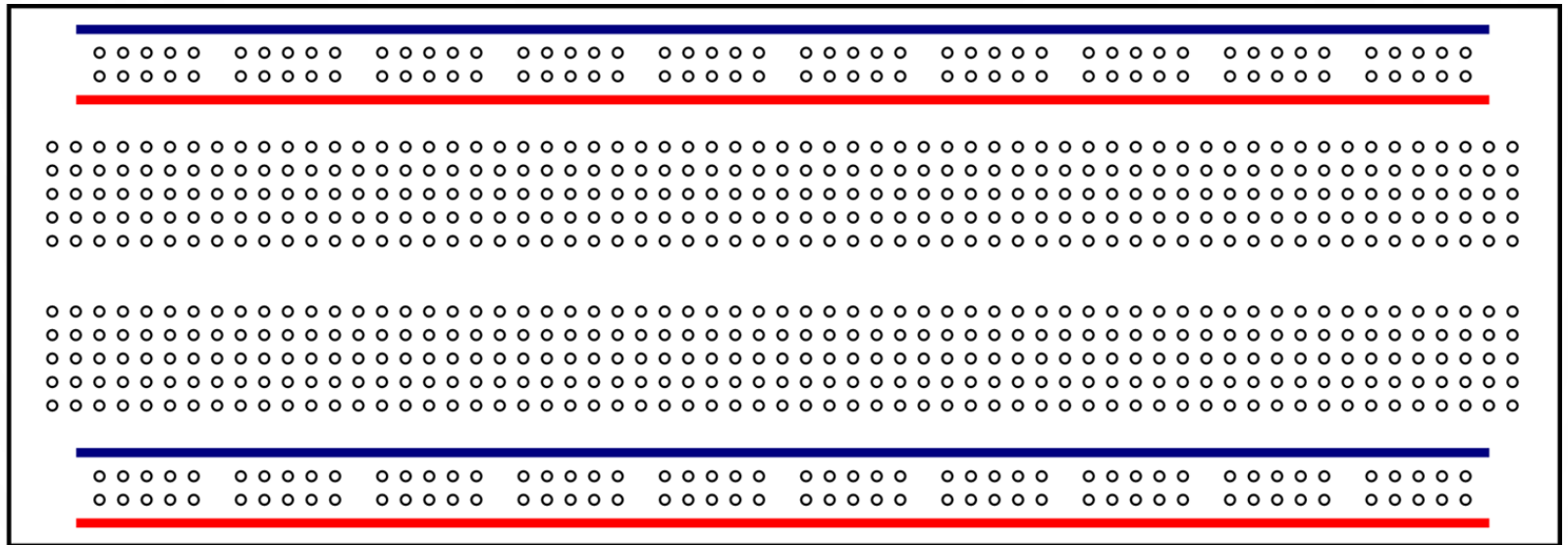
LED – THRESHOLD VOLTAGE

Colour	Approx. Forward Voltage Vf (V)
Red	1.7
HE Red*	2.0
Bright Red	2.3
Orange	2.0
Yellow	2.1
Green	2.2
Blue	3.2
White	3.2

*HE - High Efficiency

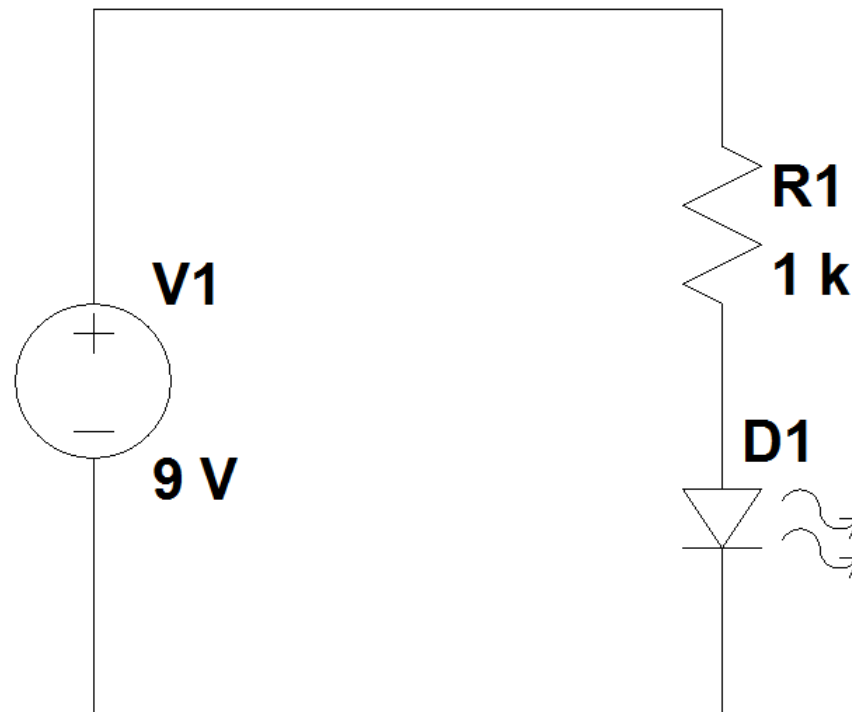
103SV

BREADBOARDS

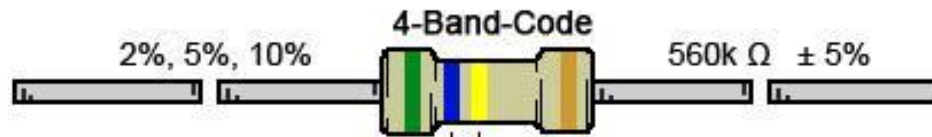


— $V+$
— $V-$

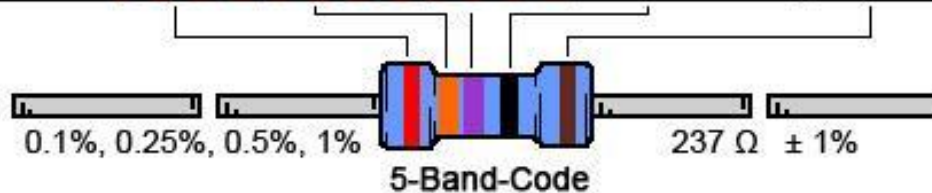
EXPERIENCE 1 – LED AND RESISTOR



COLOUR CODE



COLOR	1 ST BAND	2 ND BAND	3 RD BAND	MULTIPLIER	TOLERANCE
Black	0	0	0	1 Ω	
Brown	1	1	1	10 Ω	\pm 1% (F)
Red	2	2	2	100 Ω	\pm 2% (G)
Orange	3	3	3	1K Ω	
Yellow	4	4	4	10K Ω	
Green	5	5	5	100K Ω	\pm 0.5% (D)
Blue	6	6	6	1M Ω	\pm 0.25% (C)
Violet	7	7	7	10M Ω	\pm 0.10% (B)
Grey	8	8	8		\pm 0.05%
White	9	9	9		
Gold				0.1 Ω	\pm 5% (J)
Silver				0.01 Ω	\pm 10% (K)



HOW TO DIMENSION A RESISTOR TO A LED

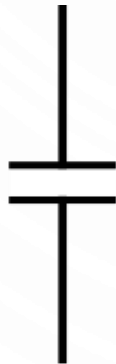
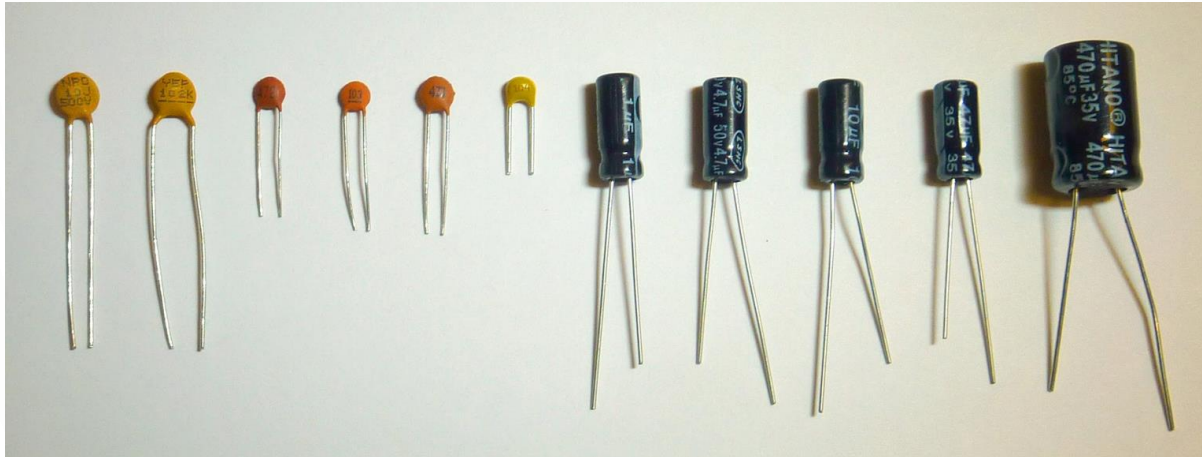
We want to connect a LED, using a 9 V battery and a resistor. What's the appropriate resistor value to the following LEDs, in order to turn up the LED with maximum intensity?

- Red LED, $V_f = 1,7 \text{ V}$, $I_{\max} = 10 \text{ mA}$
- Yellow LED, $V_f = 2,1 \text{ V}$, $I_{\max} = 20 \text{ mA}$
- White LED, $V_f = 3,2 \text{ V}$, $I_{\max} = 20 \text{ mA}$

MULTIMETERS



CAPACITORS



Fixed Capacitor

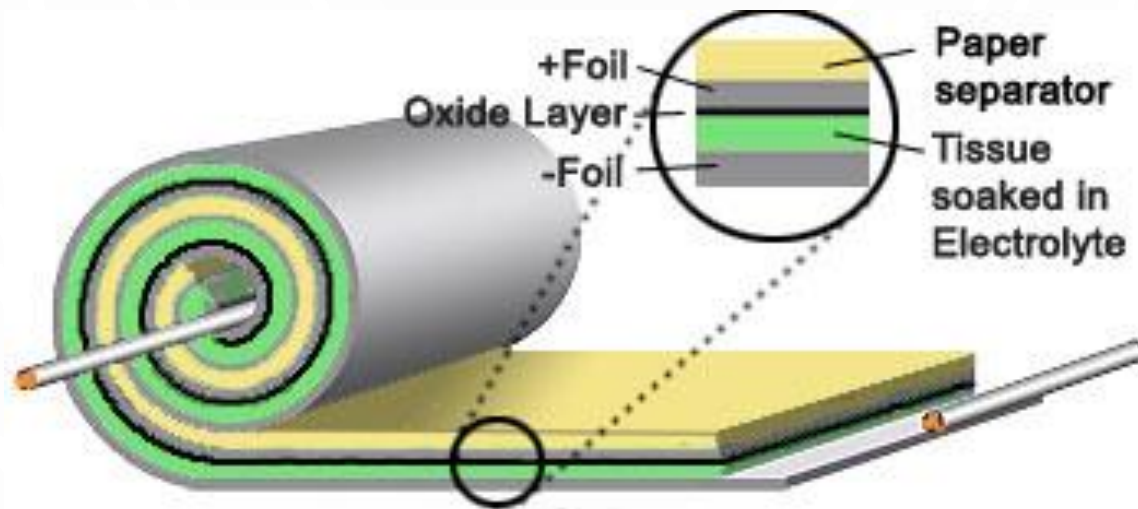


Polarized Capacitor

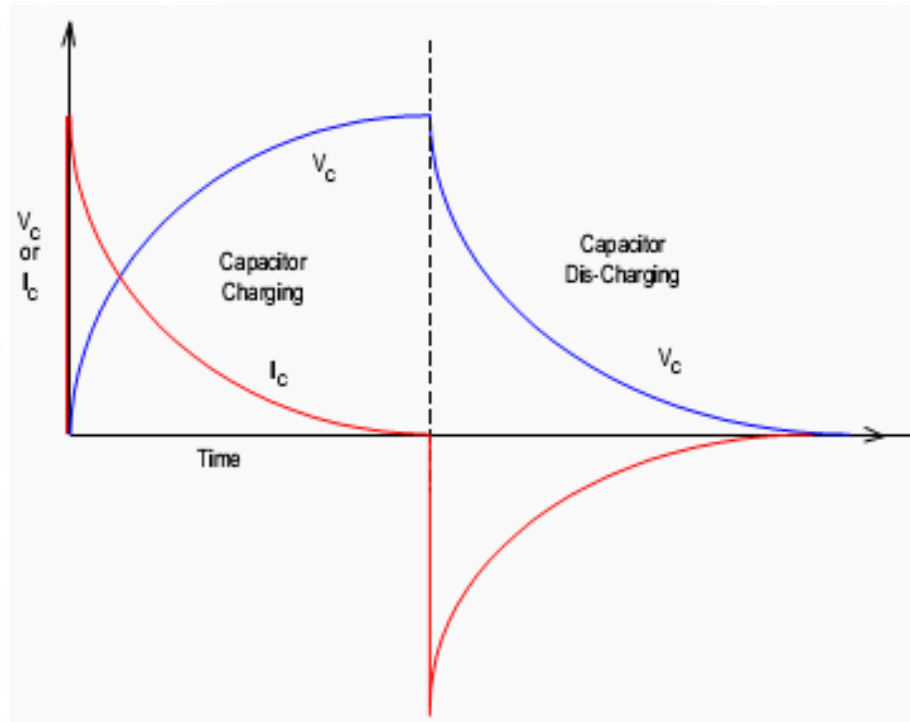


Variable Capacitor

CAPACITORS



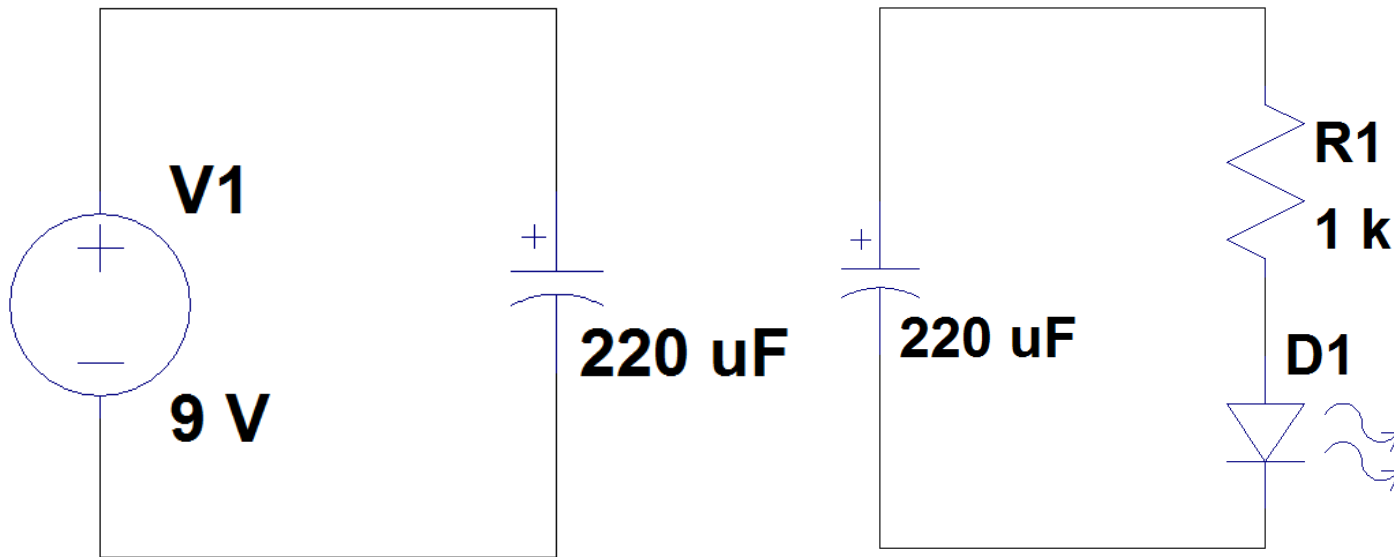
CAPACITORS



$$C = \frac{Q}{V}$$

$$I(t) = \frac{dQ(t)}{dt} = C \frac{dV(t)}{dt}$$

EXPERIENCE 3 – CHARGE A CAPACITOR

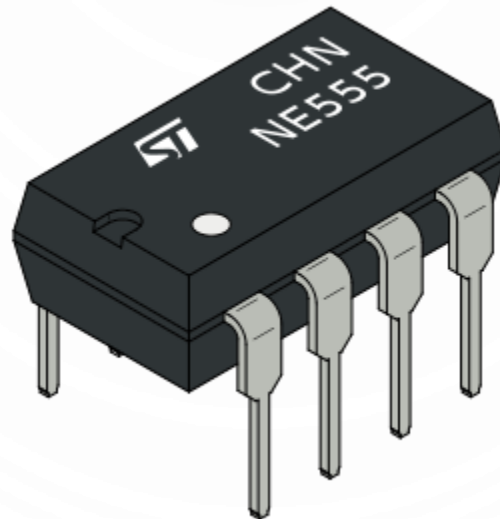


charging...

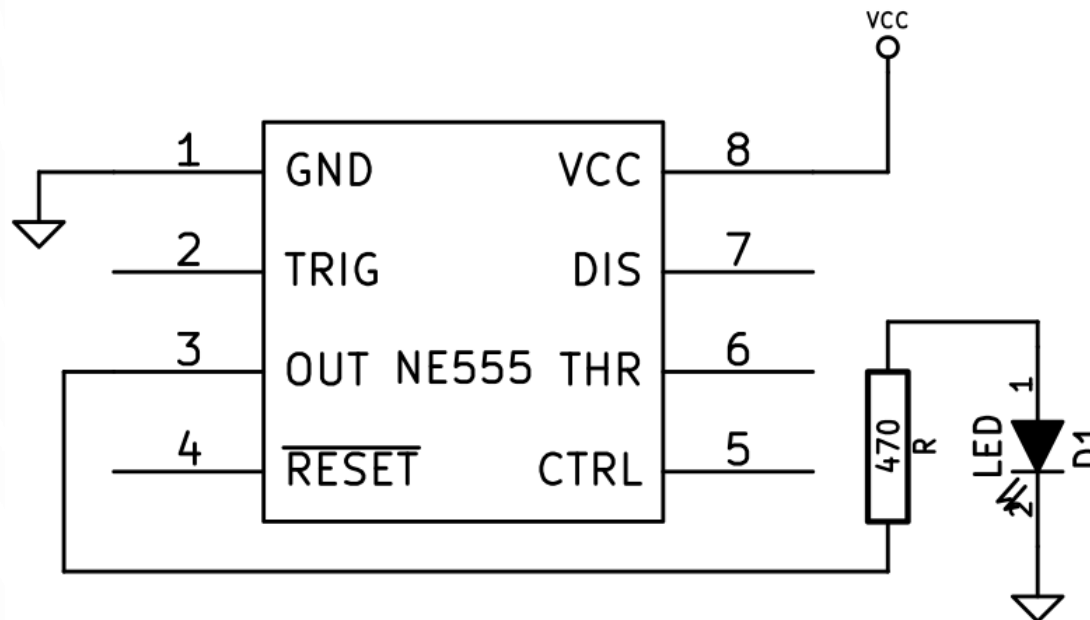
discharging

The capacitor is electrolytic. **Don't forget it has polarity!**

INTEGRATED CIRCUITS

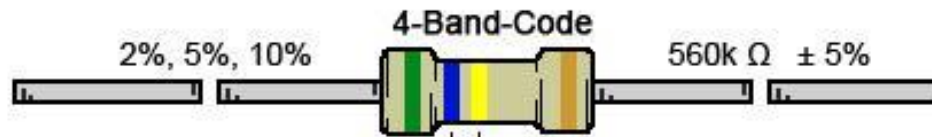


EXPERIENCE 4 – CREATE A SQUARE WAVEFORM

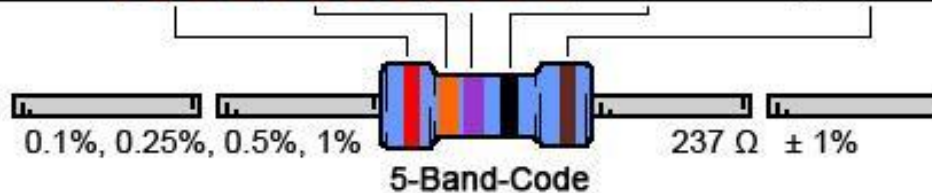


The remaining connections can be seen in the datasheet, circuit for astable operation

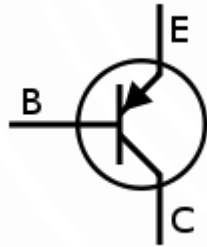
COLOUR CODE



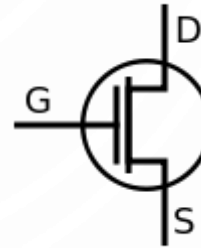
COLOR	1 ST BAND	2 ND BAND	3 RD BAND	MULTIPLIER	TOLERANCE
Black	0	0	0	1 Ω	
Brown	1	1	1	10 Ω	\pm 1% (F)
Red	2	2	2	100 Ω	\pm 2% (G)
Orange	3	3	3	1K Ω	
Yellow	4	4	4	10K Ω	
Green	5	5	5	100K Ω	\pm 0.5% (D)
Blue	6	6	6	1M Ω	\pm 0.25% (C)
Violet	7	7	7	10M Ω	\pm 0.10% (B)
Grey	8	8	8		\pm 0.05%
White	9	9	9		
Gold				0.1 Ω	\pm 5% (J)
Silver				0.01 Ω	\pm 10% (K)



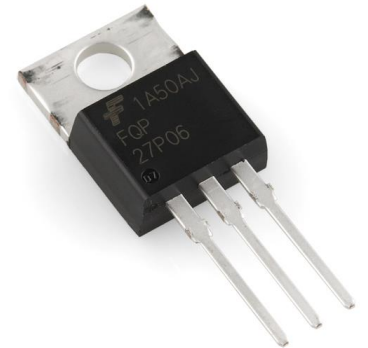
TRANSISTOR



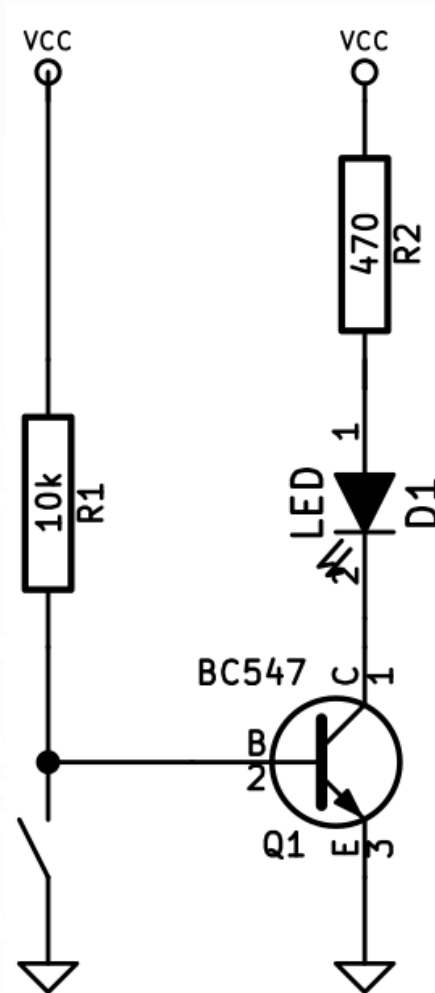
BJT



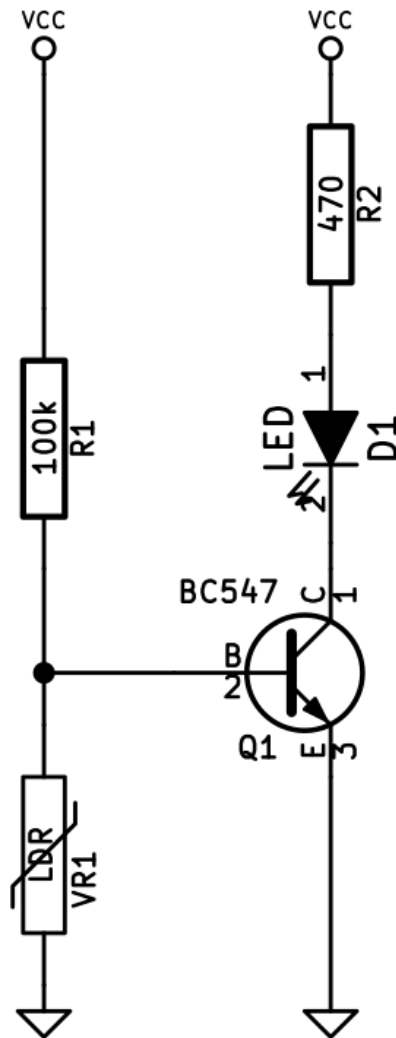
MOSFET



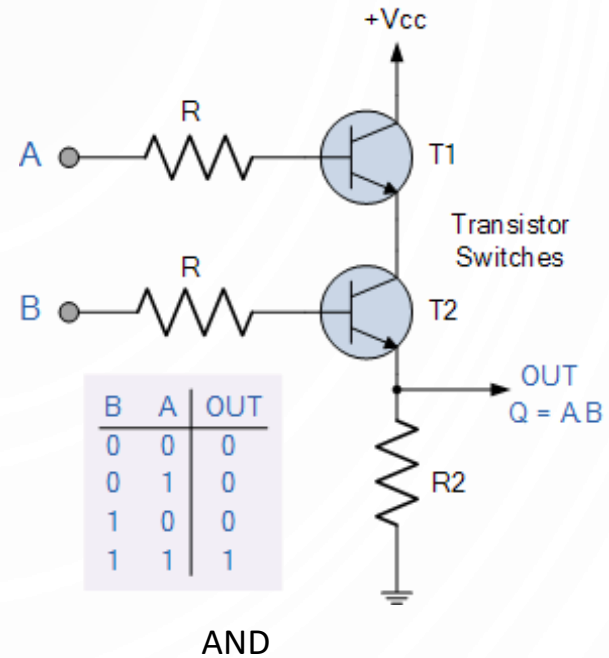
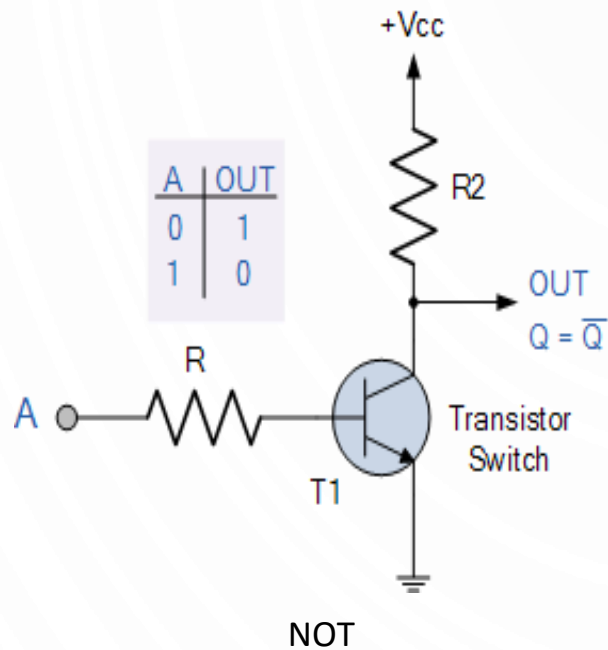
EXPERIENCE 6



EXPERIENCE 7 – LIGHT SENSOR

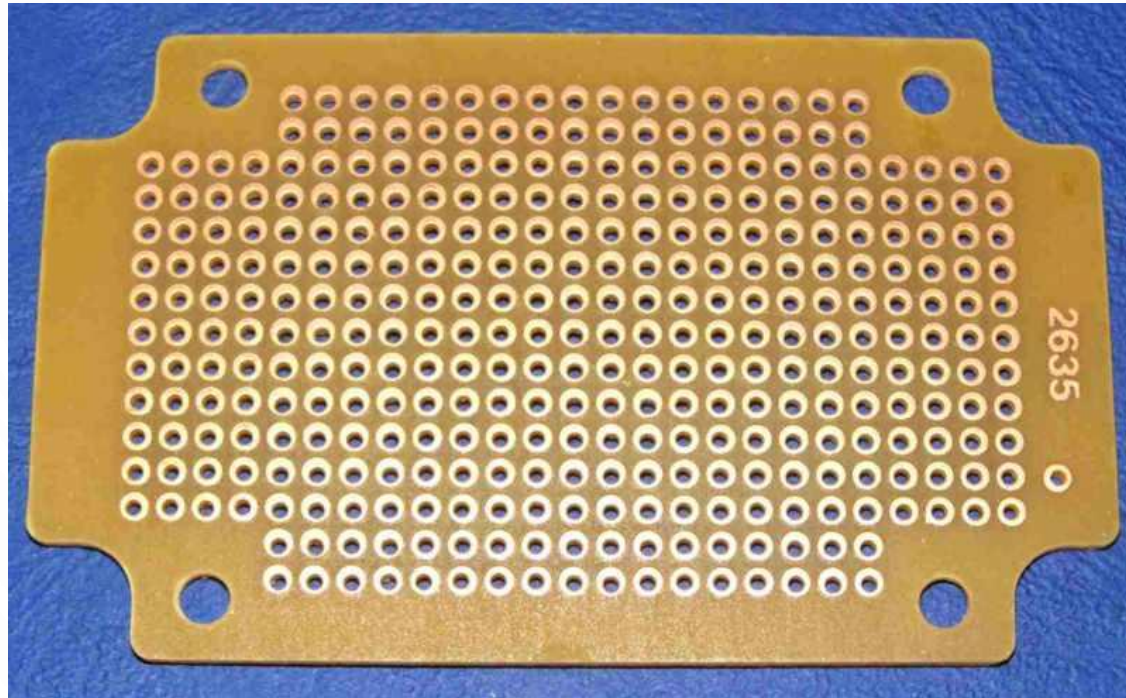


TRANSISTORS AS LOGIC GATES

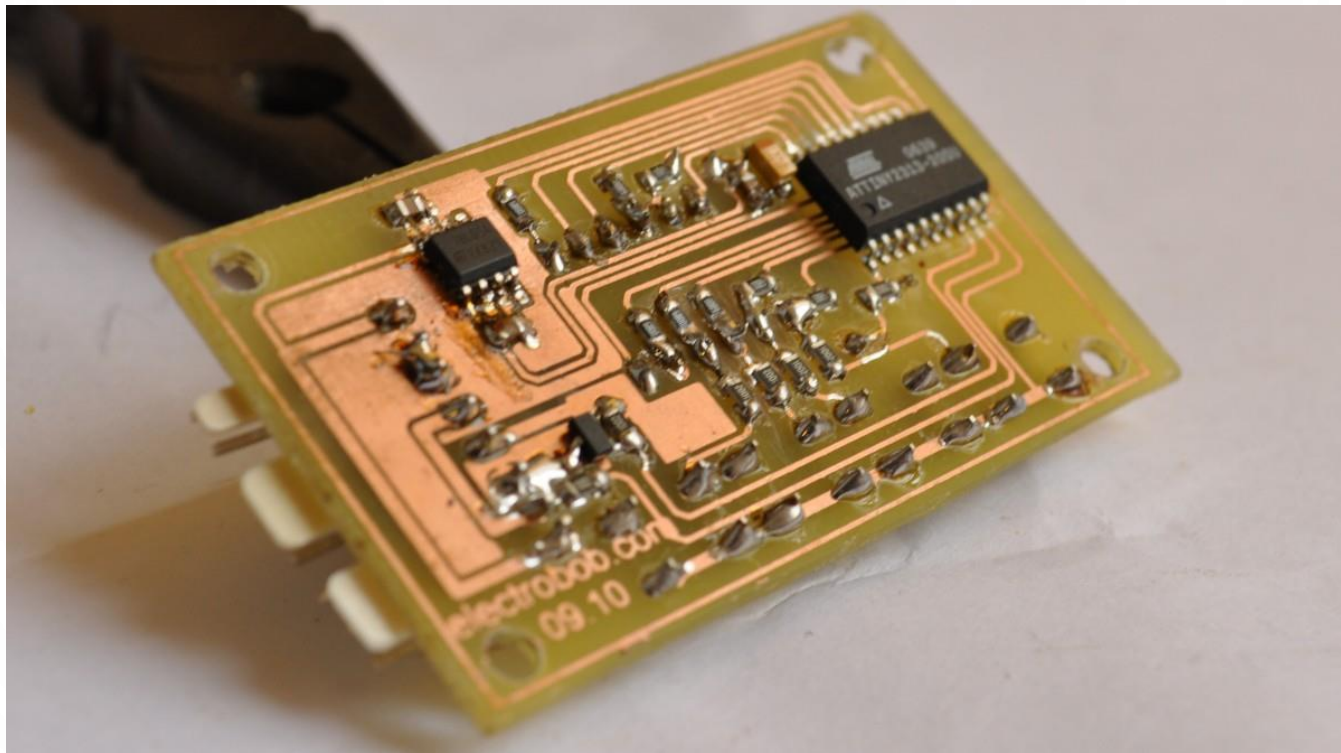


Exemplo:
 $R = 10 \text{ k}\Omega$
 $R2 = 1 \text{ k}\Omega$

PERFURATED BOARDS



PRINTED CIRCUIT BOARDS



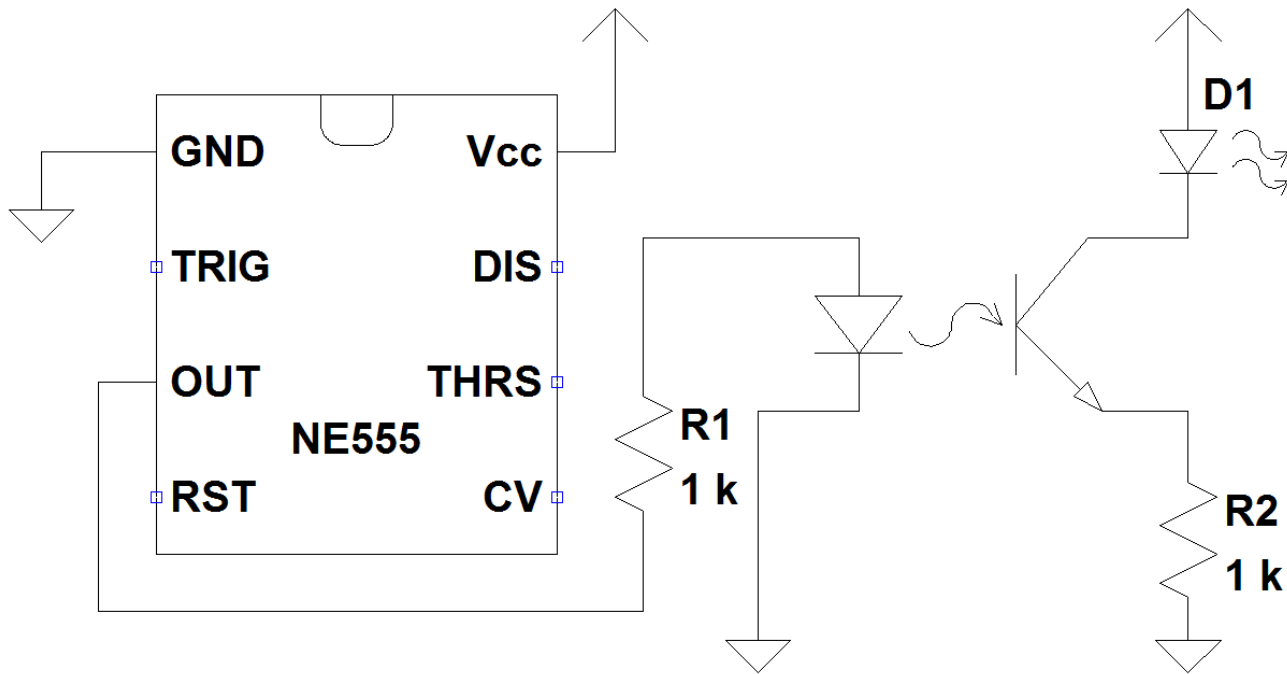
The image features a light gray background with a subtle pattern of concentric circles. In the four corners, there are decorative circuit-like patterns consisting of thin black lines and small open circles, resembling a printed circuit board (PCB) layout.

HACKER { school }

The image features a minimalist design with decorative circuit-like lines in the corners. These lines, composed of straight segments and small circles, resemble electronic traces and are located in the top-left, top-right, bottom-left, and bottom-right corners. The background is a light gray with a subtle pattern of concentric circles centered around the text.

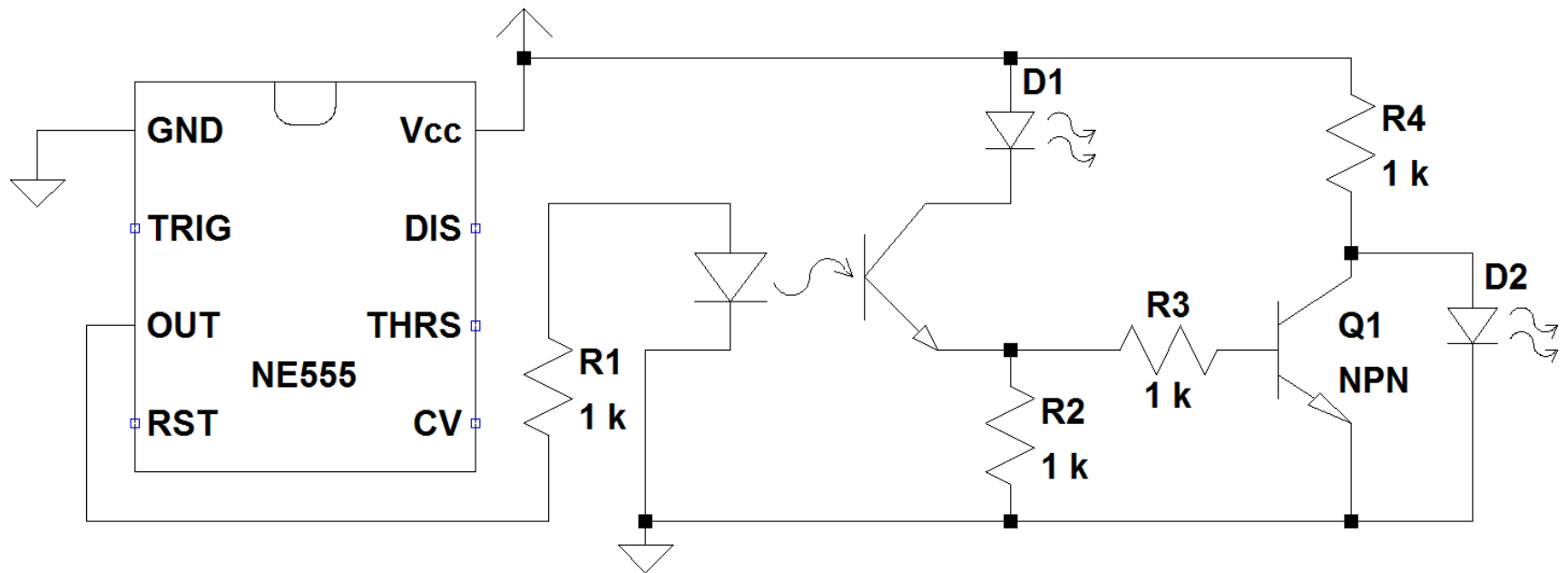
EXTRA EXPERIENCES

CREATE AN INFRA-RED RECEPTOR



The remaining connections can be seen on the datasheet, astable circuit

LOGIC GATES (NOT)



The remaining connections can be seen on the datasheet, astable circuit