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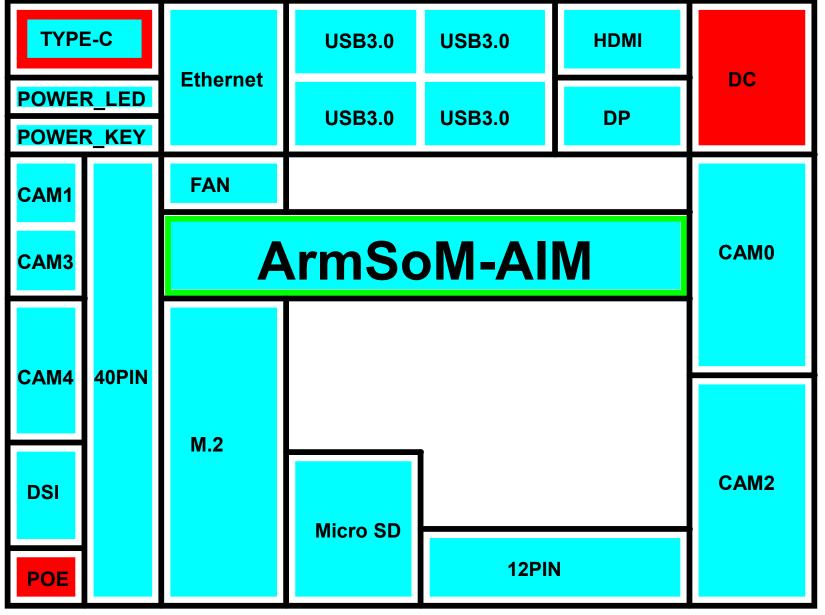
Sarmsom ArmSoM						
Project:	AIM_IO_Board					
File:	Table of	Contents				
Date:	Friday, November 29, 2024			Rev:	V1.1	
Designed by:	Park	Reviewed by:	<checker></checker>	Sheet:	1 of 25	

# Revision History

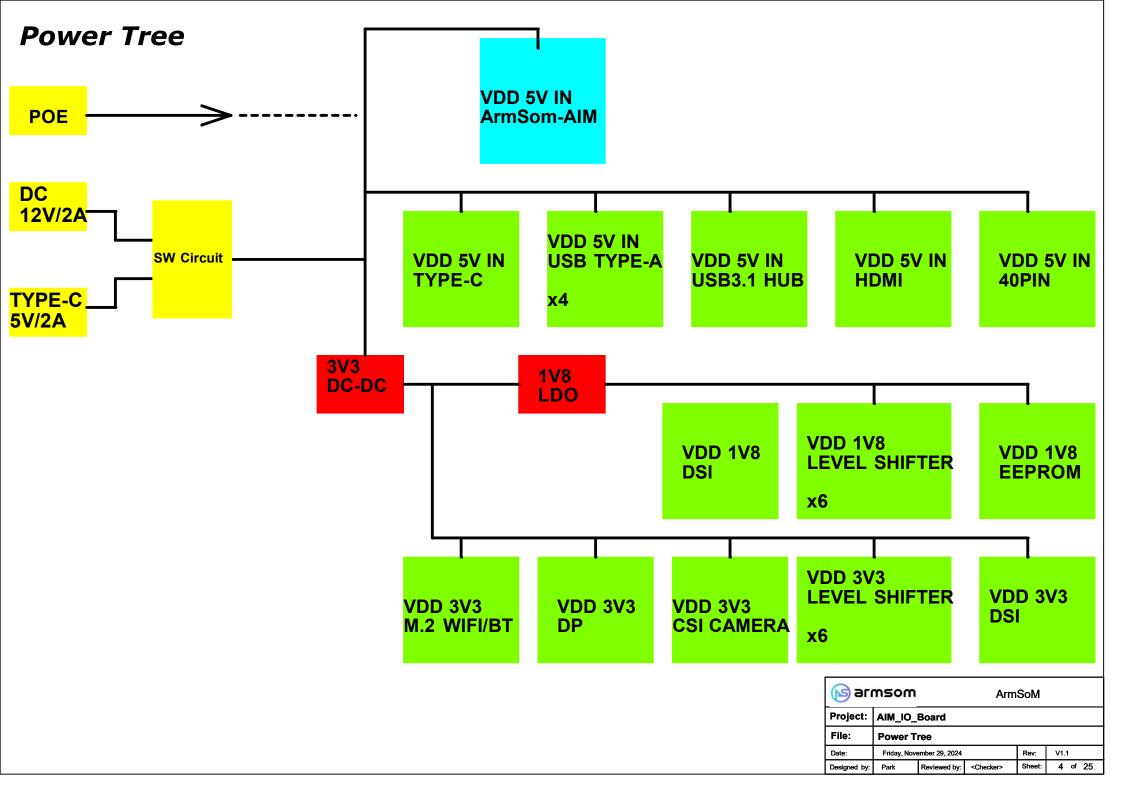
Version	Date	Ву	Change Dsecription	Approved
V1.0	2024-03-18	Liu Xinglin	1: First release;	
V1.1	2024-11-19	Park	1: TypeC DET: add R6370; USBHUB:add Jump testpoint VS1-VS15; 2: DSI: Lcd&tp two connectors change to one connector; DP:add R6371&R6372 3: CAM: Cam2&Cam3&Cam4 RST singal change to uart0_rts&uart0_cts; 4: DCIN: change DC-DC IC; USB SW change to MOS; 5: MEC5/MEC6A/MEC6B change PCB Footprint;	

S armsom ArmSoM							
Project:	AIM_IO_Board						
File:	Revision	Revision History					
Date:	Tuesday, December 10, 2024			Rev:	V1.1		
Designed by:	Park	Reviewed by:	Sheet:	2 of 25			

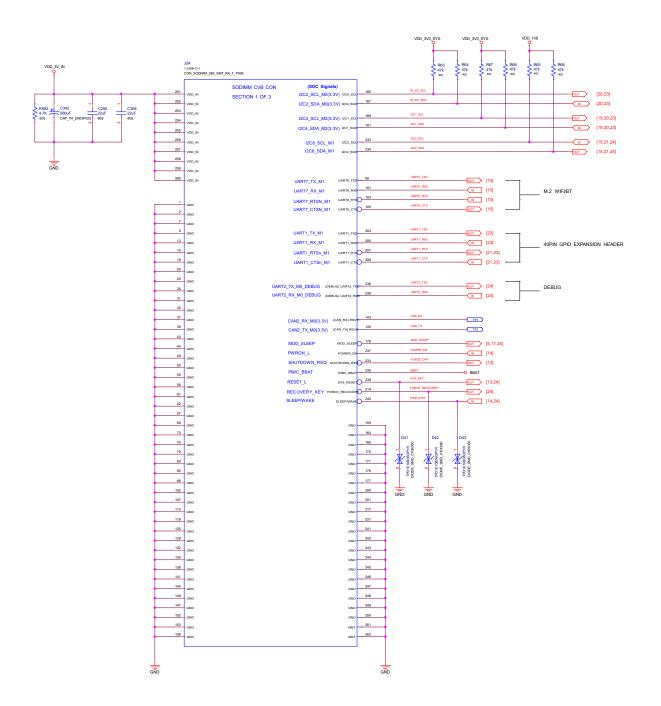
## **Block Diagram**

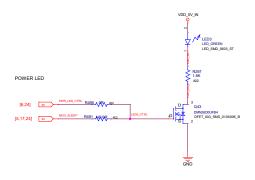


Sarmsom ArmS						
Project:	AIM_IO_Board					
File:	Block Di	agram				
Date:	Friday, November 29, 2024			Rev:	V1.1	
Designed by:	Park	Reviewed by:	<checker></checker>	Sheet:	3 of 25	



### UART/ I2C/CAN/POWER\_GPIO



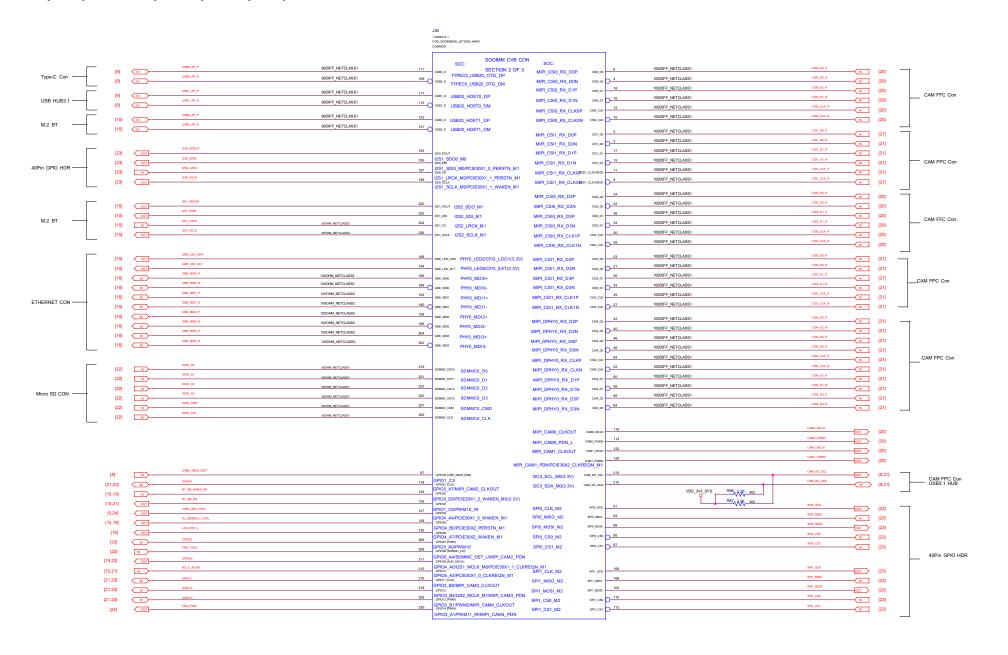




USE EITHER SUPERCAP OR COIN CELL BATTERY

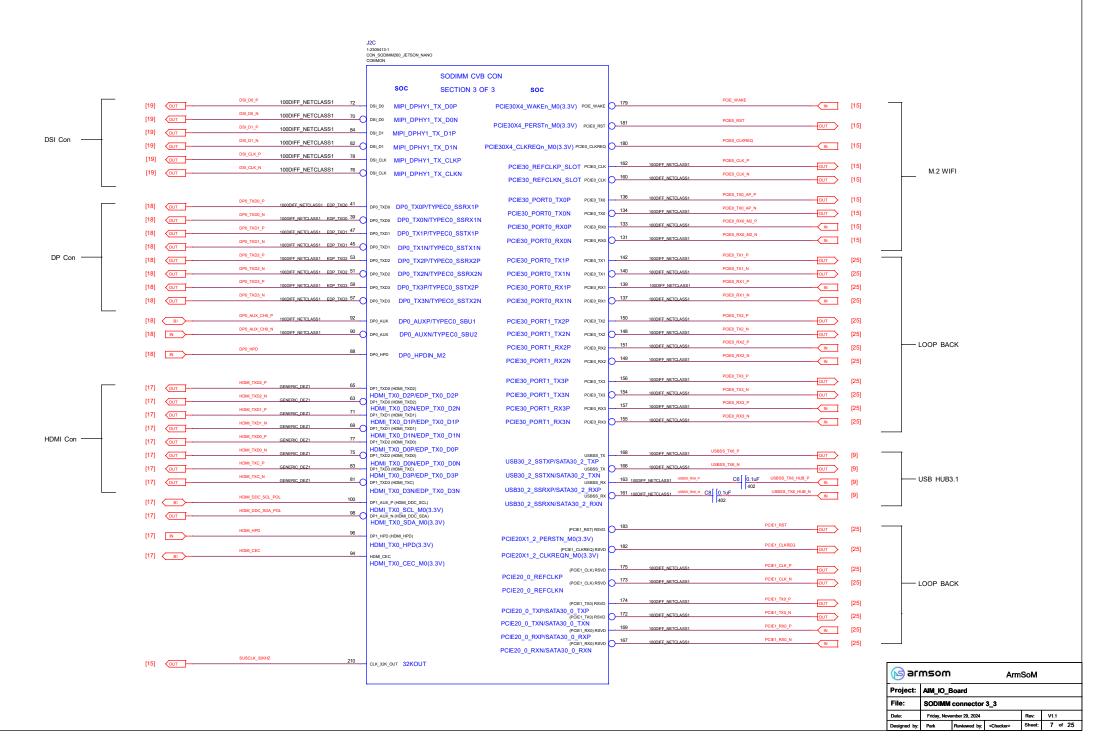
<b>№</b> аг	msom		Агп	SoM		
Project:	AIM_IO_	Board				
File:	SODIMN	connecto	r 1_3			
Date:	Tuesday, December 10, 2024			Rev:	V1.1	
Designed by:	Park	Reviewed by:	<checker></checker>	Sheet:	5 of	25

#### USB2.0/I2S/Ethernet/SDIO/GPIO/CSI/SPI

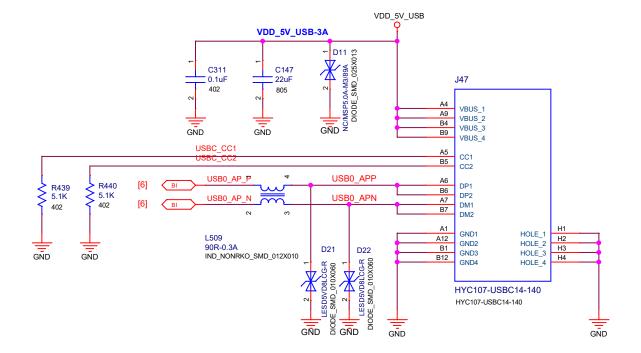


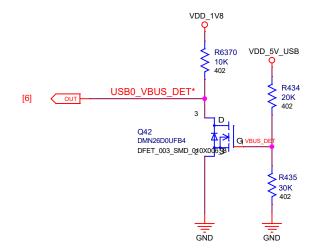
<b>⋈</b> ar	msom	ArmSoM				
Project:	AIM_IO_	Board				
File:	SODIMM	I connecto	r 2_3			
Date:	Friday, Nov	Friday, November 29, 2024			V1.1	
Designed by:	Park	Reviewed by:	<checker></checker>	Sheet:	6 of 25	

### DSI/DP/HDMI/M.2/USB3.1/LOOP BACK



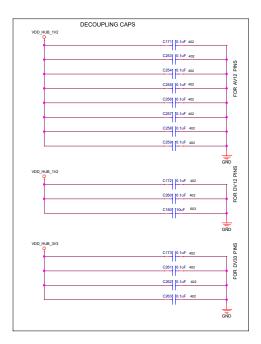
## Type-c

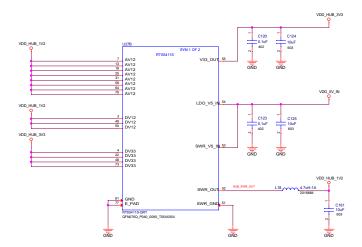


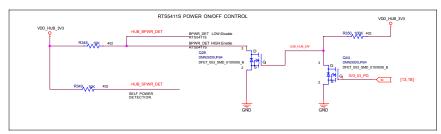


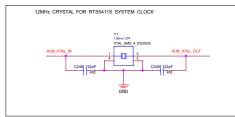
S armsom ArmSoM							
Project:	AIM_IO_Board						
File:	USB2.0	USB2.0 Type-C					
Date:	Tuesday, December 10, 2024			Rev:	V1.1		
Designed by:	Park	Reviewed by:	<checker></checker>	Sheet:	8 of 25		

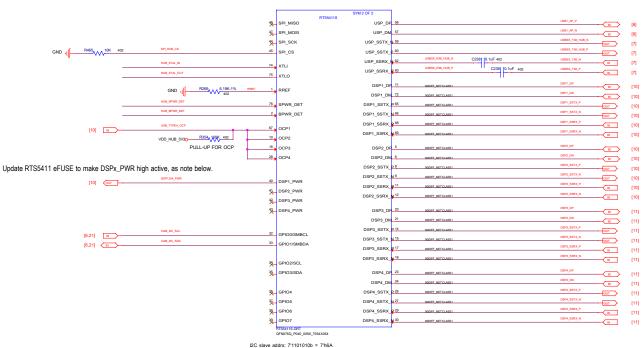
#### USB3.1 HUB









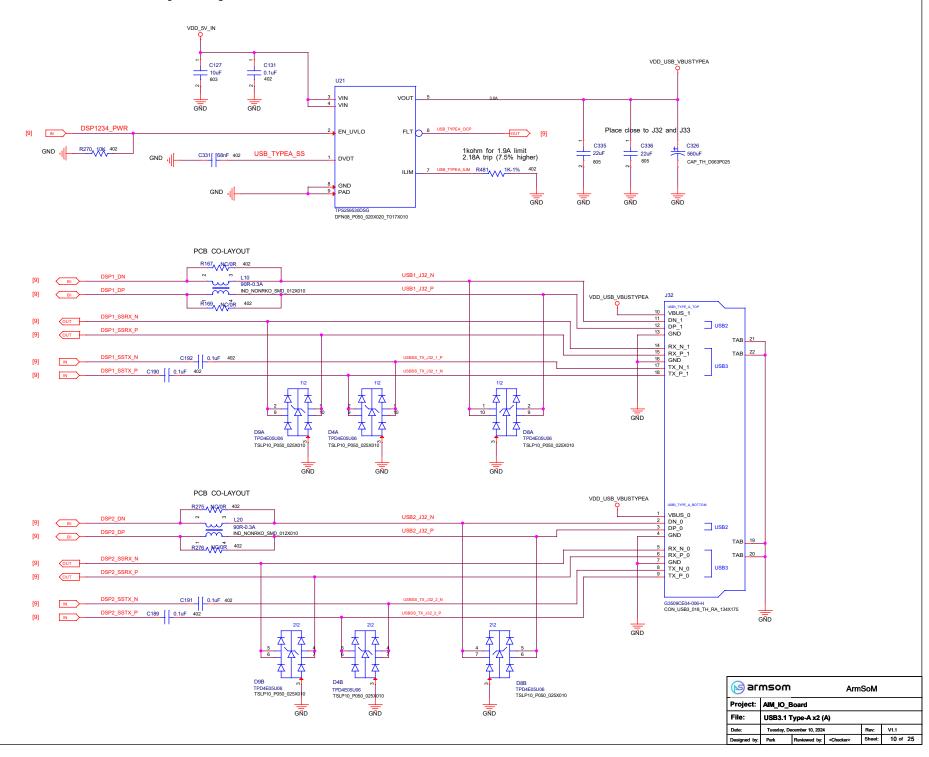


U27A

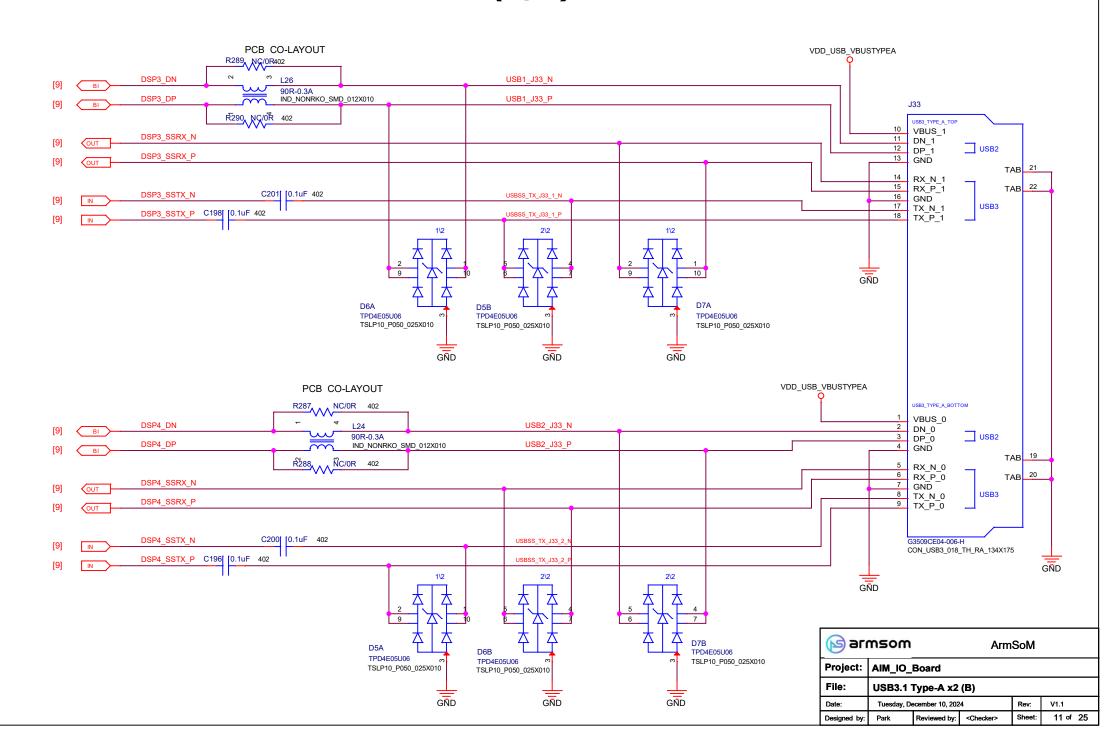
SPI Flash was removed from B01, customize RTS5411 internal eFuse (DSP1/2/3/4\_PWR High active, disable charging feature, SSC valid) with Realtek tool.

<b>⊗</b> armsom			Arm	SoM		
Project:	AIM_IO_Board					
File:	USB3.1	x4 HUB				
Date:	Tuesday, December 10, 2024			Rev:	V1.1	
Designed by:	Park	Reviewed by:	<checker></checker>	Sheet:	9 of 25	

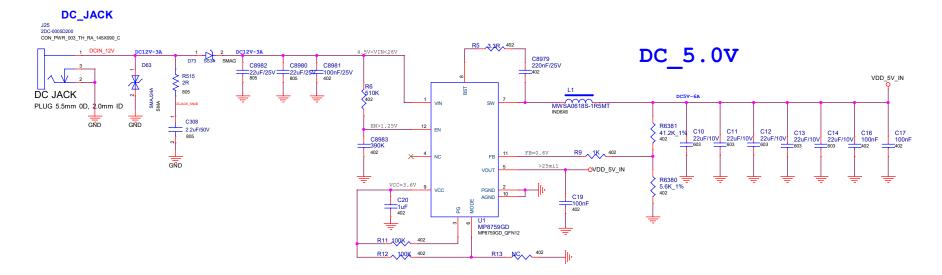
## USB 3.0 TYPE A x2 CONNECTOR (1/2)

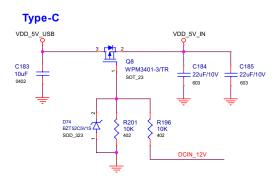


## USB 3.0 TYPE A x2 CONNECTOR (2/2)



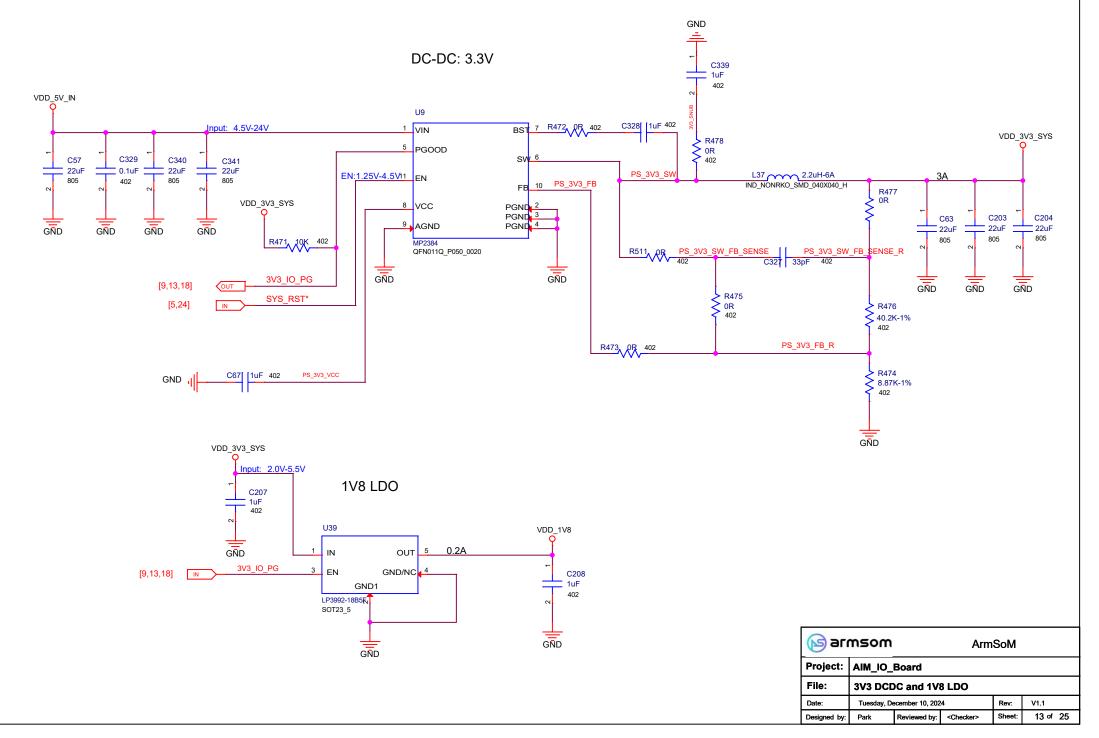
## DC JACK & Type C POWER



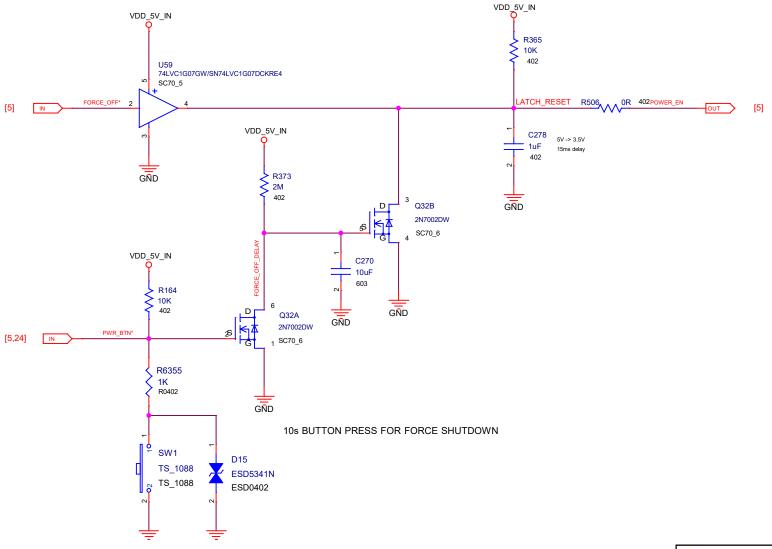


Som ArmSoM ArmSoM						
Project:	AIM_IO_E	AIM_IO_Board				
File:	DC Jack					
Date:	Tuesday, De	Tuesday, December 10, 2024			V1.1	
Designed by:	Park	Reviewed by:	<checker></checker>	Sheet:	12 of 25	

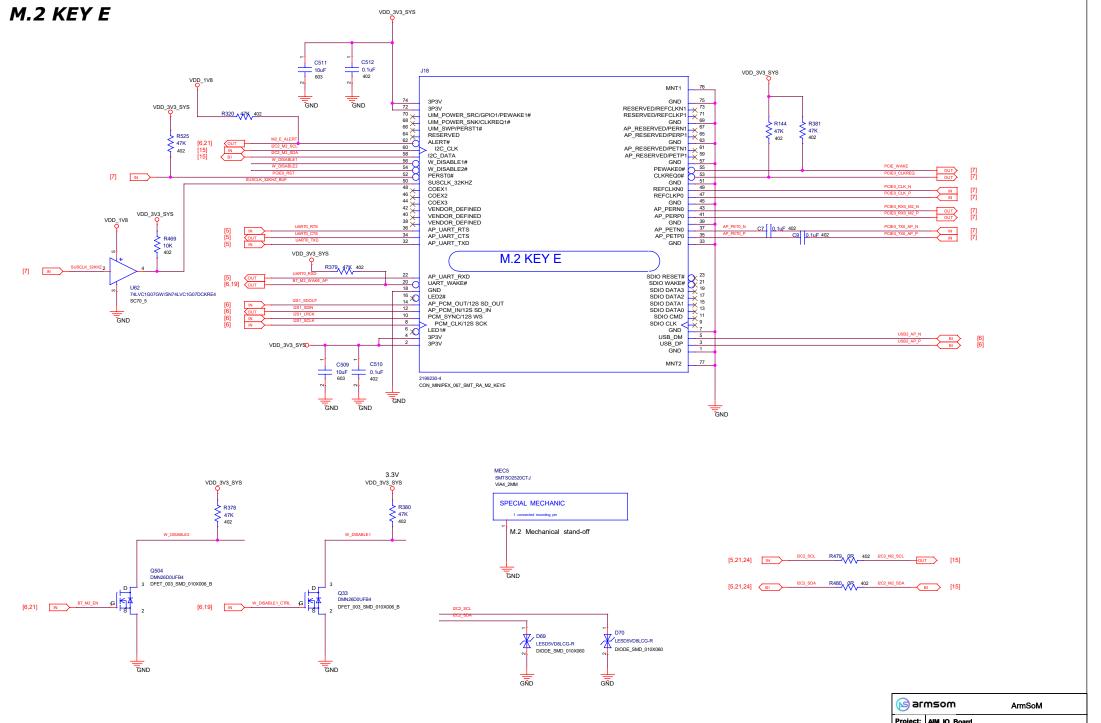
## *3V3/1V8*



# Module Power ON/OFF logic

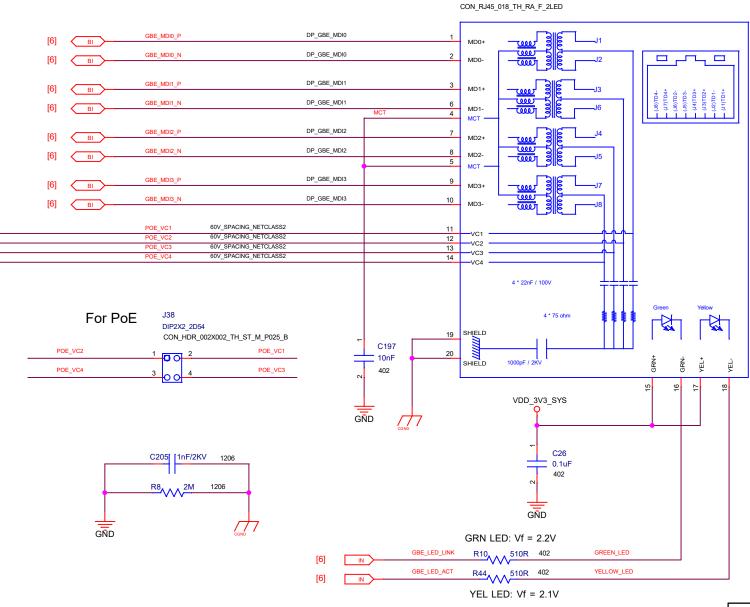


Sarmsom ArmSoM							
Project:	AIM_IO_Board						
File:	Power ON_OFF Logic						
Date:	Tuesday, De	Tuesday, December 10, 2024			V1.1		
Designed by:	Park	Reviewed by:	Sheet:	14 of 25			



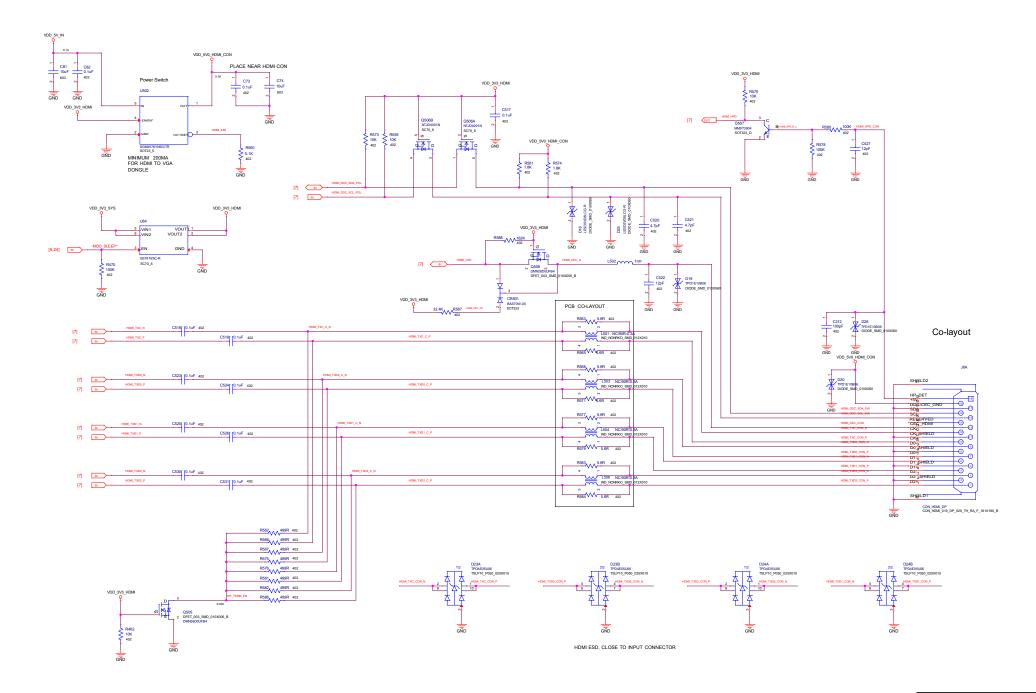
<b>⊗</b> armsom			Arm	ArmSoM		
Project:	ct: AIM_IO_Board M.2 Key-E					
File:						
Date:	Tuesday, De	Tuesday, December 10, 2024			V1.1	
Designed by:	Park	Reviewed by:	<checker></checker>	Sheet:	15 of 25	

# Gigabit Ethernet with PoE

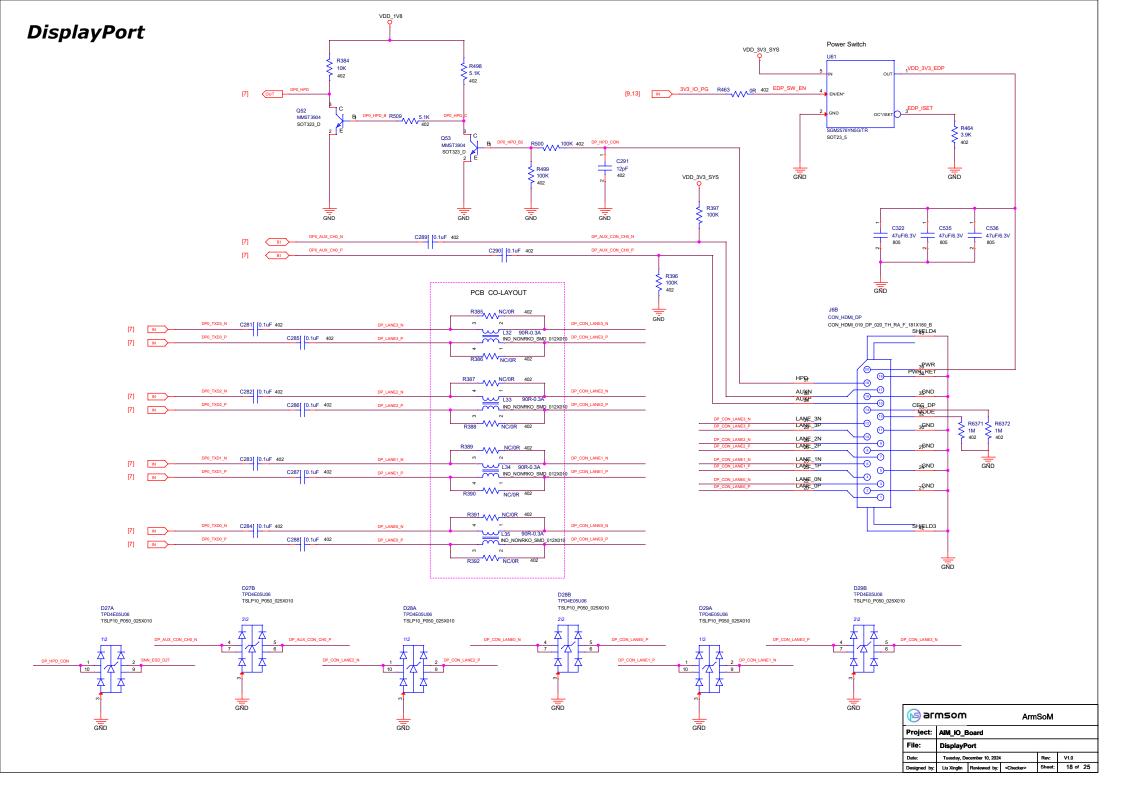


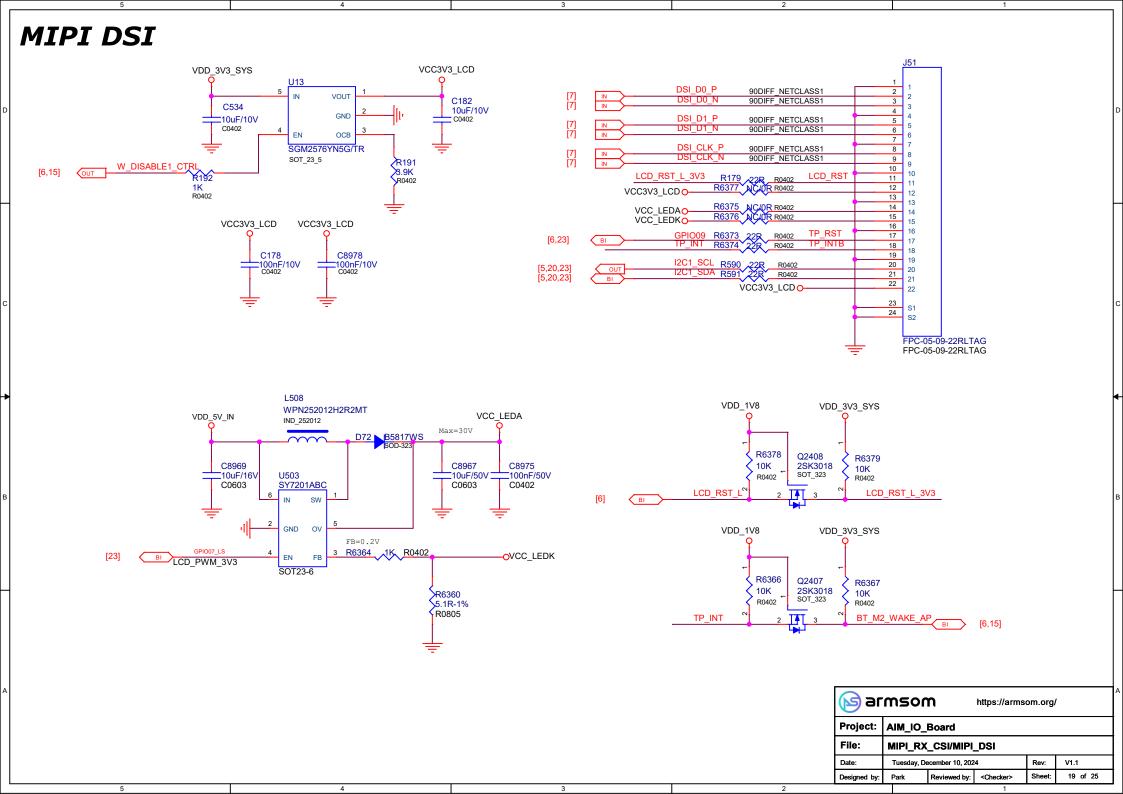
J43 A70-112-331N126

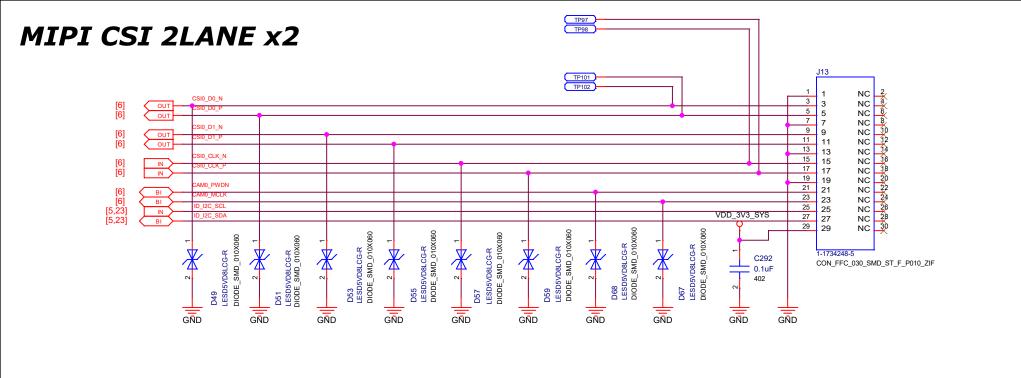
<b>№</b> armsom				ArmSoM			
Project:	AIM_IO_Board						
File:	Gigabit Ethernet POE						
Date:	Tuesday, De	Tuesday, December 10, 2024			V1.1		
Designed by:	Park	Reviewed by:	<checker></checker>	Sheet:	16 of 25		

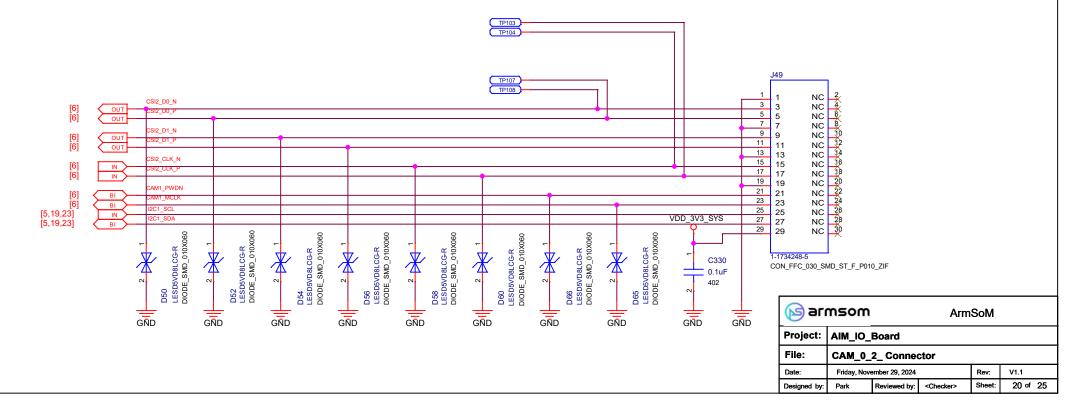


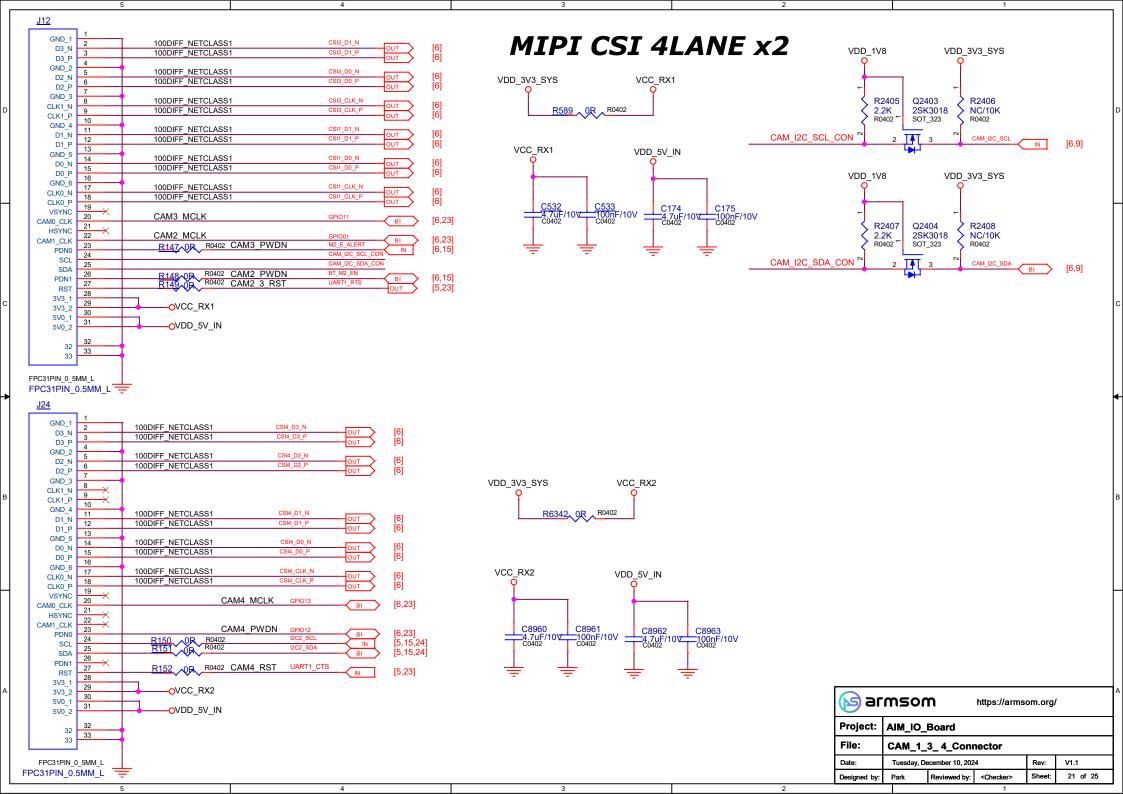
<b>⊗</b> ar	msom	ArmSoM				
Project:	AIM_IO_	Board				
File:	HDMI					
Date:	Tuesday, D	lecember 10, 20	cember 10, 2024		V1.1	
Designed by:	Park	Reviewed by:	<checker></checker>	Sheet:	17 of 25	

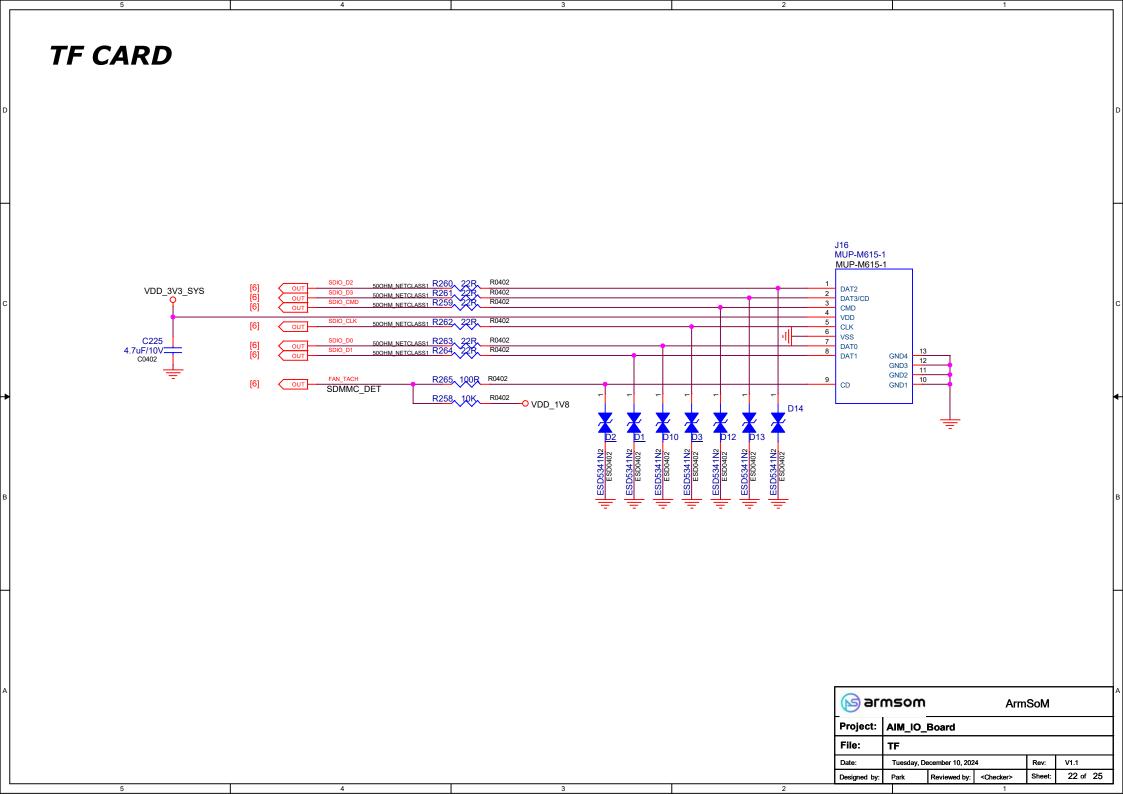




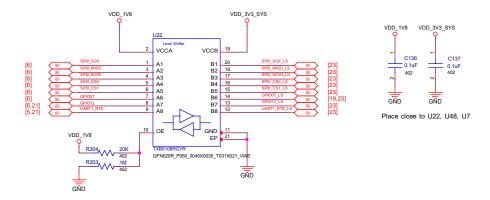


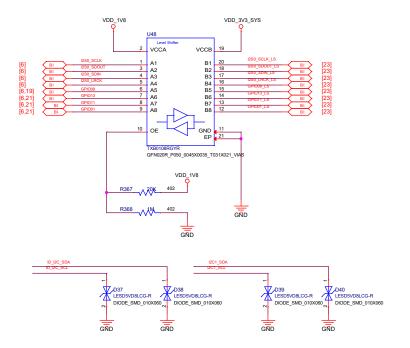


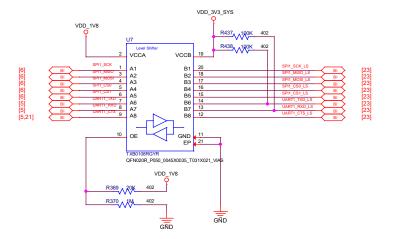


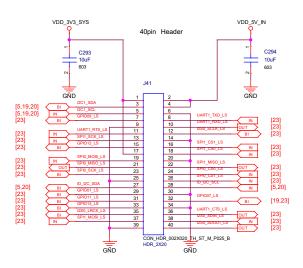


### 40PIN Header

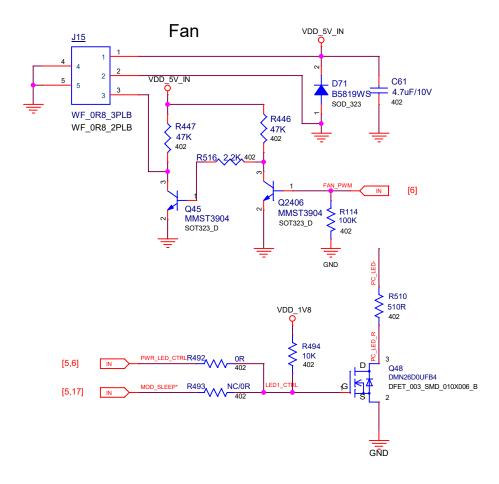


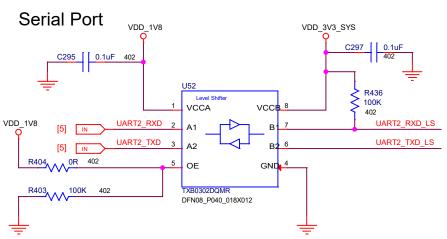


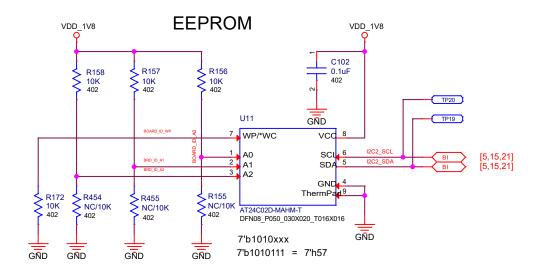


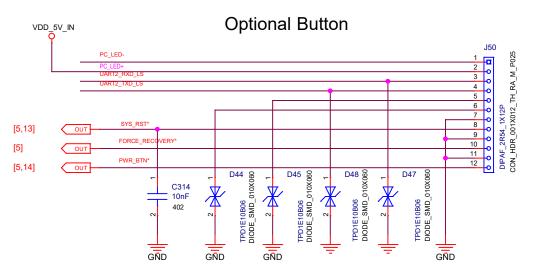


<b>№</b> ar	msom	Į	Arm	SoM		
Project:	AIM_IO_I	Board				
File:	40Pin He	40Pin Header				
Date:	Tuesday, December 10, 2024			Rev:	V1.1	
Designed by:	Park	Reviewed by:	<checker></checker>	Sheet:	23 of 25	





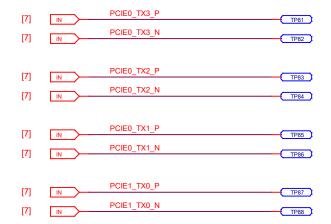




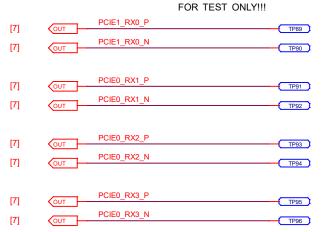
Sarmsom ArmSoM					
Project:	AIM_IO_Board				
File:	Fan_Debug _EEPROM				
Date:	Tuesday, December 10, 2024			Rev:	V1.1
Designed by:	Park	Reviewed by:	<checker></checker>	Sheet:	24 of 25

## LOOP BACK

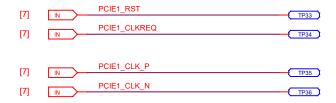
#### PCIe LOOP BACK TO TEST UNUSED PCIe SIGNALS



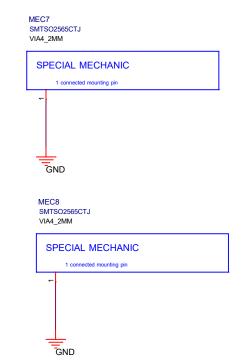
### PLACE ALL TESTPOINTS ON THE BOTTOM OF THE BOARD

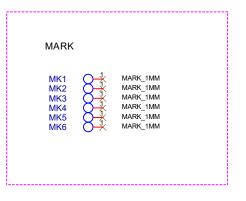


#### PLACE TESTPOINT ON THE BOTTOM OF THE BOARD



#### Mounting holes for Module





Sarmsom Arm							
Project:	AIM_IO_Board						
File:	ile: LOOP BACK						
Date:	Tuesday, December 10, 2024			Rev:	V1.1		
Designed by:	Park	Reviewed by:	<checker></checker>	Sheet:	25 of	25	