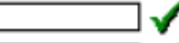
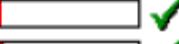


Covergroups

Name	Class Type	Coverage	Goal	% of Goal	Status	Included	Merge_in
/tb_crw_randomize_lab3_p1_hlam		23.28%					
TYPE cg_inputs		23.28%	100	23.28%		✓	
CVP cg_inputs::a_sig		32.42%	100	32.42%		✓	
CVP cg_inputs::b_sig		29.29%	100	29.29%		✓	
CVP cg_inputs::opcode_sig		31.25%	100	31.25%		✓	
CROSS cg_inputs::{#cross_0#}		0.15%	100	0.15%		✓	
INST Vtb_crw_randomize_lab3_p1_hlam/cov_inst...		23.28%	100	23.28%		✓	

Name	Class Type	Coverage	Goal	% of Goal	Status	Included	Merge_ins
/tb_crvt_randomize_lab3_p1_hlam		23.28%					
TYPE cg_inputs		23.28%	100	23.28%	 		
CVP cg_inputs::a_sig		32.42%	100	32.42%	 		
CVP cg_inputs::b_sig		29.29%	100	29.29%	 		
CVP cg_inputs::opcode_sig		31.25%	100	31.25%	 		
CROSS cg_inputs::#cross_0#		0.15%	100	0.15%	 		
INST Vtb_crvt_randomize_lab3_p1_hlam/cov_inst...		23.28%	100	23.28%	 		
CVP a_sig		32.42%	100	32.42%	 		
CVP b_sig		29.29%	100	29.29%	 		
CVP opcode_sig		31.25%	100	31.25%	 		
CROSS #cross_0#		0.15%	100	0.15%	 		
B] bin <a[246],b[234]>		1	1	100.00%	 		
B] bin <a[236],b[222]>		1	1	100.00%	 		
B] bin <a[255],b[215]>		1	1	100.00%	 		
B] bin <a[255],b[210]>		1	1	100.00%	 		
B] bin <a[234],b[210]>		1	1	100.00%	 		
B] bin <a[210],b[194]>		1	1	100.00%	 		
B] bin <a[196],b[194]>		1	1	100.00%	 		
B] bin <a[194],b[193]>		1	1	100.00%	 		
B] bin <a[246],b[190]>		1	1	100.00%	 		
B] bin <a[196],b[185]>		1	1	100.00%	 		
B] bin <a[243],b[172]>		1	1	100.00%	 		
B] bin <a[255],b[170]>		1	1	100.00%	 		
B] bin <a[185],b[167]>		1	1	100.00%	 		
B] bin <a[210],b[162]>		1	1	100.00%	 		
B] bin <a[229],b[157]>		1	1	100.00%	 		
B] bin <a[155],b[154]>		1	1	100.00%	 		
B] bin <a[203],b[147]>		1	1	100.00%	 		
B] bin <a[222],b[146]>		1	1	100.00%	 		
B] bin <a[244],b[140]>		1	1	100.00%	 		
B] bin <a[222],b[137]>		1	1	100.00%	 		