

ARMAN SOLTANI

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www.armansoltani.ir

EDUCATION

Shiraz University, Shiraz, Iran

Bachelor of Engineering, Electronics

Department of Electrical and Computer Engineering

September 2014 - January 2019

RESEARCH INTERESTS

- Artificial Intelligence
- Deep Learning
- Image Processing
- Biomedical Engineering
- Machine Learning
- Digital Signal Processing

RESEARCH EXPERIENCE

Surgical room engineer Assistant at Namazi Hospital

Least squares audio and speech compression

Face recolonization on the ORL face database

ADSL protocols

USB interface

Design Digital Lock with Logic Works and Implement it on Bread board

Design Analog AM Modulator with ADS, Design its Printed Circuit Board with Altium Designer and Implement it on board

Design Digital AM Modulator with Xilinx and Implement it on FPGA Spartan 6 development board

Design Efficient Vector Processor with Xilinx and Implement it on FPGA Spartan 6 development board

Supervised by Eng. M. Karimi

Supervised by Prof. A. Masnadi

Supervised by Prof. H. Keshavarz

Supervised by Prof. N. Yasrebi

supervised by Prof. M. Farhang

Supervised by Prof. Abiri

Supervised by Prof. N. Yasrebi

Supervised by Prof. N. Yasrebi

WORK EXPERIENCE

Teaching Assistant:

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|---|--------------------------------------|
| Digital Logic Circuits course instructor Prof. N. Yasrebi | <i>February 2017 - June 2017</i> |
| Computer Architecture course instructor Prof. N. Yasrebi | <i>September 2017 - January 2018</i> |
| Digital Logic Circuits Laboratory course instructor Prof. M. Yazdi | <i>September 2017 - January 2018</i> |
| Digital Logic Circuits Laboratory course instructor Prof. M. Farhang | <i>September 2017 - January 2018</i> |
| Digital Logic Circuits course instructor Prof. M. Yazdi | <i>February 2018 - June 2018</i> |
| Digital Logic Circuits Laboratory course instructor Prof. N. Yasrebi | <i>February 2018 - June 2018</i> |
| Probability and Statistics course instructor Prof. M. Derakhtian and Mr. Enjavi | <i>February 2018 - June 2018</i> |
| Digital Communication course instructor Prof. A. Sheikhi and Mr. Enjavi | <i>February 2018 - June 2018</i> |
| Field-Programmable Gate Array (FPGA) course instructor Prof. M. Raji | <i>September 2018 - January 2019</i> |

Instructor for software learning:

- Matlab
- Xilinx ISE
- Logic Works
- Proteus

SKILLS

• Programming Skills

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|------------------|--------|-------------------|--------------|
| Matlab: | Expert | Python: | Intermediate |
| Verilog: | Expert | C/C++: | Intermediate |
| Assembly: | Expert | Q/V Basic: | Intermediate |
| Latex: | Expert | HTML/CSS: | Intermediate |

• Engineering Software Skills

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|--------------------------|--------|-------------------------|--------------|
| AVR (CodeVision): | Expert | Arduino: | Expert |
| ISIM: | Expert | Pspice: | Expert |
| Xilinx ISE: | Expert | ARM (Keil): | Intermediate |
| Logic Works: | Expert | Altium Designer: | Intermediate |
| Proteus: | Expert | ADS: | Intermediate |

EXTRACURRICULAR ACTIVITIES

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| Enroll Machine Learning course on Coursera | |
| Complete Python course on Udemy | |
| Member of IEEE Shiraz Student Branch | <i>2017 - 2019</i> |
| Member of the scientific association of Shiraz University | <i>2015 - 2019</i> |
| Member of the choir music group of Shiraz University | <i>2014 - 2015</i> |
| Member of the football team of Shiraz University | <i>2014 - 2016</i> |
| Leader of the procurement group of BTIU conference | <i>10th Round</i> |
| Referee of KAPA Competition | <i>7th Round</i> |

HONORS

Participation in Robotic Cup *Iran Open 2010*
Physical Fitness Coaching Certificate
Aqua Sport Coaching Certificate
Attending Cross Fit and functional training workshop

LANGUAGES

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|-----------------|------------------|----------------|-------|
| Persian: | Native | German: | Basic |
| English: | Fluent - IELTS 7 | Arabic: | Basic |

REFERENCES

| | |
|------------------|------------------------|
| Prof. N. Yasrebi | nyasrebi@shirazu.ac.ir |
| Prof. M. Raji | mraji@shirazu.ac.ir |
| Prof. M. Yazdi | yazdi@shirazu.ac.ir |