

ARMAN SOLTANI

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Bagh howz Blv. , Shiraz, Iran

EDUCATION

Shiraz university , Shiraz,Iran

Bachelor of Engineering, Electronics.

Department of Electrical and Computer Engineering

Shahid Dastgheib 2 Highschool, Shiraz,Iran

Student of Mathematics and Physics

National Organization for Development of Exceptional Talents

September 2014 - January 2019

Overall Percentage: 70.08

September 2010 - June 2014

Overall Percentage: 94.75

RESEARCH INTERESTS

- Embedded systems
- Digital Design
- Computer Architecture
- Digital Signal Processing
- Image Processing
- Data Processing
- Artificial Intelligence
- Bio electronics
- Biomedical engineering
- Internet of thing

SELECTED EXPERIENCES

Teaching Assistant:

Digital Logic Circuits: In collaboration with Prof. N. Yasrebi

February 2017 - June 2017

Computer Architecture: In collaboration with Prof. N. Yasrebi

September 2017 - January 2018

Digital Logic Circuits Laboratory: In collaboration with Prof. M. Yazdi

September 2017 - January 2018

Digital Logic Circuits Laboratory: In collaboration with Prof. M. Farhang

September 2017 - January 2018

Digital Logic Circuits: In collaboration with Prof. M. Yazdi

February 2018 - June 2018

Digital Logic Circuits Laboratory: In collaboration with Prof. N. Yasrebi

February 2018 - June 2018

Probability and Statistics: In collaboration with Prof. M. Derakhtian and Mr. Enjavi

February 2018 - June 2018

Digital Communication: In collaboration with Prof. A. Sheikhi and Mr. Enjavi

February 2018 - June 2018

Field-Programmable Gate Array (FPGA): In collaboration with Prof. M. Raji

September 2018 - January 2019

Instructor for software learning:

- Matlab
- Xilinx ISE
- Logic Works
- Proteus

Researches:

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|--|---|
| ADSL protocols | <i>supervised by Prof. H. Keshavarz</i> |
| USB interface | <i>supervised by Prof. N. Yasrebi</i> |
| Design Digital Lock with Logic Works and Implement it on Bread board | <i>supervised by Prof. M. Farhang</i> |
| Design Analog AM Modulator with ADS ,Design its Printed Circuit Board with Altium Designer and Implement it on board | <i>supervised by Prof. Abiri</i> |
| Design Digital AM Modulator with Xilinx and Implement it on FPGA Spartan 6 development board | <i>supervised by Prof. N. Yasrebi</i> |
| Design Efficient Vector Processor with Xilinx and Implement it on FPGA Spartan 6 development board | <i>supervised by Prof. N. Yasrebi</i> |
| Surgical room engineer Assistant at Namazi Hospital | <i>supervised by Eng. M. Karimi</i> |

ACTIVITIES AND HONORS

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|---|--------------------------------------|
| Member of the IEEE student branch of Shiraz University | <i>September 2016 - January 2019</i> |
| Member of the scientific association of Shiraz University | <i>September 2015 - January 2019</i> |
| Member of the student committee of radar conference | <i>simin</i> |
| Member of choir music group of Shiraz University | <i>September 2014 - June 2015</i> |
| Member of faculty of engineering football team | <i>September 2014 - June 2016</i> |
| Leader of the procurement group of BTIU conference | <i>10th Session</i> |
| Referee of Ebn Sina Competition | <i>7th Session</i> |
| Participation in RoboCup Iran Open 2010 | |
| Attending crossfit and functional training workshop | |
| Physical Fitness Coaching licence | |
| C Sharp Programing language licence | |
| HTML Web designing language licence | |
| UNIX user licence | |
| ICDL user licence | |

SKILLS

• Programming Skills

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|------------------|--------------|-------------------|--------------|
| Matlab: | Expert | Latex: | Expert |
| Verilog: | Expert | Q/V Basic: | Intermediate |
| C/C++: | Intermediate | HTML/CSS: | Intermediate |
| Assembly: | Intermediate | Python: | Intermediate |

• Engineering Software Skills

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|-------------------------|--------|-------------------------|--------------|
| AVR(CodeVision): | Expert | Pspice: | Expert |
| ISIM: | Expert | ARM(Keil): | Intermediate |
| Xilinx ISE: | Expert | Altium Designer: | Intermediate |
| LogicWorks: | Expert | ADS: | Intermediate |
| Proteus: | Expert | Arduino: | Intermediate |

- **Teaching Majors:**

Computer Architecture
Digital Logic Circuits

Micro Controllers
Micro Processors

FPGAs

LANGUAGES

Persian:
English:

Native
Fluent - IELTS 6.5

German:
Arabic:

Basic
Basic

REFERENCES

My website