## **INSTRUCTION SET**

## 13 Bit Instruction Word

No Operation	00000	"00000"
Load A	00001	"0000100000001" load 1
Load C	00010	"0001000000001" load 1
Add value to A	00011	"0011000000001" add 1
Subtract Value from A	00100	"001000000001" sub 1
NOT A	00101	"00101"
AND A	00110	"0011000000001" AND 1
OR A	00111	"0011100000001" OR 1
XOR A	01000	"010000000001" XOR 1
Output to display	01001	"01001"
Store A to RAM addr specified	01010	"010100000000" 0x00
Load RAM address to A	01011	"0101100000000" 0x00
Move C to A	01100	"01100"
Store Output to RAM addr specified	01101	"0110100000000" 0x00
Store C to RAM addr specified	01110	"011100000000" 0x00
Jump to Address	10000	"100000000000" 0x00
Clear A	10001	"10001"
Clear C	10010	"10010"
Multiply A	10111	"1011100000010" times 2
HALT	11111	(not implemented)