PIC32 CONFIGURATION SETTINGS

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PIC32 CONFIGURATION SETTINGS

Configuration Settings

This document lists the configuration settings available for each of the PIC32 MCU devices for use with MPLAB XC32's #pragma config directive.

32M4KCORE

PIC32MX110F016B

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| F | USBIDIO = OFF | Controlled by Port Function |
|---|---------------|------------------------------|
| F | USBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| · | |
|------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------|-----|

| WDTPS = PS2 | 1:2 |
|-------------------|-----------|
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| g | |
|--------------|-----------|
| PWP = PWP32K | First 32K |
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX110F016C

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |

| l | FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |
|---|----------------|----------------------------------|
| - | FNOSC = FRCDIV | Fast RC Osc w/Div-by-in (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| F | CKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|---|---------------|-------------------------------------|
| F | CKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| F | CKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|------------------|----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |

| WDTPS = PS262144 | 1:262144 |
|-------------------|-----------|
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS | = ON | Watchdog Timer is in Window Mode |
|--------|-------|--------------------------------------|
| WINDIS | = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| First 32K |
|-----------|
| First 31K |
| First 30K |
| First 29K |
| First 28K |
| First 27K |
| First 26K |
| First 25K |
| First 24K |
| First 23K |
| First 22K |
| First 21K |
| First 20K |
| First 19K |
| First 18K |
| First 17K |
| First 16K |
| |

| PWP = PWP14K First 15K PWP = PWP13K First 13K PWP = PWP12K First 12K PWP = PWP11K First 11K PWP = PWP10K First 10K PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP4K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K PWP = PWP1K First 1K PWP = OFF Disable | | |
|--|--------------|-----------|
| PWP = PWP13K First 13K PWP = PWP12K First 12K PWP = PWP11K First 11K PWP = PWP10K First 10K PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP15K | First 15K |
| PWP = PWP12K First 12K PWP = PWP11K First 11K PWP = PWP10K First 10K PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP14K | First 14K |
| PWP = PWP11K First 11K PWP = PWP10K First 10K PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP13K | First 13K |
| PWP = PWP10K First 10K PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP12K | First 12K |
| PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP11K | First 11K |
| PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP10K | First 10K |
| PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP9K | First 9K |
| PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP8K | First 8K |
| PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP7K | First 7K |
| PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP6K | First 6K |
| PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP5K | First 5K |
| PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP4K | First 4K |
| PWP = PWP1K First 1K | PWP = PWP3K | First 3K |
| | PWP = PWP2K | First 2K |
| PWP = OFF Disable | PWP = PWP1K | First 1K |
| | PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX110F016D

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| par =a | |
|------------------|------------|
| FPLLIDIV = DIV_1 | 1x Divider |
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |

| FPLLIDIV = DIV_10 | 10x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | |
|-------------------|-----------|
| WDIPS - PSI | 1:1 |
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| W | INDIS = ON | Watchdog Timer is in Window Mode |
|---|-------------|--------------------------------------|
| W | INDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

|--|

| DEBUG = OFF | Debugger is Disabled |
|-------------|----------------------|
|-------------|----------------------|

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|------------------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| <pre>ICESEL = ICS_PGx1</pre> | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| i rogram i lasti wille i rotet | /L. |
|--------------------------------|-----------|
| PWP = PWP32K | First 32K |
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

|--|

| BWP = OFF | Protection Disabled | |
|---------------|---------------------|--|
| Code Protect: | | |
| CP = ON | Protection Enabled | |
| CP = OFF | Protection Disabled | |

PIC32MX120F032B

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 PLL Divide by 1 FPLLODIV = DIV_2 PLL Divide by 2 FPLLODIV = DIV_4 PLL Divide by 4 FPLLODIV = DIV_8 PLL Divide by 8 FPLLODIV = DIV_16 PLL Divide by 16 FPLLODIV = DIV_32 PLL Divide by 32 | | |
|--|-------------------|------------------|
| FPLLODIV = DIV_4 PLL Divide by 4 FPLLODIV = DIV_8 PLL Divide by 8 FPLLODIV = DIV_16 PLL Divide by 16 | FPLLODIV = DIV_1 | PLL Divide by 1 |
| FPLLODIV = DIV_8 PLL Divide by 8 FPLLODIV = DIV_16 PLL Divide by 16 | FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_16 PLL Divide by 16 | FPLLODIV = DIV_4 | PLL Divide by 4 |
| | FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_32 PLL Divide by 32 | FPLLODIV = DIV_16 | PLL Divide by 16 |
| | FPLLODIV = DIV_32 | PLL Divide by 32 |

| FPLLODIV = DIV_64 | PLL Divide by 64 |
|--------------------|-------------------|
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| • | |
|---------------|----------------------|
| POSCMOD = EC | External clock mode |
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSC1 | OFNC = ON | Enabled |
|------|------------|----------|
| OSC1 | OFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|---------------|-------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |

| WDTPS = PS256 | 1:256 |
|-------------------|-----------|
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|------------------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| <pre>ICESEL = ICS_PGx1</pre> | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP32K | First 32K |
|--------------|-----------|
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |

| PWP = PWP25K | First 25K |
|--------------|-----------|
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP | = ON | Protection Enabled |
|----|-------|---------------------|
| СР | = OFF | Protection Disabled |

PIC32MX120F032C

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| Cycloin i == Calpat Cicon | 211.001 |
|---------------------------|-------------------|
| FPLLODIV = DIV_1 | PLL Divide by 1 |
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |
| | |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|

| IESO = ON Enabled | IESO = ON | Enabled |
|-------------------|-----------|---------|
|-------------------|-----------|---------|

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |
| | |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DI | EBUG = ON | Debugger is Enabled |
|----|------------|----------------------|
| DI | EBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP32K | First 32K |
|--------------|-----------|
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| | |

| PWP = PWP8K | First 8K |
|-------------|----------|
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX120F032D

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |

| FPLLMUL = MUL_19 | 19x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-----------------------------------|
|----------------|-----------------------------------|

| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
|----------------|-------------------------------------|
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| 1:1 |
|-----------|
| 1:2 |
| 1:4 |
| 1:8 |
| 1:16 |
| 1:32 |
| 1:64 |
| 1:128 |
| 1:256 |
| 1:512 |
| 1:1024 |
| 1:2048 |
| 1:4096 |
| 1:8192 |
| 1:16384 |
| 1:32768 |
| 1:65536 |
| 1:131072 |
| 1:262144 |
| 1:524288 |
| 1:1048576 |
| |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|-------------------|----------|
|-------------------|----------|

| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
|------------------------------|----------------------------|
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| <pre>ICESEL = ICS_PGx1</pre> | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| i rogiami i lashi wille i rote | .01. |
|--------------------------------|-----------|
| PWP = PWP32K | First 32K |
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX130F064B

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| Ι | OL1WAY = OFF | Allow multiple reconfigurations |
|---|--------------|---------------------------------|
| I | OL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| <u> </u> | |
|------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|----------------|----------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |

| FNOSC = PRI | Primary Osc (XT,HS,EC) |
|------------------|------------------------------------|
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|----------------|--------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |

| NUMBER DOOLOG | 1:8192 |
|-------------------|-----------|
| WDTPS = PS8192 | 1.0192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|------------------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| <pre>ICESEL = ICS_PGx1</pre> | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP32K | First 32K |
|--------------|-----------|
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |

| PWP = PWP20K | First 20K |
|--------------|-----------|
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX130F064C

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|------------------|------------|
|------------------|------------|

| FPLLIDIV = DIV_2 | 2x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|--------------|---------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |

| POSCMOD = OFF | Primary osc disabled |
|---------------|----------------------|
|---------------|----------------------|

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 |
|---|
| WDTPS = PS4 |
| WDTPS = PS8 1:8 WDTPS = PS16 1:16 WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS16 1:16 WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS8192 1:8192 |
| WDTPS = PS8192 1:8192 |
| 10.02 |
| WDTPS = PS16384 1:16384 |
| |
| WDTPS = PS32768 1:32768 |
| WDTPS = PS65536 1:65536 |
| WDTPS = PS131072 1:131072 |
| WDTPS = PS262144 1:262144 |
| WDTPS = PS524288 1:524288 |
| WDTPS = PS1048576 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|--------------------|
|----------------------|--------------------|

| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled | |
|-------------|----------------------|--|
| DEBUG = OFF | Debugger is Disabled | |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx4 | Communicate on PGEC4/PGED4 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| 1 Togram Flaon Willo Flor | |
|---------------------------|-------------|
| PWP = PWP32K | First 32K |
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| | |

| PWP = PWP2K | First 2K |
|-------------|----------|
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX130F064D

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| • | |
|-------------------|-------------|
| FPLLIDIV = DIV_1 | 1x Divider |
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------|-----|

| WDTPS = PS2 | 1:2 |
|-------------------|-----------|
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx4 | Communicate on PGEC4/PGED4 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| - 10 grunn 1 naon 111110 1 10 10 1 | |
|------------------------------------|-----------|
| PWP = PWP32K | First 32K |
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX150F128B

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |

| FNOSC = FRCDIV Fast RC Osc w/Div-by-N (FRCDIV) | |
|--|--|
|--|--|

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| F | CKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|---|---------------|-------------------------------------|
| F | CKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| F | CKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|------------------|----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |

| WDTPS = PS262144 | 1:262144 |
|-------------------|-----------|
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS | = ON | Watchdog Timer is in Window Mode |
|--------|-------|--------------------------------------|
| WINDIS | = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| | DEBUG = ON | Debugger is Enabled |
|---|-------------|----------------------|
| Ī | DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP32K | First 32K |
|--------------|-----------|
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |

| PWP = PWP15K | First 15K |
|--------------|-----------|
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX150F128C

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| par =a | |
|------------------|------------|
| FPLLIDIV = DIV_1 | 1x Divider |
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |

| FPLLIDIV = DIV_10 | 10x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| | |
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| W | INDIS = ON | Watchdog Timer is in Window Mode |
|---|-------------|--------------------------------------|
| W | INDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|------------|---------------------|

| DEBUG = OFF | Debugger is Disabled |
|-------------|----------------------|
|-------------|----------------------|

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx4 | Communicate on PGEC4/PGED4 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| Program Flash Write Protect: | | |
|------------------------------|-----------|--|
| PWP = PWP32K | First 32K | |
| PWP = PWP31K | First 31K | |
| PWP = PWP30K | First 30K | |
| PWP = PWP29K | First 29K | |
| PWP = PWP28K | First 28K | |
| PWP = PWP27K | First 27K | |
| PWP = PWP26K | First 26K | |
| PWP = PWP25K | First 25K | |
| PWP = PWP24K | First 24K | |
| PWP = PWP23K | First 23K | |
| PWP = PWP22K | First 22K | |
| PWP = PWP21K | First 21K | |
| PWP = PWP20K | First 20K | |
| PWP = PWP19K | First 19K | |
| PWP = PWP18K | First 18K | |
| PWP = PWP17K | First 17K | |
| PWP = PWP16K | First 16K | |
| PWP = PWP15K | First 15K | |
| PWP = PWP14K | First 14K | |
| PWP = PWP13K | First 13K | |
| PWP = PWP12K | First 12K | |
| PWP = PWP11K | First 11K | |
| PWP = PWP10K | First 10K | |
| PWP = PWP9K | First 9K | |
| PWP = PWP8K | First 8K | |
| PWP = PWP7K | First 7K | |
| PWP = PWP6K | First 6K | |
| PWP = PWP5K | First 5K | |
| PWP = PWP4K | First 4K | |
| PWP = PWP3K | First 3K | |
| PWP = PWP2K | First 2K | |
| PWP = PWP1K | First 1K | |
| PWP = OFF | Disable | |
| | | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|----------|--------------------|
| | |

| BWP = OFF | Protection Disabled | |
|---------------|---------------------|--|
| Code Protect: | | |
| CP = ON | Protection Enabled | |
| CP = OFF | Protection Disabled | |

PIC32MX150F128D

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 PLL Divide by 1 FPLLODIV = DIV_2 PLL Divide by 2 FPLLODIV = DIV_4 PLL Divide by 4 FPLLODIV = DIV_8 PLL Divide by 8 FPLLODIV = DIV_16 PLL Divide by 16 FPLLODIV = DIV_32 PLL Divide by 32 | | |
|--|-------------------|------------------|
| FPLLODIV = DIV_4 PLL Divide by 4 FPLLODIV = DIV_8 PLL Divide by 8 FPLLODIV = DIV_16 PLL Divide by 16 | FPLLODIV = DIV_1 | PLL Divide by 1 |
| FPLLODIV = DIV_8 PLL Divide by 8 FPLLODIV = DIV_16 PLL Divide by 16 | FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_16 PLL Divide by 16 | FPLLODIV = DIV_4 | PLL Divide by 4 |
| | FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_32 PLL Divide by 32 | FPLLODIV = DIV_16 | PLL Divide by 16 |
| | FPLLODIV = DIV_32 | PLL Divide by 32 |

| FPLLODIV = DIV_64 | PLL Divide by 64 |
|--------------------|-------------------|
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = 1 | EC | External clock mode |
|-------------|-----|----------------------|
| POSCMOD = 2 | XT | XT osc mode |
| POSCMOD = I | HS | HS osc mode |
| POSCMOD = 0 | OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|---------------|-------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |

| WDTPS = PS256 | 1:256 |
|-------------------|-----------|
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx4 | Communicate on PGEC4/PGED4 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP32K | First 32K |
|--------------|-----------|
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |

| PWP = PWP25K | First 25K |
|--------------|-----------|
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |
| | |

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX210F016B

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC Fast RC Osc (FRC) |
|-------------------------------|
|-------------------------------|

| FNOSC = FRCPLL | Fast RC Osc with PLL |
|------------------|------------------------------------|
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|----------------|--------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |

| WDTPS = PS4096 | 1:4096 |
|-------------------|-----------|
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| - | |
|--------------|-----------|
| PWP = PWP32K | First 32K |
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |

| PWP = PWP21K First 20K PWP = PWP19K First 19K PWP = PWP18K First 18K PWP = PWP17K First 17K PWP = PWP16K First 16K PWP = PWP15K First 15K PWP = PWP14K First 14K PWP = PWP13K First 13K PWP = PWP12K First 12K PWP = PWP1K First 11K PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP6K First 7K PWP = PWP5K First 6K PWP = PWP4K First 5K PWP = PWP3K First 4K PWP = PWP4K First 3K PWP = PWP1K First 3K PWP = PWP1K First 2K PWP = PWP1K First 1K PWP = PWP1K First 1K PWP = PWP1K First 1K | | |
|--|--------------|-----------|
| PWP = PWP19K First 19K PWP = PWP18K First 18K PWP = PWP17K First 17K PWP = PWP16K First 16K PWP = PWP15K First 15K PWP = PWP14K First 14K PWP = PWP13K First 13K PWP = PWP12K First 12K PWP = PWP11K First 11K PWP = PWP9K First 9K PWP = PWP8K First 9K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 2K | PWP = PWP21K | First 21K |
| PWP = PWP18K First 18K PWP = PWP17K First 17K PWP = PWP16K First 16K PWP = PWP15K First 15K PWP = PWP14K First 14K PWP = PWP13K First 13K PWP = PWP12K First 12K PWP = PWP11K First 11K PWP = PWP10K First 10K PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP4K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP1K First 2K PWP = PWP1K First 1K | PWP = PWP20K | First 20K |
| PWP = PWP17K First 17K PWP = PWP16K First 16K PWP = PWP15K First 15K PWP = PWP14K First 14K PWP = PWP13K First 13K PWP = PWP12K First 12K PWP = PWP11K First 11K PWP = PWP10K First 10K PWP = PWP8K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP4K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 3K PWP = PWP1K First 2K PWP = PWP1K First 1K | PWP = PWP19K | First 19K |
| PWP = PWP16K First 16K PWP = PWP15K First 15K PWP = PWP14K First 14K PWP = PWP13K First 13K PWP = PWP12K First 12K PWP = PWP11K First 11K PWP = PWP10K First 10K PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP4K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 2K PWP = PWP1K First 1K | PWP = PWP18K | First 18K |
| PWP = PWP15K First 15K PWP = PWP14K First 14K PWP = PWP13K First 13K PWP = PWP12K First 12K PWP = PWP11K First 11K PWP = PWP10K First 10K PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP17K | First 17K |
| PWP = PWP14K First 14K PWP = PWP13K First 13K PWP = PWP12K First 12K PWP = PWP11K First 11K PWP = PWP10K First 10K PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP16K | First 16K |
| PWP = PWP13K First 13K PWP = PWP12K First 12K PWP = PWP11K First 11K PWP = PWP10K First 10K PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP15K | First 15K |
| PWP = PWP12K First 12K PWP = PWP11K First 11K PWP = PWP10K First 10K PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP14K | First 14K |
| PWP = PWP11K First 11K PWP = PWP10K First 10K PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP13K | First 13K |
| PWP = PWP10K First 10K PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP12K | First 12K |
| PWP = PWP9K First 9K PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP11K | First 11K |
| PWP = PWP8K First 8K PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP10K | First 10K |
| PWP = PWP7K First 7K PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP9K | First 9K |
| PWP = PWP6K First 6K PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP8K | First 8K |
| PWP = PWP5K First 5K PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP7K | First 7K |
| PWP = PWP4K First 4K PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP6K | First 6K |
| PWP = PWP3K First 3K PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP5K | First 5K |
| PWP = PWP2K First 2K PWP = PWP1K First 1K | PWP = PWP4K | First 4K |
| PWP = PWP1K First 1K | PWP = PWP3K | First 3K |
| | PWP = PWP2K | First 2K |
| PWP = OFF Disable | PWP = PWP1K | First 1K |
| | PWP = OFF | Disable |

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX210F016C

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| 1x Divider |
|-------------|
| 2x Divider |
| 3x Divider |
| 4x Divider |
| 5x Divider |
| 6x Divider |
| 10x Divider |
| 12x Divider |
| |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|----------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |

| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
|------------------|----------------------------------|
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| Traterial granter recipied | |
|----------------------------|---------|
| WDTPS = PS1 | 1:1 |
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |

| WDTPS = PS32768 | 1:32768 |
|-------------------|-----------|
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| | JTAGEN = OFF | JTAG Disabled |
|---|--------------|-------------------|
| Ī | JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP32K | First 32K |
|--------------|-----------|
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |

| PWP = PWP18K | First 18K |
|--------------|-----------|
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |
| | |

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| C: | P = ON | Protection Enabled |
|----|---------|---------------------|
| C: | P = OFF | Protection Disabled |

PIC32MX210F016D

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|------------------|------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |

| FPLLIDIV = DIV_4 | 4x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled | |
|------------|----------|--|
| IESO = ON | Enabled | |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|------------------|----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |

| WDTPS = PS1048576 | 1:1048576 | |
|-------------------|-----------|--|
|-------------------|-----------|--|

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| | t 32K |
|-------------------|-------|
| | |
| PWP = PWP31K Firs | t 31K |
| PWP = PWP30K Firs | t 30K |
| PWP = PWP29K Firs | t 29K |
| PWP = PWP28K Firs | t 28K |
| PWP = PWP27K Firs | t 27K |
| PWP = PWP26K Firs | t 26K |
| PWP = PWP25K Firs | t 25K |
| PWP = PWP24K Firs | t 24K |
| PWP = PWP23K Firs | t 23K |
| PWP = PWP22K Firs | t 22K |
| PWP = PWP21K Firs | t 21K |
| PWP = PWP20K Firs | t 20K |
| PWP = PWP19K Firs | t 19K |
| PWP = PWP18K Firs | t 18K |
| PWP = PWP17K Firs | t 17K |
| PWP = PWP16K Firs | t 16K |
| PWP = PWP15K Firs | t 15K |
| PWP = PWP14K Firs | t 14K |

| PWP = PWP13K | First 13K |
|--------------|-----------|
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX220F032B

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| | 211191911 |
|--------------------|-------------------|
| FPLLODIV = DIV_1 | PLL Divide by 1 |
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|

| IESO = ON Enabled | IESO = ON | Enabled |
|-------------------|-----------|---------|
|-------------------|-----------|---------|

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| 0 | SCIOFNC = ON | Enabled |
|---|---------------|----------|
| 0 | SCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 | |
|-------------------|-----------|--|
| WDTPS = PS2 | 1:2 | |
| WDTPS = PS4 | 1:4 | |
| WDTPS = PS8 | 1:8 | |
| WDTPS = PS16 | 1:16 | |
| WDTPS = PS32 | 1:32 | |
| WDTPS = PS64 | 1:64 | |
| WDTPS = PS128 | 1:128 | |
| WDTPS = PS256 | 1:256 | |
| WDTPS = PS512 | 1:512 | |
| WDTPS = PS1024 | 1:1024 | |
| WDTPS = PS2048 | 1:2048 | |
| WDTPS = PS4096 | 1:4096 | |
| WDTPS = PS8192 | 1:8192 | |
| WDTPS = PS16384 | 1:16384 | |
| WDTPS = PS32768 | 1:32768 | |
| WDTPS = PS65536 | 1:65536 | |
| WDTPS = PS131072 | 1:131072 | |
| WDTPS = PS262144 | 1:262144 | |
| WDTPS = PS524288 | 1:524288 | |
| WDTPS = PS1048576 | 1:1048576 | |
| | | |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DI | EBUG = ON | Debugger is Enabled |
|----|------------|----------------------|
| DI | EBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAG | EN = OFF | JTAG Disabled |
|------|----------|-------------------|
| JTAG | EN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP32K | First 32K |
|--------------|-----------|
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| | |

| PWP = PWP8K | First 8K |
|-------------|----------|
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX220F032C

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| · | |
|------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |

| FPLLMUL = MUL_19 | 19x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| Ţ | JPLLEN = ON | Enabled |
|---|--------------|-----------------------|
| Ţ | JPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| - 7 | |
|--------------------|-------------------|
| FPLLODIV = DIV_1 | PLL Divide by 1 |
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|--------------|---------------------|
| POSCMOD = XT | XT osc mode |

| POSCMOD = HS | HS osc mode |
|---------------|----------------------|
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP32K | First 32K |
|--------------|-----------|
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |

| PWP = PWP4K | First 4K |
|-------------|----------|
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX220F032D

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|---------------|---------|
|---------------|---------|

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| 00010110 011 | 2.000.00 |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| Tratoriand Timer Tootsouler: | | |
|------------------------------|-----------|--|
| WDTPS = PS1 | 1:1 | |
| WDTPS = PS2 | 1:2 | |
| WDTPS = PS4 | 1:4 | |
| WDTPS = PS8 | 1:8 | |
| WDTPS = PS16 | 1:16 | |
| WDTPS = PS32 | 1:32 | |
| WDTPS = PS64 | 1:64 | |
| WDTPS = PS128 | 1:128 | |
| WDTPS = PS256 | 1:256 | |
| WDTPS = PS512 | 1:512 | |
| WDTPS = PS1024 | 1:1024 | |
| WDTPS = PS2048 | 1:2048 | |
| WDTPS = PS4096 | 1:4096 | |
| WDTPS = PS8192 | 1:8192 | |
| WDTPS = PS16384 | 1:16384 | |
| WDTPS = PS32768 | 1:32768 | |
| WDTPS = PS65536 | 1:65536 | |
| WDTPS = PS131072 | 1:131072 | |
| WDTPS = PS262144 | 1:262144 | |
| WDTPS = PS524288 | 1:524288 | |
| WDTPS = PS1048576 | 1:1048576 | |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| Flogram Flash Write Flotect. | | |
|------------------------------|-----------|--|
| PWP = PWP32K | First 32K | |
| PWP = PWP31K | First 31K | |
| PWP = PWP30K | First 30K | |
| PWP = PWP29K | First 29K | |
| PWP = PWP28K | First 28K | |
| PWP = PWP27K | First 27K | |
| PWP = PWP26K | First 26K | |
| PWP = PWP25K | First 25K | |
| PWP = PWP24K | First 24K | |
| PWP = PWP23K | First 23K | |
| PWP = PWP22K | First 22K | |
| PWP = PWP21K | First 21K | |
| PWP = PWP20K | First 20K | |
| PWP = PWP19K | First 19K | |
| PWP = PWP18K | First 18K | |
| PWP = PWP17K | First 17K | |
| PWP = PWP16K | First 16K | |
| PWP = PWP15K | First 15K | |
| PWP = PWP14K | First 14K | |
| PWP = PWP13K | First 13K | |
| PWP = PWP12K | First 12K | |
| PWP = PWP11K | First 11K | |
| PWP = PWP10K | First 10K | |
| PWP = PWP9K | First 9K | |
| PWP = PWP8K | First 8K | |
| PWP = PWP7K | First 7K | |
| PWP = PWP6K | First 6K | |
| PWP = PWP5K | First 5K | |
| PWP = PWP4K | First 4K | |
| PWP = PWP3K | First 3K | |
| PWP = PWP2K | First 2K | |
| PWP = PWP1K | First 1K | |
| PWP = OFF | Disable | |
| | | |

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX230F064B

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|------------------|------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |

| UPLLIDIV = DIV_5 | 5x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IES | O = OFF | Disabled |
|-----|---------|----------|
| IES | O = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |

| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 | |
|----------------|---------------------|--|
|----------------|---------------------|--|

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| Material of Transfer | | |
|---|-----------|--|
| WDTPS = PS1 | 1:1 | |
| WDTPS = PS2 | 1:2 | |
| WDTPS = PS4 | 1:4 | |
| WDTPS = PS8 | 1:8 | |
| WDTPS = PS16 | 1:16 | |
| WDTPS = PS32 | 1:32 | |
| WDTPS = PS64 | 1:64 | |
| WDTPS = PS128 | 1:128 | |
| WDTPS = PS256 | 1:256 | |
| WDTPS = PS512 | 1:512 | |
| WDTPS = PS1024 | 1:1024 | |
| WDTPS = PS2048 | 1:2048 | |
| WDTPS = PS4096 | 1:4096 | |
| WDTPS = PS8192 | 1:8192 | |
| WDTPS = PS16384 | 1:16384 | |
| WDTPS = PS32768 | 1:32768 | |
| WDTPS = PS65536 | 1:65536 | |
| WDTPS = PS131072 | 1:131072 | |
| WDTPS = PS262144 | 1:262144 | |
| WDTPS = PS524288 | 1:524288 | |
| WDTPS = PS1048576 | 1:1048576 | |
| | | |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|---------------|
|--------------|---------------|

| JTAGEN = ON | JTAG Port Enabled | |
|-------------|-------------------|--|
|-------------|-------------------|--|

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| 1 Togram Tiash Write Trote | |
|----------------------------|-----------|
| PWP = PWP32K | First 32K |
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = | ON | Protection Enabled |
|-------|-----|---------------------|
| BWP = | OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|---------|--------------------|
| | |

| CP = OFF Protection Disabled |
|------------------------------|
|------------------------------|

PIC32MX230F064C

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| <u> </u> | |
|-------------------|-------------|
| UPLLIDIV = DIV_1 | 1x Divider |
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |

| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |
|----------------|-------------------------------------|
|----------------|-------------------------------------|

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx4 | Communicate on PGEC4/PGED4 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |

| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| 3 | |
|--------------|-----------|
| PWP = PWP32K | First 32K |
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX230F064D

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------|-----|

| WDTPS = PS2 | 1:2 |
|-------------------|-----------|
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx4 | Communicate on PGEC4/PGED4 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| i rogiami i lasm write i roted | , t. |
|--------------------------------|-----------|
| PWP = PWP32K | First 32K |
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX250F128B

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|------------------|-----------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |

| FPLLODIV = DIV_8 | PLL Divide by 8 |
|--------------------|-------------------|
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|--------------|------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |

| WDTPS = PS32 | 1:32 |
|-------------------|-----------|
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = RESERVED | Reserved |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP32K | First 32K |
|--------------|-----------|
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |

| PWP = PWP28K | First 28K |
|--------------|-----------|
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX250F128C

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|-------------------|------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |

| FPLLODIV = DIV_256 | PLL Divide by 256 | |
|--------------------|-------------------|--|
|--------------------|-------------------|--|

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| <u> </u> | |
|----------------|----------|
| OSCIOFNC = ON | Enabled |
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| 1:1 |
|-------|
| 1:2 |
| 1:4 |
| 1:8 |
| 1:16 |
| 1:32 |
| 1:64 |
| 1:128 |
| 1:256 |
| |

| WDTPS = PS512 | 1:512 |
|-------------------|-----------|
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx4 | Communicate on PGEC4/PGED4 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP32K | First 32K |
|--------------|-----------|
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |

| PWP = PWP24K | First 24K |
|--------------|-----------|
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |
| PWP = PWP19K | First 19K |
| PWP = PWP18K | First 18K |
| PWP = PWP17K | First 17K |
| PWP = PWP16K | First 16K |
| PWP = PWP15K | First 15K |
| PWP = PWP14K | First 14K |
| PWP = PWP13K | First 13K |
| PWP = PWP12K | First 12K |
| PWP = PWP11K | First 11K |
| PWP = PWP10K | First 10K |
| PWP = PWP9K | First 9K |
| PWP = PWP8K | First 8K |
| PWP = PWP7K | First 7K |
| PWP = PWP6K | First 6K |
| PWP = PWP5K | First 5K |
| PWP = PWP4K | First 4K |
| PWP = PWP3K | First 3K |
| PWP = PWP2K | First 2K |
| PWP = PWP1K | First 1K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX250F128D

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = ON | Controlled by USB Module |
|----------------|--------------------------|
|----------------|--------------------------|

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| | UPLLEN = ON | Enabled |
|---|--------------|-----------------------|
| Ī | UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|----------------|------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |

| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
|------------------|------------------------------------|
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|----------------|--------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |

| WDTPS = PS16384 | 1:16384 |
|-------------------|-----------|
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx4 | Communicate on PGEC4/PGED4 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP32K | First 32K |
|--------------|-----------|
| PWP = PWP31K | First 31K |
| PWP = PWP30K | First 30K |
| PWP = PWP29K | First 29K |
| PWP = PWP28K | First 28K |
| PWP = PWP27K | First 27K |
| PWP = PWP26K | First 26K |
| PWP = PWP25K | First 25K |
| PWP = PWP24K | First 24K |
| PWP = PWP23K | First 23K |
| PWP = PWP22K | First 22K |
| PWP = PWP21K | First 21K |
| PWP = PWP20K | First 20K |

| First 19K |
|-----------|
| First 18K |
| First 17K |
| First 16K |
| First 15K |
| First 14K |
| First 13K |
| First 12K |
| First 11K |
| First 10K |
| First 9K |
| First 8K |
| First 7K |
| First 6K |
| First 5K |
| First 4K |
| First 3K |
| First 2K |
| First 1K |
| Disable |
| |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX320F032H

PLL Input Divider:

| 1x Divider |
|-------------|
| 2x Divider |
| 3x Divider |
| 4x Divider |
| 5x Divider |
| 6x Divider |
| 10x Divider |
| 12x Divider |
| |

PLL Multiplier:

| · == | |
|------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |

| FPLLMUL = MUL_24 | 24x Multiplier |
|------------------|----------------|
|------------------|----------------|

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| 1:1 |
|-----------|
| 1:2 |
| 1:4 |
| 1:8 |
| 1:16 |
| 1:32 |
| 1:64 |
| 1:128 |
| 1:256 |
| 1:512 |
| 1:1024 |
| 1:2048 |
| 1:4096 |
| 1:8192 |
| 1:16384 |
| 1:32768 |
| 1:65536 |
| 1:131072 |
| 1:262144 |
| 1:524288 |
| 1:1048576 |
| |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled | |
|-------------|----------------------|--|
| DEBUG = OFF | Debugger is disabled | |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |

Configuration Settings

| PWP = PWP460K | First 460K |
|---------------|------------|
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| | |

Configuration Settings

| | T= |
|---------------|---------------------------------------|
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP212K | First 208K |
| PWP = PWP200K | First 204K |
| PWP = PWP204K | First 200K |
| | First 196K |
| PWP = PWP196K | First 192K |
| PWP = PWP192K | |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
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| PWP = PWP84K | First 84K |
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| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX320F064H

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |

| FPLLMUL = MUL_20 | 20x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOS | SCEN = OFF | Disabled |
|------|------------|----------|
| FSOS | SCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |

| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |
|----------------|-------------------------------------|
|----------------|-------------------------------------|

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |

Configuration Settings

| PWP = PWP464K First 468K PWP = PWP460K First 460K PWP = PWP456K First 456K PWP = PWP452K First 456K PWP = PWP448K First 448K PWP = PWP440K First 444K PWP = PWP436K First 440K PWP = PWP432K First 430K PWP = PWP432K First 432K PWP = PWP428K First 420K PWP = PWP420K First 416K PWP = PWP410K First 410K PWP = PWP410K First 410K PWP = PWP40BK First 40K PWP = PWP40BK First 40K PWP = PWP40BK First 40K PWP = PWP396K First 40K PWP = PWP396K First 39CK PWP = PWP398K First 39CK PWP = PWP398K First 38K PWP = PWP398K First 38K PWP = PWP398K First 38K PWP = PWP38AK First 38K PWP | | |
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| PWP = PWP376K First 376K PWP = PWP372K First 372K PWP = PWP368K First 368K PWP = PWP364K First 364K PWP = PWP360K First 360K PWP = PWP356K First 356K PWP = PWP352K First 352K PWP = PWP344K First 344K PWP = PWP344K First 344K PWP = PWP336K First 336K PWP = PWP332K First 332K PWP = PWP328K First 324K PWP = PWP324K First 324K PWP = PWP316K First 316K PWP = PWP312K First 316K PWP = PWP308K First 308K PWP = PWP300K First 300K PWP = PWP300K First 300K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP384K | First 384K |
| PWP = PWP372K First 372K PWP = PWP368K First 368K PWP = PWP364K First 364K PWP = PWP360K First 360K PWP = PWP356K First 356K PWP = PWP352K First 352K PWP = PWP344K First 344K PWP = PWP344K First 340K PWP = PWP336K First 336K PWP = PWP332K First 328K PWP = PWP328K First 328K PWP = PWP324K First 324K PWP = PWP320K First 320K PWP = PWP316K First 316K PWP = PWP312K First 312K PWP = PWP308K First 308K PWP = PWP300K First 304K | PWP = PWP380K | First 380K |
| PWP = PWP368K First 364K PWP = PWP364K First 364K PWP = PWP360K First 360K PWP = PWP356K First 356K PWP = PWP352K First 352K PWP = PWP344K First 344K PWP = PWP340K First 340K PWP = PWP336K First 336K PWP = PWP332K First 332K PWP = PWP328K First 324K PWP = PWP324K First 324K PWP = PWP316K First 316K PWP = PWP316K First 316K PWP = PWP308K First 30K PWP = PWP300K First 30K PWP = PWP300K First 30K PWP = PWP300K First 30K PWP = PWP296K First 296K PWP = PWP288K First 292K PWP = PWP288K First 288K | PWP = PWP376K | First 376K |
| PWP = PWP364K First 364K PWP = PWP360K First 360K PWP = PWP356K First 356K PWP = PWP352K First 352K PWP = PWP344K First 344K PWP = PWP340K First 340K PWP = PWP336K First 336K PWP = PWP322K First 332K PWP = PWP328K First 324K PWP = PWP320K First 324K PWP = PWP316K First 316K PWP = PWP312K First 312K PWP = PWP308K First 308K PWP = PWP304K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP288K First 292K PWP = PWP288K First 288K | PWP = PWP372K | First 372K |
| PWP = PWP360K First 360K PWP = PWP356K First 356K PWP = PWP352K First 352K PWP = PWP344K First 348K PWP = PWP344K First 344K PWP = PWP336K First 340K PWP = PWP332K First 332K PWP = PWP322K First 328K PWP = PWP324K First 324K PWP = PWP320K First 316K PWP = PWP316K First 316K PWP = PWP312K First 30K PWP = PWP308K First 308K PWP = PWP300K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP368K | First 368K |
| PWP = PWP356K First 356K PWP = PWP352K First 352K PWP = PWP348K First 348K PWP = PWP344K First 344K PWP = PWP340K First 340K PWP = PWP336K First 336K PWP = PWP322K First 322K PWP = PWP324K First 324K PWP = PWP320K First 320K PWP = PWP316K First 316K PWP = PWP312K First 312K PWP = PWP308K First 308K PWP = PWP300K First 300K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP288K First 288K | PWP = PWP364K | First 364K |
| PWP = PWP352K First 352K PWP = PWP348K First 348K PWP = PWP344K First 344K PWP = PWP340K First 340K PWP = PWP336K First 336K PWP = PWP332K First 332K PWP = PWP328K First 328K PWP = PWP324K First 324K PWP = PWP320K First 320K PWP = PWP316K First 316K PWP = PWP312K First 312K PWP = PWP308K First 308K PWP = PWP304K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP360K | First 360K |
| PWP = PWP348K First 348K PWP = PWP344K First 344K PWP = PWP340K First 340K PWP = PWP336K First 336K PWP = PWP332K First 332K PWP = PWP328K First 328K PWP = PWP324K First 324K PWP = PWP320K First 320K PWP = PWP316K First 316K PWP = PWP312K First 312K PWP = PWP308K First 308K PWP = PWP304K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP356K | First 356K |
| PWP = PWP344K First 344K PWP = PWP340K First 340K PWP = PWP336K First 336K PWP = PWP322K First 332K PWP = PWP328K First 328K PWP = PWP324K First 324K PWP = PWP316K First 316K PWP = PWP312K First 312K PWP = PWP308K First 308K PWP = PWP304K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP352K | First 352K |
| PWP = PWP340K First 340K PWP = PWP336K First 336K PWP = PWP322K First 332K PWP = PWP328K First 328K PWP = PWP324K First 324K PWP = PWP320K First 320K PWP = PWP316K First 316K PWP = PWP312K First 312K PWP = PWP308K First 308K PWP = PWP304K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP348K | First 348K |
| PWP = PWP336K First 336K PWP = PWP322K First 332K PWP = PWP328K First 328K PWP = PWP324K First 324K PWP = PWP320K First 320K PWP = PWP316K First 316K PWP = PWP312K First 312K PWP = PWP308K First 308K PWP = PWP304K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP344K | First 344K |
| PWP = PWP332K First 332K PWP = PWP328K First 328K PWP = PWP324K First 324K PWP = PWP320K First 320K PWP = PWP316K First 316K PWP = PWP312K First 312K PWP = PWP308K First 308K PWP = PWP304K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP340K | First 340K |
| PWP = PWP328K First 328K PWP = PWP324K First 324K PWP = PWP320K First 320K PWP = PWP316K First 316K PWP = PWP312K First 312K PWP = PWP308K First 308K PWP = PWP304K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP336K | First 336K |
| PWP = PWP324K First 324K PWP = PWP320K First 320K PWP = PWP316K First 316K PWP = PWP312K First 312K PWP = PWP308K First 308K PWP = PWP304K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP332K | First 332K |
| PWP = PWP320K First 320K PWP = PWP316K First 316K PWP = PWP312K First 312K PWP = PWP308K First 308K PWP = PWP304K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP328K | First 328K |
| PWP = PWP316K First 316K PWP = PWP312K First 312K PWP = PWP308K First 308K PWP = PWP304K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP324K | First 324K |
| PWP = PWP312K First 312K PWP = PWP308K First 308K PWP = PWP304K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP320K | First 320K |
| PWP = PWP308K First 308K PWP = PWP304K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP316K | First 316K |
| PWP = PWP304K First 304K PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP312K | First 312K |
| PWP = PWP300K First 300K PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP308K | First 308K |
| PWP = PWP296K First 296K PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP304K | First 304K |
| PWP = PWP292K First 292K PWP = PWP288K First 288K | PWP = PWP300K | First 300K |
| PWP = PWP288K First 288K | PWP = PWP296K | First 296K |
| | PWP = PWP292K | First 292K |
| PWP = PWP284K First 284K | PWP = PWP288K | First 288K |
| | PWP = PWP284K | First 284K |

Configuration Settings

| PWP = PWP280K | First 280K |
|---------------|------------|
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| | First 244K |
| PWP = PWP244K | |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| | First 160K |
| PWP = PWP160K | |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| | |

| PWP = PWP92K | First 92K |
|--------------|-----------|
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX320F128H

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |

| FPLLMUL = MUL_18 | 18x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |
| | |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |

Configuration Settings

| PWP = PWP480K | First 480K |
|---------------|------------|
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| | • |

| | I=: |
|---------------|------------|
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |

| PWP = PV | WP104K | First 104K |
|----------|--------|------------|
| PWP = PV | WP100K | First 100K |
| PWP = PV | WP96K | First 96K |
| PWP = PV | WP92K | First 92K |
| PWP = PI | WP88K | First 88K |
| PWP = PI | WP84K | First 84K |
| PWP = PV | WP80K | First 80K |
| PWP = PV | WP76K | First 76K |
| PWP = PV | WP72K | First 72K |
| PWP = PV | WP68K | First 68K |
| PWP = PV | WP64K | First 64K |
| PWP = PV | WP60K | First 60K |
| PWP = PV | WP56K | First 56K |
| PWP = PV | WP52K | First 52K |
| PWP = PV | WP48K | First 48K |
| PWP = PV | WP44K | First 44K |
| PWP = PV | WP40K | First 40K |
| PWP = PV | WP36K | First 36K |
| PWP = PV | WP32K | First 32K |
| PWP = PV | WP28K | First 28K |
| PWP = PV | WP24K | First 24K |
| PWP = PV | WP20K | First 20K |
| PWP = PV | WP16K | First 16K |
| PWP = PV | WP12K | First 12K |
| PWP = PV | WP8K | First 8K |
| PWP = PV | WP4K | First 4K |
| PWP = O | FF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX320F128L

PLL Input Divider:

| • | |
|-------------------|-------------|
| FPLLIDIV = DIV_1 | 1x Divider |
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| ļ | FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|---|----------------|---------------------|

| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
|----------------|---------------------|
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |

| PWP = PWP492K | First 492K |
|----------------|--------------|
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| | |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| | |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| I MI - EMEDOOV | 1 1130 30010 |

| PWP = PW | IP304K | First 304K |
|-------------|-----------|-------------|
| PWP = PW | IP300K | First 300K |
| PWP = PW | JP296K | First 296K |
| PWP = PW | JP292K | First 292K |
| PWP = PW | JP288K | First 288K |
| PWP = PW | IP284K | First 284K |
| PWP = PW | JP280K | First 280K |
| PWP = PW | | First 276K |
| PWP = PW | - | First 272K |
| | | First 268K |
| PWP = PW | | |
| PWP = PW | | First 264K |
| PWP = PW | | First 260K |
| PWP = PW | IP256K | First 256K |
| PWP = PW | IP252K | First 252K |
| PWP = PW | JP248K | First 248K |
| PWP = PW | JP244K | First 244K |
| PWP = PW | JP240K | First 240K |
| PWP = PW | JP236K | First 236K |
| PWP = PW | JP232K | First 232K |
| PWP = PW | JP228K | First 228K |
| PWP = PW | JP224K | First 224K |
| PWP = PW | JP220K | First 220K |
| PWP = PW | JP216K | First 216K |
| PWP = PW | | First 212K |
| PWP = PW | | First 208K |
| PWP = PW | | First 204K |
| PWP = PW | | First 200K |
| PWP = PW | | First 196K |
| PWP = PW | | First 192K |
| PWP = PW | | First 188K |
| | | |
| PWP = PW | | First 184K |
| PWP = PW | | First 180K |
| PWP = PW | | First 176K |
| PWP = PW | | First 172K |
| PWP = PW | JP168K | First 168K |
| PWP = PW | IP164K | First 164K |
| PWP = PW | JP160K | First 160K |
| PWP = PW | IP156K | First 156K |
| PWP = PW | JP152K | First 152K |
| PWP = PW | JP148K | First 148K |
| PWP = PW | JP144K | First 144K |
| PWP = PW | JP140K | First 140K |
| PWP = PW | JP136K | First 136K |
| PWP = PW | JP132K | First 132K |
| PWP = PW | | First 128K |
| PWP = PW | | First 124K |
| PWP = PW | | First 120K |
| T VVI — PVV | 11 12 010 | 11100 12010 |

| PWP = PWP116K | First 116K |
|---------------|------------|
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX330F064H

Shadow Register Set Priority Select:

| onaaon nogiotoi ootii nom | ., |
|---------------------------|----------------|
| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| | |

| FSRSSEL = PRIORITY_7 | SRS Priority 7 | |
|----------------------|----------------|--|
|----------------------|----------------|--|

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| Ι | OL1WAY = OFF | Allow multiple reconfigurations |
|---|--------------|---------------------------------|
| I | OL1WAY = ON | Allow only one reconfiguration |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled | |
|------------|----------|--|
| IESO = ON | Enabled | |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| Watchidog Tillier Postscale | l e |
|-----------------------------|----------|
| WDTPS = PS1 | 1:1 |
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| | |

| WDTPS = PS1048576 | 1:1048576 | |
|-------------------|-----------|--|
|-------------------|-----------|--|

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |

| PWP = PWP432K | First 432K |
|---------------|------------|
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| | 1 |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| | First 312K |
| PWP = PWP312K | |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| | · · |

| | E: |
|---------------|------------|
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| L | I |

| PWP = PWP56K | First 56K |
|--------------|-----------|
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX330F064L

Shadow Register Set Priority Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|------------------|------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |

| FPLLIDIV = DIV_6 | 6x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|---------------|---------|
|---------------|---------|

| OSCIOFNC = OFF Disabled | OSCIOFNC = OFF Disabled | |
|-------------------------|-------------------------|--|
|-------------------------|-------------------------|--|

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | | | |
|---|-------------------|-----------|--|
| WDTPS = PS4 | WDTPS = PS1 | 1:1 | |
| WDTPS = PS8 | WDTPS = PS2 | 1:2 | |
| WDTPS = PS16 | WDTPS = PS4 | 1:4 | |
| WDTPS = PS32 | WDTPS = PS8 | 1:8 | |
| WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 WDTPS = PS524288 1:524288 | WDTPS = PS16 | 1:16 | |
| WDTPS = PS128 | WDTPS = PS32 | 1:32 | |
| WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS64 | 1:64 | |
| WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS128 | 1:128 | |
| WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS256 | 1:256 | |
| WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS512 | 1:512 | |
| WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS1024 | 1:1024 | |
| WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS2048 | 1:2048 | |
| WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS4096 | 1:4096 | |
| WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS8192 | 1:8192 | |
| WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS16384 | 1:16384 | |
| WDTPS = PS131072 | WDTPS = PS32768 | 1:32768 | |
| WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS65536 | 1:65536 | |
| WDTPS = PS524288 1:524288 | WDTPS = PS131072 | 1:131072 | |
| | WDTPS = PS262144 | 1:262144 | |
| WDTPS = PS1048576 1:1048576 | WDTPS = PS524288 | 1:524288 | |
| | WDTPS = PS1048576 | 1:1048576 | |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| | ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
|---|------------------------------|----------------------------|
| | ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| Ī | <pre>ICESEL = ICS_PGx1</pre> | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| Program Flash Write Prote | <u> </u> |
|---------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| | |

| PWP = PWP37 | 2K | First 372K |
|--------------|----|------------|
| PWP = PWP36 | 8K | First 368K |
| PWP = PWP36 | 4K | First 364K |
| PWP = PWP36 | 0K | First 360K |
| PWP = PWP35 | 6K | First 356K |
| PWP = PWP35 | 2K | First 352K |
| PWP = PWP34 | 8K | First 348K |
| PWP = PWP34 | 4K | First 344K |
| PWP = PWP34 | 0K | First 340K |
| PWP = PWP33 | 6K | First 336K |
| PWP = PWP33 | 2K | First 332K |
| PWP = PWP32 | | First 328K |
| PWP = PWP32 | | First 324K |
| PWP = PWP32 | | First 320K |
| PWP = PWP31 | | First 316K |
| PWP = PWP31 | | First 312K |
| PWP = PWP31 | | First 308K |
| PWP = PWP30 | | First 304K |
| | | |
| PWP = PWP30 | | First 300K |
| PWP = PWP29 | | First 296K |
| PWP = PWP292 | | First 292K |
| PWP = PWP28 | | First 288K |
| PWP = PWP28 | | First 284K |
| PWP = PWP28 | | First 280K |
| PWP = PWP27 | | First 276K |
| PWP = PWP27 | | First 272K |
| PWP = PWP26 | 8K | First 268K |
| PWP = PWP26 | 4K | First 264K |
| PWP = PWP26 | 0K | First 260K |
| PWP = PWP25 | 6K | First 256K |
| PWP = PWP25 | 2K | First 252K |
| PWP = PWP24 | 8K | First 248K |
| PWP = PWP24 | 4K | First 244K |
| PWP = PWP24 | 0K | First 240K |
| PWP = PWP23 | 6K | First 236K |
| PWP = PWP23 | 2K | First 232K |
| PWP = PWP22 | 8K | First 228K |
| PWP = PWP22 | 4K | First 224K |
| PWP = PWP22 | 0K | First 220K |
| PWP = PWP21 | 6K | First 216K |
| PWP = PWP21 | 2K | First 212K |
| PWP = PWP20 | 8K | First 208K |
| PWP = PWP20 | 4K | First 204K |
| PWP = PWP20 | 0K | First 200K |
| PWP = PWP19 | 6K | First 196K |
| PWP = PWP19 | 2K | First 192K |
| PWP = PWP18 | 8K | First 188K |
| L | | |

| PWP = PWP184K | First 184K |
|---------------|------------|
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| | First 148K |
| PWP = PWP148K | |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP70K | First 72K |
| | First 68K |
| PWP = PWP68K | |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| I WI - OLI | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX340F128H

PLL Input Divider:

| • | |
|-------------------|-------------|
| FPLLIDIV = DIV_1 | 1x Divider |
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled | |
|------------|----------|--|
| IESO = ON | Enabled | |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| valchdog Tiller Fostscaler. | |
|-----------------------------|--|
| 1:1 | |
| 1:2 | |
| 1:4 | |
| 1:8 | |
| 1:16 | |
| 1:32 | |
| 1:64 | |
| 1:128 | |
| 1:256 | |
| 1:512 | |
| 1:1024 | |
| 1:2048 | |
| 1:4096 | |
| 1:8192 | |
| 1:16384 | |
| 1:32768 | |
| 1:65536 | |
| 1:131072 | |
| 1:262144 | |
| 1:524288 | |
| | |

| WDTPS = PS1048576 | 1:1048576 | |
|-------------------|-----------|--|
|-------------------|-----------|--|

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBU | JG = ON | Debugger is enabled |
|------|----------|----------------------|
| DEBU | JG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| i rogiami riasii wiite i rote | ··· |
|-------------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| · | |

| | I = 1 1 1 |
|---------------|------------|
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |

| | T=1 |
|---------------|------------|
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| | |

| DWD = OFF | Disable | |
|-----------|-----------|--|
| PWP = OFF | l Disable | |
| | | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX340F128L

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |

| FNOSC = FRCDIV Fast RC Osc w/Div-by-N (FRCDIV) | |
|--|--|
|--|--|

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| 1:1 |
|----------|
| 1:2 |
| 1:4 |
| 1:8 |
| 1:16 |
| 1:32 |
| 1:64 |
| 1:128 |
| 1:256 |
| 1:512 |
| 1:1024 |
| 1:2048 |
| 1:4096 |
| 1:8192 |
| 1:16384 |
| 1:32768 |
| 1:65536 |
| 1:131072 |
| |

| WDTPS = PS262144 | 1:262144 |
|-------------------|-----------|
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| - 109.0 | |
|---------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| | |

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|--------------------------------|--------------------------|
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 233K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 224K |
| PWP = PWP224K | First 224K First 220K |
| PWP = PWP220K | |
| PWP = PWP216K PWP = PWP212K | First 216K First 212K |
| PWP = PWP212K PWP = PWP208K | First 208K |
| | First 204K |
| PWP = PWP204K PWP = PWP200K | First 200K |
| TWE - FWEZUUN | 1 HOL ZOOK |

| PMP = PMP196K |
|---|
| PWP PWP188K |
| PWP = PWP184K First 184K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP164K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP144K First 140K PWP = PWP136K First 132K PWP = PWP128K First 132K PWP = PWP128K First 124K PWP = PWP128K First 124K PWP = PWP120K First 120K PWP = PWP100K First 116K PWP = PWP100K First 10K PWP = PWP100K First 10K PWP = PWP100K First 10K PWP = PWP100K First 96K PWP = PWP38A First 80K PWP = PWP30K First 80K PWP = PWP46K First 80K PWP = PWP66K First 66K PWP = PWP68K First 66K PWP = PWP |
| PWP |
| PWP = PWP176K First 176K PWP = PWP168K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 132K PWP = PWP132K First 132K PWP = PWP128K First 124K PWP = PWP124K First 120K PWP = PWP120K First 110K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP104K First 108K PWP = PWP24K First 88K PWP = PWP34K First 88K PWP = PWP64K First 86K P |
| PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 144K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 124K PWP = PWP120K First 120K PWP = PWP112K First 110K PWP = PWP112K First 110K PWP = PWP10AK First 104K PWP = PWP10AK First 100K PWP = PWP10AK First 10K PWP = PWP10AK First 10K PWP = PWP2AK First 80K PWP = PWP3AK First 80K PWP = PWP3AK First 80K PWP |
| PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP104K First 104K PWP = PWP104K First 100K PWP = PWP104K First 100K PWP = PWP20K First 96K PWP = PWP30K First 84K PWP = PWP80K First 84K PWP = PWP80K First 80K PWP = PWP60K First 76K PWP = PWP60K First 72K PWP = PWP60K First 60K |
| PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP104K First 104K PWP = PWP104K First 100K PWP = PWP96K First 96K PWP = PWP98K First 96K PWP = PWP88K First 84K PWP = PWP80K First 80K PWP = PWP80K First 80K PWP = PWP68K First 60K PWP = PWP68K First 72K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP144K First 140K PWP = PWP140K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 100K PWP = PWP116K First 116K PWP = PWP108K First 108K PWP = PWP108K First 104K PWP = PWP100K First 100K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP98K First 88K PWP = PWP88K First 88K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP156K First 156K PWP = PWP148K First 148K PWP = PWP148K First 144K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 116K PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP100K First 100K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP98K First 88K PWP = PWP88K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
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| PWP = PWP144K First 144K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP108K First 112K PWP = PWP108K First 108K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP98K First 92K PWP = PWP84K First 88K PWP = PWP80K First 80K PWP = PWP76K First 80K PWP = PWP76K First 76K PWP = PWP68K First 68K PWP = PWP64K First 68K PWP = PWP64K First 64K PWP = PWP60K First 64K PWP = PWP60K First 64K PWP = PWP60K First 60K |
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| PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP108K First 108K PWP = PWP108K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP76K First 80K PWP = PWP72K First 76K PWP = PWP68K First 72K PWP = PWP68K First 68K PWP = PWP60K First 64K PWP = PWP60K First 60K |
| PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP76K First 80K PWP = PWP76K First 76K PWP = PWP68K First 72K PWP = PWP64K First 64K PWP = PWP60K First 64K PWP = PWP60K First 60K |
| PWP = PWP128K First 124K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP60K First 64K PWP = PWP60K First 64K PWP = PWP60K First 60K |
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| PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP12K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP64K First 64K PWP = PWP60K First 60K |
| PWP = PWP60K First 60K |
| 5 1 |
| PWP = PWP56K First 56K |
| |
| PWP = PWP52K First 52K |
| PWP = PWP48K First 48K |
| PWP = PWP44K First 44K |
| PWP = PWP40K First 40K |
| PWP = PWP36K First 36K |
| PWP = PWP32K First 32K |
| PWP = PWP28K First 28K |
| PWP = PWP24K First 24K |
| PWP = PWP20K First 20K |
| PWP = PWP16K First 16K |
| PWP = PWP12K First 12K |

| PWP = PWP8K | First 8K |
|-------------|----------|
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX340F256H

PLL Input Divider:

| <u> </u> | |
|-------------------|-------------|
| FPLLIDIV = DIV_1 | 1x Divider |
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| - , | | |
|------------|---------|-------------------|
| FPLLODIV = | DIV_1 | PLL Divide by 1 |
| FPLLODIV = | DIV_2 | PLL Divide by 2 |
| FPLLODIV = | DIV_4 | PLL Divide by 4 |
| FPLLODIV = | DIV_8 | PLL Divide by 8 |
| FPLLODIV = | DIV_16 | PLL Divide by 16 |
| FPLLODIV = | DIV_32 | PLL Divide by 32 |
| FPLLODIV = | DIV_64 | PLL Divide by 64 |
| FPLLODIV = | DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|----------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |

| FNOSC = LPRC | Low Power RC Osc (LPRC) |
|------------------|----------------------------------|
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-----------------|---------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |

| WDTPS = PS65536 | 1:65536 |
|-------------------|-----------|
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| <pre>ICESEL = ICS_PGx1</pre> | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|------------------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| Trogram Flaon Wille From | |
|--------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| | · |

| PWP = PWP392K | First 392K |
|---------------|-------------|
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| | First 356K |
| PWP = PWP356K | |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| | |
| PWP = PWP272K | First 269K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| INT - LMLZOOV | 1 1131 ZOON |

| | E: |
|---------------|------------|
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |

| PWP = PWP16K | First 16K |
|--------------|-----------|
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX340F512H

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| <u> </u> | |
|------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| • | |
|--------------------|-------------------|
| FPLLODIV = DIV_1 | PLL Divide by 1 |
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|----------------|------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |

| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
|------------------|------------------------------------|
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|----------------|--------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |

| WDTPS = PS16384 | 1:16384 |
|-------------------|-----------|
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| | - |
|---------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| | |

| PWP = PWP400K | First 400K |
|---------------|---------------|
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| | First 364K |
| PWP = PWP364K | |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| | |
| PWP = PWP280K | First 270K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| INT - EMESTOU | 1 1130 2 1010 |

| | I = 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 |
|---------------|---|
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| | |

| PWP = PWP24K | First 24K |
|--------------|-----------|
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX350F128H

Shadow Register Set Priority Select:

| | · |
|----------------------|----------------|
| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |

| FPLLMUL = MUL_18 | 18x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| • | |
|----------------|---------------------|
| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | | |
|--|-------------------|-----------|
| WDTPS = PS4 | WDTPS = PS1 | 1:1 |
| WDTPS = PS8 | WDTPS = PS2 | 1:2 |
| WDTPS = PS16 | WDTPS = PS4 | 1:4 |
| WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 WDTPS = PS524288 1:524288 | WDTPS = PS8 | 1:8 |
| WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 WDTPS = PS524288 1:524288 | WDTPS = PS16 | 1:16 |
| WDTPS = PS128 | WDTPS = PS32 | 1:32 |
| WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS64 | 1:64 |
| WDTPS = PS512 | WDTPS = PS128 | 1:128 |
| WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS256 | 1:256 |
| WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS512 | 1:512 |
| WDTPS = PS4096 | WDTPS = PS1024 | 1:1024 |
| WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS2048 | 1:2048 |
| WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS4096 | 1:4096 |
| WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS8192 | 1:8192 |
| WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS16384 | 1:16384 |
| WDTPS = PS131072 | WDTPS = PS32768 | 1:32768 |
| WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS65536 | 1:65536 |
| WDTPS = PS524288 1:524288 | WDTPS = PS131072 | 1:131072 |
| | WDTPS = PS262144 | 1:262144 |
| WDTPS = PS1048576 1:1048576 | WDTPS = PS524288 | 1:524288 |
| <u> </u> | WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
|------------------------------|----------------------------|
| <pre>ICESEL = ICS_PGx2</pre> | Communicate on PGEC2/PGED2 |
| <pre>ICESEL = ICS_PGx1</pre> | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| Program Flash Write Protec | il. |
|----------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| | |

| PWP = I | PWP344K | First 344K |
|---------|---------|------------|
| PWP = I | PWP340K | First 340K |
| PWP = I | PWP336K | First 336K |
| PWP = I | PWP332K | First 332K |
| PWP = I | PWP328K | First 328K |
| PWP = I | PWP324K | First 324K |
| PWP = I | PWP320K | First 320K |
| PWP = I | PWP316K | First 316K |
| PWP = I | PWP312K | First 312K |
| PWP = I | PWP308K | First 308K |
| PWP = I | PWP304K | First 304K |
| PWP = I | PWP300K | First 300K |
| PWP = F | PWP296K | First 296K |
| | PWP292K | First 292K |
| | PWP288K | First 288K |
| | PWP284K | First 284K |
| | PWP280K | First 280K |
| | PWP276K | First 276K |
| | PWP270K | First 272K |
| | PWP272K | First 268K |
| | | First 264K |
| | PWP264K | |
| | PWP260K | First 260K |
| | PWP256K | First 256K |
| | PWP252K | First 252K |
| | PWP248K | First 248K |
| | PWP244K | First 244K |
| | PWP240K | First 240K |
| | PWP236K | First 236K |
| | PWP232K | First 232K |
| | PWP228K | First 228K |
| | PWP224K | First 224K |
| | PWP220K | First 220K |
| | PWP216K | First 216K |
| | PWP212K | First 212K |
| PWP = I | PWP208K | First 208K |
| PWP = I | PWP204K | First 204K |
| PWP = I | PWP200K | First 200K |
| PWP = I | PWP196K | First 196K |
| PWP = I | PWP192K | First 192K |
| PWP = I | PWP188K | First 188K |
| PWP = I | PWP184K | First 184K |
| PWP = I | PWP180K | First 180K |
| PWP = I | PWP176K | First 176K |
| PWP = I | PWP172K | First 172K |
| PWP = I | PWP168K | First 168K |
| PWP = I | PWP164K | First 164K |
| PWP = I | PWP160K | First 160K |
| 1 | | |

| PWP = PWP156K | First 156K |
|---------------|------------|
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX350F128L

Shadow Register Set Priority Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|---------------|-------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |

| WDTPS = PS1024 | 1:1024 |
|-------------------|-----------|
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
|------------------------------|----------------------------|
| <pre>ICESEL = ICS_PGx2</pre> | Communicate on PGEC2/PGED2 |
| <pre>ICESEL = ICS_PGx1</pre> | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |

| PWP = PWP472K | First 472K |
|----------------|-------------|
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| | |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| | |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| INNI - LMLZOOV | 1 1131 2001 |

| PWP = PWP284K | First 284K |
|---------------|------------|
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| | |

| PWP = PWP96K | First 96K |
|--------------|-----------|
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX350F256H

Shadow Register Set Priority Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF Disabled |
|---------------------|
|---------------------|

| IESO = ON Enabled | IESO = ON | Enabled |
|-------------------|-----------|---------|
|-------------------|-----------|---------|

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| 0 | SCIOFNC = ON | Enabled |
|---|---------------|----------|
| 0 | SCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| Wateridog Tillier i Ostset | aiei. |
|----------------------------|-----------|
| WDTPS = PS1 | 1:1 |
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
|------------------------------|----------------------------|
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| <pre>ICESEL = ICS_PGx1</pre> | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| | _ · |
|---------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| L | · |

| | 1 |
|---------------|------------|
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |

| | E: |
|---------------|-------------|
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| | |

| PWP = PWP36K | First 36K |
|--------------|-----------|
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX350F256L

Shadow Register Set Priority Select:

| - | |
|----------------------|----------------|
| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| • | |
|----------------|---------------------|
| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |

| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
|----------------|---------------------|
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCK | SM = CSECME | Clock Switch Enable, FSCM Enabled |
|-----|-------------|-------------------------------------|
| FCK | SM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCK | SM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |
| | |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| <pre>ICESEL = ICS_PGx3</pre> | Communicate on PGEC3/PGED3 |
|------------------------------|----------------------------|
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| <pre>ICESEL = ICS_PGx1</pre> | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| r regram r raem rrinte r retec | |
|--------------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
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|---------------|------------|
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| | |

| PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP152K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP128K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP116K First 112K PWP = PWP108K First 108K PWP = PWP108K First 100K PWP = PWP20K First 38K PWP = PWP96K First 38K PWP = PWP88K First 38K PWP = PWP60K First 70K PWP |
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| PWP = PWP44K First 44K |
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| PWP = PWP40K First 40K |
| PWP = PWP36K First 36K |
| PWP = PWP32K First 32K |
| PWP = PWP28K First 28K |
| PWP = PWP24K First 24K |
| PWP = PWP20K First 20K |
| PWP = PWP16K First 16K |
| PWP = PWP12K First 12K |
| PWP = PWP8K First 8K |
| PWP = PWP4K First 4K |
| PWP = OFF Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX360F256L

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| • • • • • • • • • • • • • • • • • • • | |
|---------------------------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| 1:1 |
|-----------|
| 1:2 |
| 1:4 |
| 1:8 |
| 1:16 |
| 1:32 |
| 1:64 |
| 1:128 |
| 1:256 |
| 1:512 |
| 1:1024 |
| 1:2048 |
| 1:4096 |
| 1:8192 |
| 1:16384 |
| 1:32768 |
| 1:65536 |
| 1:131072 |
| 1:262144 |
| 1:524288 |
| 1:1048576 |
| |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
|--------------|------------------------------------|

| FWDTEN = ON | WDT Enabled |
|-------------|-------------|
|-------------|-------------|

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

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|---------------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
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|---------------|------------|
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
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| PWP = PWP172K | First 172K |
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| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX360F512L

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

|] | IESO = OFF | Disabled |
|---|------------|----------|
| | IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |
| | |

Watchdog Timer Enable:

| FWDTEN = OFF WDT Disabled (SWDTEN Bit Controls) |
|---|
|---|

| FWDTEN = ON | WDT Enabled | |
|-------------|-------------|--|
|-------------|-------------|--|

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| i rogiami i lasii wiite i rotet | , |
|---------------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| | <u> </u> |

| PWP = PWP360K | First 360K |
|-----------------|---------------|
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| | First 324K |
| PWP = PWP324K | |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| | |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| TAAT - TAATT/OK | 1 1130 17 010 |

| | F: |
|---------------|------------|
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| 📲 | 1 |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX370F512H

Shadow Register Set Priority Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| , | |
|-------------------|------------------|
| FPLLODIV = DIV_1 | PLL Divide by 1 |
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |

| FPLLODIV = DIV_32 | PLL Divide by 32 |
|--------------------|-------------------|
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IES | O = OFF | Disabled |
|-----|---------|----------|
| IES | O = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|--------------|------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |

| WDTPS = PS128 | 1:128 |
|-------------------|-----------|
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| <pre>ICESEL = ICS_PGx3</pre> | Communicate on PGEC3/PGED3 |
|------------------------------|----------------------------|
| <pre>ICESEL = ICS_PGx2</pre> | Communicate on PGEC2/PGED2 |
| <pre>ICESEL = ICS_PGx1</pre> | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |

| PWP = PWP484K | First 484K |
|---------------|------------|
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| | |

| PWP = PWP296K | First 296K |
|--------------------------------|-----------------------|
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP244K PWP = PWP240K | First 240K |
| | First 236K |
| PWP = PWP236K | |
| PWP = PWP232K | First 232K First 228K |
| PWP = PWP228K | |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| L | |

| PWP = PWP108K | First 108K |
|---------------|------------|
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX370F512L

Shadow Register Set Priority Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF Disabled |
|------------------------|
|------------------------|

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |

| | Te: |
|---------------|------------|
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| | |

| PWP = PWP236K | First 236K |
|---------------|------------|
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| L | -1 |

| PWP = PWP48K | First 48K |
|--------------|-----------|
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX420F032H

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| • • • • • • • • • • • • • • • • • • • | |
|---------------------------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| - | |
|------------------|------------|
| UPLLIDIV = DIV_1 | 1x Divider |
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |

| UPLLIDIV = DIV_5 | 5x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IES | O = OFF | Disabled |
|-----|---------|----------|
| IES | O = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |

| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |
|----------------|---------------------|
|----------------|---------------------|

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| • | |
|---------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |

| PWP = PWP484K | First 484K |
|----------------|--------------|
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| | |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| | First 368K |
| PWP = PWP368K | |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| I MI - EMEDOOV | 1 1130 30010 |

| PWP = PWP296K | First 296K |
|---------------|------------|
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |

| PWP = PWP108K | First 108K |
|---------------|------------|
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX430F064H

Shadow Register Set Priority Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| 15x Multiplier |
|----------------|
| 16x Multiplier |
| 17x Multiplier |
| 18x Multiplier |
| 19x Multiplier |
| 20x Multiplier |
| 21x Multiplier |
| 24x Multiplier |
| |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------|-----|

| WDTPS = PS2 | 1:2 |
|-------------------|-----------|
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| <pre>ICESEL = ICS_PGx3</pre> | Communicate on PGEC3/PGED3 |
|------------------------------|----------------------------|
| <pre>ICESEL = ICS_PGx2</pre> | Communicate on PGEC2/PGED2 |
| <pre>ICESEL = ICS_PGx1</pre> | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
|---------------|------------|

| | 1 |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| L | |

| I DMD = DMD 2 2 0 W I Liret 220M | |
|----------------------------------|--|
| PWP = PWP320K First 320K | |
| PWP = PWP316K First 316K | |
| PWP = PWP312K First 312K | |
| PWP = PWP308K First 308K | |
| PWP = PWP304K First 304K | |
| PWP = PWP300K First 300K | |
| PWP = PWP296K First 296K | |
| PWP = PWP292K First 292K | |
| PWP = PWP288K First 288K | |
| PWP = PWP284K First 284K | |
| PWP = PWP280K First 280K | |
| PWP = PWP276K First 276K | |
| PWP = PWP272K First 272K | |
| PWP = PWP268K First 268K | |
| PWP = PWP264K First 264K | |
| PWP = PWP260K First 260K | |
| PWP = PWP256K First 256K | |
| PWP = PWP252K First 252K | |
| PWP = PWP248K First 248K | |
| PWP = PWP244K First 244K | |
| PWP = PWP240K First 240K | |
| PWP = PWP236K First 236K | |
| PWP = PWP232K First 232K | |
| PWP = PWP228K First 228K | |
| PWP = PWP224K First 224K | |
| PWP = PWP220K First 220K | |
| PWP = PWP216K First 216K | |
| PWP = PWP212K First 212K | |
| PWP = PWP208K First 208K | |
| PWP = PWP204K First 204K | |
| PWP = PWP200K First 200K | |
| PWP = PWP196K First 196K | |
| PWP = PWP192K First 192K | |
| PWP = PWP188K First 188K | |
| PWP = PWP184K First 184K | |
| PWP = PWP180K First 180K | |
| PWP = PWP176K First 176K | |
| PWP = PWP172K First 172K | |
| PWP = PWP168K First 168K | |
| PWP = PWP164K First 164K | |
| PWP = PWP160K First 160K | |
| PWP = PWP156K First 156K | |
| PWP = PWP152K First 152K | |
| PWP = PWP148K First 148K | |
| THE TWITTON THOU | |
| PWP = PWP144K First 144K | |
| | |

| PWP = PWP132K | First 132K |
|---------------|------------|
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX430F064L

Shadow Register Set Priority Select:

| 9.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2 | ., |
|---|----------------|
| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |

| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| • | |
|------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| <u> </u> | |
|-------------------|-------------|
| UPLLIDIV = DIV_1 | 1x Divider |
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |

| UPLLIDIV = DIV_12 | 12x Divider | |
|-------------------|-------------|--|
|-------------------|-------------|--|

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | | |
|--|-------------------|-----------|
| WDTPS = PS4 | WDTPS = PS1 | 1:1 |
| WDTPS = PS8 | WDTPS = PS2 | 1:2 |
| WDTPS = PS16 | WDTPS = PS4 | 1:4 |
| WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 WDTPS = PS524288 1:524288 | WDTPS = PS8 | 1:8 |
| WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 WDTPS = PS524288 1:524288 | WDTPS = PS16 | 1:16 |
| WDTPS = PS128 | WDTPS = PS32 | 1:32 |
| WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS64 | 1:64 |
| WDTPS = PS512 | WDTPS = PS128 | 1:128 |
| WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS256 | 1:256 |
| WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS512 | 1:512 |
| WDTPS = PS4096 | WDTPS = PS1024 | 1:1024 |
| WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS2048 | 1:2048 |
| WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS4096 | 1:4096 |
| WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS8192 | 1:8192 |
| WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS16384 | 1:16384 |
| WDTPS = PS131072 | WDTPS = PS32768 | 1:32768 |
| WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS65536 | 1:65536 |
| WDTPS = PS524288 1:524288 | WDTPS = PS131072 | 1:131072 |
| | WDTPS = PS262144 | 1:262144 |
| WDTPS = PS1048576 1:1048576 | WDTPS = PS524288 | 1:524288 |
| <u> </u> | WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
|------------------------------|----------------------------|
| <pre>ICESEL = ICS_PGx2</pre> | Communicate on PGEC2/PGED2 |
| <pre>ICESEL = ICS_PGx1</pre> | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| Program Flash Write Protect | ,l. |
|-----------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| | |

| | Te: . o. u. |
|---------------|-------------|
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| | • |

| PWP = PWP156K | First 156K |
|---------------|------------|
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX440F128H

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| - | |
|------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| oob i LL input bitiaoii | |
|-------------------------|-------------|
| UPLLIDIV = DIV_1 | 1x Divider |
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|----------------|------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |

| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
|------------------|------------------------------------|
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|----------------|--------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |

| WDTPS = PS16384 | 1:16384 |
|-------------------|-----------|
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| | - |
|---------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| | |

| PWP = PWP400K | First 400K |
|---------------|---------------|
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| | First 364K |
| PWP = PWP364K | |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| | |
| PWP = PWP280K | First 270K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
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| PWP = PWP212K | First 212K |
|---------------|-------------|
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| | First 176K |
| PWP = PWP176K | |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| | |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
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| PWP = PWP24K | First 24K |
|--------------|-----------|
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX440F128L

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| • | |
|------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|-------------|---------|
|-------------|---------|

| UPLLEN = OFF | Disabled and Bypassed |
|--------------|-----------------------|

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| 1:1 |
|-----------|
| 1:2 |
| 1:4 |
| 1:8 |
| 1:16 |
| 1:32 |
| 1:64 |
| 1:128 |
| 1:256 |
| 1:512 |
| 1:1024 |
| 1:2048 |
| 1:4096 |
| 1:8192 |
| 1:16384 |
| 1:32768 |
| 1:65536 |
| 1:131072 |
| 1:262144 |
| 1:524288 |
| 1:1048576 |
| |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled | |
|-------------|----------------------|--|
| DEBUG = OFF | Debugger is disabled | |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |

| PWP = PWP460K | First 460K |
|---------------|-----------------------|
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| | First 400K |
| PWP = PWP400K | |
| PWP = PWP396K | First 396K First 392K |
| PWP = PWP392K | |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| L | |

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| PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K | PWP = PWP108K | First 108K |
| PWP = PWP96K First 96K PWP = PWP92K First 92K | PWP = PWP104K | First 104K |
| PWP = PWP92K First 92K | PWP = PWP100K | First 100K |
| | PWP = PWP96K | First 96K |
| PWP = PWP88K First 88K | PWP = PWP92K | First 92K |
| | PWP = PWP88K | First 88K |

| PWP = PWP84K | First 84K |
|--------------|-----------|
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX440F256H

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |

| FPLLMUL = MUL_20 | 20x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| <u> </u> | |
|--------------|---------------------|
| POSCMOD = EC | External clock mode |
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |

| POSCMOD = OFF | Primary osc disabled |
|---------------|----------------------|

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 |
|---|
| WDTPS = PS4 |
| WDTPS = PS8 1:8 WDTPS = PS16 1:16 WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS16 1:16 WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 |
| WDTPS = PS8192 1:8192 |
| WDTPS = PS8192 1:8192 |
| 10.02 |
| WDTPS = PS16384 1:16384 |
| |
| WDTPS = PS32768 1:32768 |
| WDTPS = PS65536 1:65536 |
| WDTPS = PS131072 1:131072 |
| WDTPS = PS262144 1:262144 |
| WDTPS = PS524288 1:524288 |
| WDTPS = PS1048576 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
|-------------------|--|

| ICESEL = ICS PGx2 ICE EMUCZ/EMUDZ pins snared with PGCZ/PGD | ICESEL = ICS PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |
|---|-------------------|--|
|---|-------------------|--|

Program Flash Write Protect:

| 1 Togram Flash Write From | |
|---------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| | |

| <u> </u> | |
|---------------|------------|
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| <u></u> | |

| PWP = PWP144K | First 144K |
|---------------|------------|
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX440F512H

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| - | |
|------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| 1x Divider |
|-------------|
| 2x Divider |
| 3x Divider |
| 4x Divider |
| 5x Divider |
| 6x Divider |
| 10x Divider |
| 12x Divider |
| |

USB PLL Enable:

| | UPLLEN = ON | Enabled |
|---|--------------|-----------------------|
| Ī | UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|----------------|------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |

| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
|------------------|------------------------------------|
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| LIDEDG DG1 | 1.1 |
|----------------|--------|
| WDTPS = PS1 | 1:1 |
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |

| WDTPS = PS16384 | 1:16384 |
|-------------------|-----------|
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| | - |
|---------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| | |

| PWP = PWP400K | First 400K |
|---------------|------------|
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |

| PWP = PWP212K | First 212K |
|---------------|------------|
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| | |

| PWP = PWP24K | First 24K |
|--------------|-----------|
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX450F128H

Shadow Register Set Priority Select:

| | <u> </u> |
|----------------------|----------------|
| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|------------------|------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |

| FPLLIDIV = DIV_10 | 10x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| Ι | ESO = OFF | Disabled |
|---|-----------|----------|
| I | ESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| 0; | SCIOFNC = ON | Enabled |
|----|---------------|----------|
| 0; | SCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |
| | |

Watchdog Timer Window Enable:

| WINDIS = ON Watchdog Timer is in Window Mode | |
|--|--|
|--|--|

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled | |
|-------------|----------------------|--|
| DEBUG = OFF | Debugger is Disabled | |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| i rogiami i lasm write i rotet | |
|--------------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| | |

| | I=0 |
|---------------|------------|
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| · | |

| PWP = PWP228K | First 228K |
|---------------|------------|
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 64K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 44K |
| PWP = PWP44K | First 44K |

| PWP = PWP40K | First 40K |
|--------------|-----------|
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX450F128L

Shadow Register Set Priority Select:

| SRS Priority 0 |
|----------------|
| SRS Priority 1 |
| SRS Priority 2 |
| SRS Priority 3 |
| SRS Priority 4 |
| SRS Priority 5 |
| SRS Priority 6 |
| SRS Priority 7 |
| |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|------------------|------------|
| FPLLIDIV = DIV_2 | 2x Divider |

| FPLLIDIV = DIV_3 | 3x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| <u> </u> | |
|-------------------|-------------|
| UPLLIDIV = DIV_1 | 1x Divider |
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|------------------|----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |

| WDTPS = PS262144 | 1:262144 |
|-------------------|-----------|
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |

| PWP = PWP440K | First 440K |
|---------------|------------|
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| | |

| PWP = PWP252K | First 252K |
|---------------|------------|
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| | |

| PWP = PWP64K | First 64K |
|--------------|-----------|
| PWP = PWP64K | Filst 04K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX450F256H

Shadow Register Set Priority Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC Fast RC Osc (FRC) | |
|-------------------------------|--|
|-------------------------------|--|

| FNOSC = FRCPLL | Fast RC Osc with PLL |
|------------------|------------------------------------|
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|----------------|--------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |

| WDTPS = PS4096 | 1:4096 |
|-------------------|-----------|
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |

| PWP = PWP464K | First 464K |
|---------------|------------|
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| L | 1 |

| PWP = PWP276K | First 276K |
|---------------------------------------|------------|
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| · · · · · · · · · · · · · · · · · · · | |

| PWP = PWP88K | First 88K |
|--------------|-----------|
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX450F256L

Shadow Register Set Priority Select:

| SRS Priority 0 |
|----------------|
| SRS Priority 1 |
| SRS Priority 2 |
| SRS Priority 3 |
| SRS Priority 4 |
| SRS Priority 5 |
| SRS Priority 6 |
| SRS Priority 7 |
| |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|------------------|-----------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |

| FPLLODIV = DIV_8 | PLL Divide by 8 |
|--------------------|-------------------|
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|--------------|------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |

| WDTPS = PS32 | 1:32 |
|-------------------|-----------|
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DE | BUG = ON | Debugger is Enabled |
|----|-----------|----------------------|
| DE | BUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS PGx3 | Communicate on PGEC3/PGED3 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |

| PWP = PWP492K | First 492K |
|---------------|------------|
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| · | |

| PWP = PWP304K | First 304K |
|---------------|------------|
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |

| PWP = PWP116K | First 116K |
|---------------|------------|
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX460F256L

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |

|--|

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

|] | IESO = OFF | Disabled |
|---|------------|----------|
| | IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 1:1 WDTPS = PS2 1:2 WDTPS = PS4 1:4 WDTPS = PS8 1:8 WDTPS = PS16 1:16 WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS128 1:128 WDTPS = PS512 1:512 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS131072 1:131072 WDTPS = PS262144 WDTPS = PS524288 WDTPS = PS1048576 1:048576 | | |
|---|-------------------|-----------|
| WDTPS = PS4 1:4 WDTPS = PS8 1:8 WDTPS = PS16 1:16 WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS1 | 1:1 |
| WDTPS = PS8 1:8 WDTPS = PS16 1:16 WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS131072 1:131072 WDTPS = PS262144 WDTPS = PS524288 1:524288 | WDTPS = PS2 | 1:2 |
| WDTPS = PS16 | WDTPS = PS4 | 1:4 |
| WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS8 | 1:8 |
| WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS16 | 1:16 |
| WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS32 | 1:32 |
| WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS64 | 1:64 |
| WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS128 | 1:128 |
| WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS256 | 1:256 |
| WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS512 | 1:512 |
| WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS1024 | 1:1024 |
| WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS2048 | 1:2048 |
| WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS4096 | 1:4096 |
| WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS8192 | 1:8192 |
| WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS16384 | 1:16384 |
| WDTPS = PS131072 | WDTPS = PS32768 | 1:32768 |
| WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS65536 | 1:65536 |
| WDTPS = PS524288 1:524288 | WDTPS = PS131072 | 1:131072 |
| | WDTPS = PS262144 | 1:262144 |
| WDTPS = PS1048576 1:1048576 | WDTPS = PS524288 | 1:524288 |
| | WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
|--------------|------------------------------------|

| FWDTEN = ON | WDT Enabled | |
|-------------|-------------|--|
|-------------|-------------|--|

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| i rogiami i lasii wiite i rotet | , |
|---------------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| | <u> </u> |

| | T=1 |
|---------------|------------|
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |

| PWP = PWP172K | First 172K |
|---------------|------------|
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX460F512L

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| • | |
|------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| • | |
|--------------------|-------------------|
| FPLLODIV = DIV_1 | PLL Divide by 1 |
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|---------------|-------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |

| WDTPS = PS1024 | 1:1024 |
|-------------------|-----------|
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| 9 | |
|---------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| | |

| PWP = PWP416K | First 416K |
|---------------|------------|
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| | |

| PWP = PWP228K | First 228K |
|---------------|------------|
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| | |

| PWP = PWP40K | First 40K |
|--------------|-----------|
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX470F512H

Shadow Register Set Priority Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |
| | |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|------------------|------------|
| FPLLIDIV = DIV_2 | 2x Divider |

| FPLLIDIV = DIV_3 | 3x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| • | |
|-------------------|-------------|
| UPLLIDIV = DIV_1 | 1x Divider |
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |

| FNOSC = FRCDIV Fast RC Osc w/Div-by-N (FRCDIV) | |
|--|--|
|--|--|

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|------------------|----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |

| WDTPS = PS262144 | 1:262144 |
|-------------------|-----------|
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS | = ON | Watchdog Timer is in Window Mode |
|--------|-------|--------------------------------------|
| WINDIS | = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |

| PWP = PWP440K | First 440K |
|---------------|------------|
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| | |

| PWP = PWP252K | First 252K |
|---------------|------------|
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| | |

| PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K PWP = PWP52K First 52K PWP = PWP48K First 48K PWP = PWP44K First 44K PWP = PWP40K First 40K PWP = PWP36K First 36K PWP = PWP32K First 32K PWP = PWP24K First 24K PWP = PWP24K First 24K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP4K First 8K PWP = PWP4K First 4K PWP = PWP4K First 4K PWP = PWP4 PWP4K PWP = PWP4K First 4K | | |
|---|--------------|-----------|
| PWP = PWP56K First 56K PWP = PWP52K First 52K PWP = PWP48K First 48K PWP = PWP44K First 44K PWP = PWP40K First 40K PWP = PWP36K First 36K PWP = PWP32K First 32K PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP4K First 8K PWP = PWP4K First 4K | PWP = PWP64K | First 64K |
| PWP = PWP52K First 52K PWP = PWP48K First 48K PWP = PWP44K First 44K PWP = PWP40K First 40K PWP = PWP36K First 36K PWP = PWP32K First 32K PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP4K First 8K PWP = PWP4K First 4K | PWP = PWP60K | First 60K |
| PWP = PWP48K First 48K PWP = PWP44K First 44K PWP = PWP40K First 40K PWP = PWP36K First 36K PWP = PWP32K First 32K PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP4K First 8K PWP = PWP4K First 4K | PWP = PWP56K | First 56K |
| PWP = PWP44K First 44K PWP = PWP40K First 40K PWP = PWP36K First 36K PWP = PWP32K First 32K PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP52K | First 52K |
| PWP = PWP40K First 40K PWP = PWP36K First 36K PWP = PWP32K First 32K PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP48K | First 48K |
| PWP = PWP36K First 36K PWP = PWP32K First 32K PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP44K | First 44K |
| PWP = PWP32K First 32K PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP40K | First 40K |
| PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP36K | First 36K |
| PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP32K | First 32K |
| PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP28K | First 28K |
| PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP24K | First 24K |
| PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP20K | First 20K |
| PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP16K | First 16K |
| PWP = PWP4K First 4K | PWP = PWP12K | First 12K |
| | PWP = PWP8K | First 8K |
| PWP = OFF Disable | PWP = PWP4K | First 4K |
| | PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX470F512L

Shadow Register Set Priority Select:

| FSRSSEL = PRIORITY_0 SRS Priority 0 | |
|-------------------------------------|---|
| | |
| FSRSSEL = PRIORITY_1 SRS Priority 1 | |
| FSRSSEL = PRIORITY_2 SRS Priority 2 | |
| FSRSSEL = PRIORITY_3 SRS Priority 3 | |
| FSRSSEL = PRIORITY_4 SRS Priority 4 | |
| FSRSSEL = PRIORITY_5 SRS Priority 5 | |
| FSRSSEL = PRIORITY_6 SRS Priority 6 | _ |
| FSRSSEL = PRIORITY_7 SRS Priority 7 | |

Peripheral Module Disable Configuration:

| PMDL1WAY = OFF | Allow multiple reconfigurations |
|----------------|---------------------------------|
| PMDL1WAY = ON | Allow only one reconfiguration |

Peripheral Pin Select Configuration:

| IOL1WAY = OFF | Allow multiple reconfigurations |
|---------------|---------------------------------|
| IOL1WAY = ON | Allow only one reconfiguration |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| , , | FNOSC = FRC | Fast RC Osc (FRC) |
|-----|-------------|-------------------|
|-----|-------------|-------------------|

| FNOSC = FRCPLL | Fast RC Osc with PLL |
|------------------|------------------------------------|
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|----------------|--------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |

| WDTPS = PS4096 | 1:4096 |
|-------------------|-----------|
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Window Enable:

| WINDIS = ON | Watchdog Timer is in Window Mode |
|--------------|--------------------------------------|
| WINDIS = OFF | Watchdog Timer is in Non-Window Mode |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Watchdog Timer Window Size:

| FWDTWINSZ = WINSZ_75 | Window Size is 75% |
|----------------------|----------------------|
| FWDTWINSZ = WINSZ_50 | Window Size is 50% |
| FWDTWINSZ = WINSZ_37 | Window Size is 37.5% |
| FWDTWINSZ = WISZ_25 | Window Size is 25% |

Background Debugger Enable:

| DEBUG = ON | Debugger is Enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is Disabled |

JTAG Enable:

| JTAGEN = OFF | JTAG Disabled |
|--------------|-------------------|
| JTAGEN = ON | JTAG Port Enabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx3 | Communicate on PGEC3/PGED3 |
|-------------------|----------------------------|
| ICESEL = ICS_PGx2 | Communicate on PGEC2/PGED2 |
| ICESEL = ICS_PGx1 | Communicate on PGEC1/PGED1 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |

| PWP = PWP464K | First 464K |
|---------------|------------|
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| | 1 |

| PWP = PWP276K | First 276K |
|---------------------------------------|------------|
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| · · · · · · · · · · · · · · · · · · · | |

| PWP = PWP88K | First 88K |
|--------------|-----------|
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

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SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| PLL Divide by 1 |
|------------------|
| PLL Divide by 2 |
| PLL Divide by 4 |
| PLL Divide by 8 |
| PLL Divide by 16 |
| PLL Divide by 32 |
| PLL Divide by 64 |
| |

| FPLLODIV = DIV_256 | PLL Divide by 256 | |
|--------------------|-------------------|--|
|--------------------|-------------------|--|

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| <u> </u> | |
|----------------|----------|
| OSCIOFNC = ON | Enabled |
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| <u> </u> | |
|---------------|-------|
| WDTPS = PS1 | 1:1 |
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |

| WDTPS = PS512 | 1:512 |
|-------------------|-----------|
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FW | DTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|----|------------|------------------------------------|
| FW | DTEN = ON | WDT Enabled |

Background Debugger Enable:

| D | EBUG = ON | Debugger is enabled |
|---|------------|----------------------|
| D | EBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| <pre>ICESEL = ICS_PGx1</pre> | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|------------------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------------------------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
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| PWP = PWP284K First 284K PWP = PWP280K First 280K PWP = PWP276K First 276K PWP = PWP272K First 272K PWP = PWP268K First 268K PWP = PWP264K First 264K PWP = PWP260K First 260K PWP = PWP256K First 256K PWP = PWP252K First 252K PWP = PWP244K First 244K PWP = PWP240K First 240K | PWP = PWP292K | First 292K |
| PWP = PWP280K First 280K PWP = PWP276K First 276K PWP = PWP272K First 272K PWP = PWP268K First 268K PWP = PWP264K First 264K PWP = PWP260K First 260K PWP = PWP256K First 256K PWP = PWP252K First 252K PWP = PWP248K First 248K PWP = PWP244K First 244K PWP = PWP240K First 240K | PWP = PWP288K | First 288K |
| PWP = PWP276K First 276K PWP = PWP272K First 272K PWP = PWP268K First 268K PWP = PWP264K First 264K PWP = PWP260K First 260K PWP = PWP256K First 256K PWP = PWP252K First 252K PWP = PWP248K First 244K PWP = PWP244K First 244K PWP = PWP240K First 240K | PWP = PWP284K | First 284K |
| PWP = PWP272K First 272K PWP = PWP268K First 268K PWP = PWP264K First 264K PWP = PWP260K First 260K PWP = PWP256K First 256K PWP = PWP252K First 252K PWP = PWP248K First 244K PWP = PWP240K First 240K | PWP = PWP280K | First 280K |
| PWP = PWP268K First 268K PWP = PWP264K First 264K PWP = PWP260K First 260K PWP = PWP256K First 256K PWP = PWP252K First 252K PWP = PWP248K First 248K PWP = PWP244K First 244K PWP = PWP240K First 240K | PWP = PWP276K | First 276K |
| PWP = PWP264K First 264K PWP = PWP260K First 260K PWP = PWP256K First 256K PWP = PWP252K First 252K PWP = PWP248K First 248K PWP = PWP244K First 244K PWP = PWP240K First 240K | PWP = PWP272K | First 272K |
| PWP = PWP260K First 260K PWP = PWP256K First 256K PWP = PWP252K First 252K PWP = PWP248K First 248K PWP = PWP244K First 244K PWP = PWP240K First 240K | PWP = PWP268K | First 268K |
| PWP = PWP256K First 256K PWP = PWP252K First 252K PWP = PWP248K First 248K PWP = PWP244K First 244K PWP = PWP240K First 240K | PWP = PWP264K | First 264K |
| PWP = PWP252K First 252K PWP = PWP248K First 248K PWP = PWP244K First 244K PWP = PWP240K First 240K | PWP = PWP260K | First 260K |
| PWP = PWP248K First 248K PWP = PWP244K First 244K PWP = PWP240K First 240K | PWP = PWP256K | First 256K |
| PWP = PWP244K First 244K PWP = PWP240K First 240K | PWP = PWP252K | First 252K |
| PWP = PWP240K First 240K | PWP = PWP248K | First 248K |
| | PWP = PWP244K | First 244K |
| PWP = PWP236K First 236K | PWP = PWP240K | First 240K |
| | PWP = PWP236K | First 236K |

| | E: |
|---------------|------------|
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| | |

| PWP = PWP44K | First 44K |
|--------------|-----------|
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

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SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|------------------|------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |

| FPLLIDIV = DIV_5 | 5x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled | |
|------------|----------|--|
| IESO = ON | Enabled | |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 | |
|------------------|----------|--|
| WDTPS = PS2 | 1:2 | |
| WDTPS = PS4 | 1:4 | |
| WDTPS = PS8 | 1:8 | |
| WDTPS = PS16 | 1:16 | |
| WDTPS = PS32 | 1:32 | |
| WDTPS = PS64 | 1:64 | |
| WDTPS = PS128 | 1:128 | |
| WDTPS = PS256 | 1:256 | |
| WDTPS = PS512 | 1:512 | |
| WDTPS = PS1024 | 1:1024 | |
| WDTPS = PS2048 | 1:2048 | |
| WDTPS = PS4096 | 1:4096 | |
| WDTPS = PS8192 | 1:8192 | |
| WDTPS = PS16384 | 1:16384 | |
| WDTPS = PS32768 | 1:32768 | |
| WDTPS = PS65536 | 1:65536 | |
| WDTPS = PS131072 | 1:131072 | |
| WDTPS = PS262144 | 1:262144 | |
| WDTPS = PS524288 | 1:524288 | |

| WDTPS = PS1048576 | 1:1048576 | |
|-------------------|-----------|--|
|-------------------|-----------|--|

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBU | JG = ON | Debugger is enabled |
|------|----------|----------------------|
| DEBU | JG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| i rogram i lasm write i rote | ··· |
|------------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| · | |

| PWP = PWP3 | 76K | First 376K |
|--------------------------|-----|------------|
| PWP = PWP3 | 72K | First 372K |
| PWP = PWP3 | 68K | First 368K |
| PWP = PWP3 | 64K | First 364K |
| PWP = PWP3 | 60K | First 360K |
| PWP = PWP3 | 56K | First 356K |
| PWP = PWP3 | 52K | First 352K |
| PWP = PWP3 | 48K | First 348K |
| PWP = PWP3 | 44K | First 344K |
| PWP = PWP3 | 40K | First 340K |
| PWP = PWP3 | 36K | First 336K |
| PWP = PWP3 | 32K | First 332K |
| PWP = PWP3 | 28K | First 328K |
| PWP = PWP3 | | First 324K |
| PWP = PWP3 | | First 320K |
| PWP = PWP3 | | First 316K |
| PWP = PWP3 | | First 312K |
| PWP = PWP3 | | First 308K |
| PWP = PWP3 PWP = PWP3 | | First 304K |
| | | First 300K |
| PWP = PWP3 | | First 296K |
| PWP = PWP2 | | |
| PWP = PWP2 | | First 292K |
| PWP = PWP2 | | First 288K |
| PWP = PWP2 | | First 284K |
| PWP = PWP2 | | First 280K |
| PWP = PWP2 | | First 276K |
| PWP = PWP2 | | First 272K |
| PWP = PWP2 | | First 268K |
| PWP = PWP2 | | First 264K |
| PWP = PWP2 | | First 260K |
| PWP = PWP2 | | First 256K |
| PWP = PWP2 | 52K | First 252K |
| PWP = PWP2 | 48K | First 248K |
| PWP = PWP2 | | First 244K |
| PWP = PWP2 | 40K | First 240K |
| PWP = PWP2 | 36K | First 236K |
| PWP = PWP2 | 32K | First 232K |
| PWP = PWP2 | 28K | First 228K |
| PWP = PWP2 | 24K | First 224K |
| PWP = PWP2 | 20K | First 220K |
| PWP = PWP2 | 16K | First 216K |
| PWP = PWP2 | 12K | First 212K |
| PWP = PWP2 | 08K | First 208K |
| PWP = PWP2 | 04K | First 204K |
| PWP = PWP2 | 00K | First 200K |
| PWP = PWP1 | 96K | First 196K |
| PWP = PWP1 | 92K | First 192K |
| L | | |

| | I= |
|---------------------------------------|------------|
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| · · · · · · · · · · · · · · · · · · · | · |

| DWD = OFF | Disable | |
|-----------|-----------|--|
| PWP = OFF | l Disable | |
| | | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

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SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |

| FPLLMUL = MUL_21 | 21x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | wateriaby filler i bataca | |
|--|---------------------------|-----------|
| WDTPS = PS4 | WDTPS = PS1 | 1:1 |
| WDTPS = PS8 | WDTPS = PS2 | 1:2 |
| WDTPS = PS16 | WDTPS = PS4 | 1:4 |
| WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 WDTPS = PS524288 1:524288 | WDTPS = PS8 | 1:8 |
| WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 WDTPS = PS524288 1:524288 | WDTPS = PS16 | 1:16 |
| WDTPS = PS128 | WDTPS = PS32 | 1:32 |
| WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS64 | 1:64 |
| WDTPS = PS512 | WDTPS = PS128 | 1:128 |
| WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS256 | 1:256 |
| WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS512 | 1:512 |
| WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS1024 | 1:1024 |
| WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS2048 | 1:2048 |
| WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS4096 | 1:4096 |
| WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS8192 | 1:8192 |
| WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS16384 | 1:16384 |
| WDTPS = PS131072 | WDTPS = PS32768 | 1:32768 |
| WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS65536 | 1:65536 |
| WDTPS = PS524288 1:524288 | WDTPS = PS131072 | 1:131072 |
| | WDTPS = PS262144 | 1:262144 |
| WDTPS = PS1048576 1:1048576 | WDTPS = PS524288 | 1:524288 |
| | WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| L | 1 |

| PWP = PWP328K | First 328K |
|--------------------------------|------------|
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP244K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP230K | First 232K |
| PWP = PWP232K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP224K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| | First 208K |
| PWP = PWP208K PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP190K | First 192K |
| PWP = PWP192K | First 188K |
| | First 184K |
| PWP = PWP184K | First 180K |
| PWP = PWP180K | First 176K |
| PWP = PWP176K | |
| PWP = PWP172K | First 169K |
| PWP = PWP168K | First 164K |
| PWP = PWP164K | First 160K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |

| PWP = PWP140K | First 140K |
|---------------|------------|
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX564F064L

SRS Select:

| TGDGGTI DDTGDTTII A | CDC Driewith O |
|----------------------|----------------|
| FSRSSEL = PRIORITY_0 | SRS Priority 0 |

| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| • | · |
|-------------------|-------------|
| FPLLIDIV = DIV_1 | 1x Divider |
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |

| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |
|----------------|-------------------------------------|
|----------------|-------------------------------------|

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |

| PWP = PWP468K | First 468K |
|---------------|-------------|
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| | |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| | |
| PWP = PWP348K | First 344K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| TMT - LML704V | I HOL ZOTIV |

| PWP = PWP276K First 276K PWP = PWP272K First 272K PWP = PWP26AK First 272K PWP = PWP26AK First 26AK PWP = PWP24AK First 252K PWP = PWP24AK First 24AK PWP = PWP24AK First 24AK PWP = PWP23AK First 23AK PWP = PWP23AK First 23AK PWP = PWP23AK First 22AK PWP = PWP22AK First 212K PWP = PWP22AK First 212K PWP = PWP22AK First 212K PWP = PWP22AK First 20K PWP = PWP2AK First 20K PWP = PWP2AK First 20K PWP = PWP2AK First 20K PW | | 1 |
|--|---------------|------------|
| PWP = PWP272X First 272K PWP = PWP268K First 268K PWP = PWP264K First 260K PWP = PWP266K First 260K PWP = PWP256K First 256K PWP = PWP252X First 252K PWP = PWP244K First 240K PWP = PWP240K First 240K PWP = PWP236K First 230K PWP = PWP232X First 230K PWP = PWP224X First 232K PWP = PWP224X First 224K PWP = PWP224X First 220K PWP = PWP220X First 220K PWP = PWP220X First 220K PWP = PWP212X First 20K PWP = PWP20X First 196K PWP = PWP192X First 196K PWP = PWP192X First 184K PWP = | PWP = PWP280K | First 280K |
| PWP = PWP268K First 268K PWP = PWP260K First 264K PWP = PWP260K First 266K PWP = PWP256K First 256K PWP = PWP252K First 252K PWP = PWP244K First 244K PWP = PWP244K First 240K PWP = PWP240K First 236K PWP = PWP232K First 236K PWP = PWP232K First 232K PWP = PWP224K First 228K PWP = PWP224K First 220K PWP = PWP224K First 20K PWP = PWP224K First 20K PWP = PWP224K First 20K PWP = PWP204K First 19K PWP = PWP192K First 18K PWP = PWP192K First 18K PWP = | PWP = PWP276K | First 276K |
| PWP = PWP264K First 264K PWP = PWP250K First 260K PWP = PWP252K First 256K PWP = PWP252K First 252K PWP = PWP244K First 244K PWP = PWP240K First 240K PWP = PWP23CK First 236K PWP = PWP232K First 230K PWP = PWP232K First 228K PWP = PWP224K First 220K PWP = PWP220K First 220K PWP = PWP220K First 220K PWP = PWP212K First 220K PWP = PWP20K First 220K PWP = PWP20K First 20K PWP = PWP20K First 192K PWP = PWP196K First 192K PWP = PWP186K First 184K PWP = PWP180K First 180K PWP = PWP160K First 172K PWP = | PWP = PWP272K | First 272K |
| PWP = PWP256K First 256K PWP = PWP252K First 256K PWP = PWP252K First 256K PWP = PWP248K First 244K PWP = PWP240K First 240K PWP = PWP240K First 240K PWP = PWP236K First 236K PWP = PWP223K First 232K PWP = PWP224K First 228K PWP = PWP224K First 220K PWP = PWP221K First 220K PWP = PWP21CK First 216K PWP = PWP21CK First 216K PWP = PWP21CK First 20K PWP = PWP21CK First 20K PWP = PWP200K First 20K PWP = PWP200K First 20K PWP = PWP20K First 20K PWP = PWP20K First 196K PWP = PWP19CK First 196K PWP = PWP19CK First 196K PWP = PWP19CK First 184K PWP = PWP19CK First 184K PWP = PWP19CK First 180K PWP = PWP17CK First 170K PWP = PWP17CK First 160K PW | PWP = PWP268K | First 268K |
| PWP = PWP256K First 256K PWP = PWP252K First 252K PWP = PWP244K First 248K PWP = PWP244K First 244K PWP = PWP240K First 240K PWP = PWP236K First 236K PWP = PWP232K First 232K PWP = PWP224K First 228K PWP = PWP224K First 220K PWP = PWP220K First 220K PWP = PWP216K First 216K PWP = PWP212K First 212K PWP = PWP20K First 20K PWP = PWP20K First 196K PWP = PWP20K First 196K PWP = PWP19K First 196K PWP = PWP19K First 184K PWP = PWP18K First 184K PWP = PWP18K First 176K PWP = PWP17K First 16K PWP = PWP16K | PWP = PWP264K | First 264K |
| PWP = PWP252K First 252K PWP = PWP248K First 248K PWP = PWP240K First 244K PWP = PWP240K First 240K PWP = PWP236K First 236K PWP = PWP232K First 236K PWP = PWP232K First 232K PWP = PWP228K First 226K PWP = PWP224K First 220K PWP = PWP221CK First 220K PWP = PWP212K First 212K PWP = PWP212K First 220K PWP = PWP20AK First 20K PWP = PWP20AK First 196K PWP = PWP19AK First 196K PWP = PWP19AK First 188K PWP = PWP19AK First 180K PWP = PWP17AK First 170K PWP = PWP16AK First 160K PW | PWP = PWP260K | First 260K |
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| PWP = PWP20K First 20K |
| PWP = PWP16K First 16K |
| PWP = PWP12K First 12K |
| PWP = PWP8K First 8K |
| PWP = PWP4K First 4K |
| PWP = OFF Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

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SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|-------------------|------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |

| FPLLODIV = DIV_256 | PLL Divide by 256 |
|--------------------|-------------------|
|--------------------|-------------------|

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| <u> </u> | |
|----------------|----------|
| OSCIOFNC = ON | Enabled |
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|---------------|-------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |

| | 4.540 |
|-------------------|-----------|
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| D | EBUG = ON | Debugger is enabled |
|---|------------|----------------------|
| D | EBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP508K First 508K PWP = PWP504K First 504K PWP = PWP500K First 500K PWP = PWP496K First 496K PWP = PWP492K First 492K PWP = PWP488K First 488K PWP = PWP484K First 484K PWP = PWP480K First 480K PWP = PWP476K First 476K PWP = PWP472K First 472K PWP = PWP468K First 468K PWP = PWP464K First 464K PWP = PWP460K First 460K PWP = PWP456K First 456K PWP = PWP448K First 448K PWP = PWP444K First 444K PWP = PWP440K First 440K PWP = PWP436K First 436K PWP = PWP432K First 432K PWP = PWP428K First 428K PWP = PWP428K First 428K PWP = PWP424K First 424K | PWP = PWP512K | First 512K |
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| PWP = PWP428K First 428K | PWP = PWP436K | First 436K |
| | PWP = PWP432K | First 432K |
| PWP = PWP424K First 424K | PWP = PWP428K | First 428K |
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| PWP = PWP420K | First 420K |
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| PWP = PWP416K | First 416K |
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| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
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| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
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| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
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| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
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| | PWP = PWP56K | First 56K |
| PWP = PWP48K First 48K | PWP = PWP52K | First 52K |
| | PWP = PWP48K | First 48K |

| PWP = PWP44K | First 44K |
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| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX564F128L

SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|------------------|------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |

| FPLLIDIV = DIV_5 | 5x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled | |
|------------|----------|--|
| IESO = ON | Enabled | |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 | |
|------------------|----------|--|
| WDTPS = PS2 | 1:2 | |
| WDTPS = PS4 | 1:4 | |
| WDTPS = PS8 | 1:8 | |
| WDTPS = PS16 | 1:16 | |
| WDTPS = PS32 | 1:32 | |
| WDTPS = PS64 | 1:64 | |
| WDTPS = PS128 | 1:128 | |
| WDTPS = PS256 | 1:256 | |
| WDTPS = PS512 | 1:512 | |
| WDTPS = PS1024 | 1:1024 | |
| WDTPS = PS2048 | 1:2048 | |
| WDTPS = PS4096 | 1:4096 | |
| WDTPS = PS8192 | 1:8192 | |
| WDTPS = PS16384 | 1:16384 | |
| WDTPS = PS32768 | 1:32768 | |
| WDTPS = PS65536 | 1:65536 | |
| WDTPS = PS131072 | 1:131072 | |
| WDTPS = PS262144 | 1:262144 | |
| WDTPS = PS524288 | 1:524288 | |

| WDTPS = PS1048576 | 1:1048576 |
|-------------------|-----------|
|-------------------|-----------|

Watchdog Timer Enable:

| F | WDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|---|-------------|------------------------------------|
| F | WDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBU | JG = ON | Debugger is enabled |
|------|----------|----------------------|
| DEBU | JG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| i rogiami riasii wiite i rote | ··· |
|-------------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| · | |

| PWP = PWP3 | 76K | First 376K |
|--------------------------|-----|------------|
| PWP = PWP3 | 72K | First 372K |
| PWP = PWP3 | 68K | First 368K |
| PWP = PWP3 | 64K | First 364K |
| PWP = PWP3 | 60K | First 360K |
| PWP = PWP3 | 56K | First 356K |
| PWP = PWP3 | 52K | First 352K |
| PWP = PWP3 | 48K | First 348K |
| PWP = PWP3 | 44K | First 344K |
| PWP = PWP3 | 40K | First 340K |
| PWP = PWP3 | 36K | First 336K |
| PWP = PWP3 | 32K | First 332K |
| PWP = PWP3 | 28K | First 328K |
| PWP = PWP3 | | First 324K |
| PWP = PWP3 | | First 320K |
| PWP = PWP3 | | First 316K |
| PWP = PWP3 | | First 312K |
| PWP = PWP3 | | First 308K |
| PWP = PWP3 PWP = PWP3 | | First 304K |
| | | First 300K |
| PWP = PWP3 | | First 296K |
| PWP = PWP2 | | |
| PWP = PWP2 | | First 292K |
| PWP = PWP2 | | First 288K |
| PWP = PWP2 | | First 284K |
| PWP = PWP2 | | First 280K |
| PWP = PWP2 | | First 276K |
| PWP = PWP2 | | First 272K |
| PWP = PWP2 | | First 268K |
| PWP = PWP2 | | First 264K |
| PWP = PWP2 | | First 260K |
| PWP = PWP2 | | First 256K |
| PWP = PWP2 | 52K | First 252K |
| PWP = PWP2 | 48K | First 248K |
| PWP = PWP2 | | First 244K |
| PWP = PWP2 | 40K | First 240K |
| PWP = PWP2 | 36K | First 236K |
| PWP = PWP2 | 32K | First 232K |
| PWP = PWP2 | 28K | First 228K |
| PWP = PWP2 | 24K | First 224K |
| PWP = PWP2 | 20K | First 220K |
| PWP = PWP2 | 16K | First 216K |
| PWP = PWP2 | 12K | First 212K |
| PWP = PWP2 | 08K | First 208K |
| PWP = PWP2 | 04K | First 204K |
| PWP = PWP2 | 00K | First 200K |
| PWP = PWP1 | 96K | First 196K |
| PWP = PWP1 | 92K | First 192K |
| L | | |

| | T=1 |
|---------------|------------|
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| | |

| | | l l |
|-----------|-----------|-----|
| PWP = OFF | l Disable | |
| PWP - Off | Disable | |
| | | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX575F256H

SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |

| FPLLMUL = MUL_21 | 21x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| watchidog Tillier Postsca | ici. |
|---------------------------|-----------|
| WDTPS = PS1 | 1:1 |
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |

| PWP = PWP328K | First 328K |
|---------------------------------------|-----------------------|
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| $\frac{PWP - PWP270K}{PWP = PWP272K}$ | First 272K |
| $\frac{PWP - PWP272K}{PWP = PWP268K}$ | First 268K |
| | |
| PWP = PWP264K | First 264K First 260K |
| PWP = PWP260K | |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
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|---------------|----------------|
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

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SRS Select:

| FSRSSEL = PRIORITY_0 SRS Priority 0 |
|---------------------------------------|
|---------------------------------------|

| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| • | |
|-------------------|-------------|
| FPLLIDIV = DIV_1 | 1x Divider |
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled | |
|------------|----------|--|
| IESO = ON | Enabled | |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |

| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |
|----------------|-------------------------------------|
|----------------|-------------------------------------|

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |

| PWP = PWP468K | First 468K |
|-----------------|-------------|
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| | |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| | |
| PWP = PWP348K | First 344K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| TANT - LANEZOAU | I HOL ZOTIV |

| PWP = PWP280K | First 280K |
|---------------|------------|
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| | First 244K |
| PWP = PWP244K | |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| | First 160K |
| PWP = PWP160K | |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| | |

| PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K PWP = PWP52K First 52K |
|--|
| PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K |
| PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K |
| PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K |
| PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K |
| PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K |
| PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K |
| PWP = PWP60K First 60K PWP = PWP56K First 56K |
| PWP = PWP56K First 56K |
| |
| PWP = PWP52K First 52K |
| |
| PWP = PWP48K First 48K |
| PWP = PWP44K First 44K |
| PWP = PWP40K First 40K |
| PWP = PWP36K First 36K |
| PWP = PWP32K First 32K |
| PWP = PWP28K First 28K |
| PWP = PWP24K First 24K |
| PWP = PWP20K First 20K |
| PWP = PWP16K First 16K |
| PWP = PWP12K First 12K |
| PWP = PWP8K First 8K |
| PWP = PWP4K First 4K |
| PWP = OFF Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

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SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| PLL Divide by 1 |
|------------------|
| PLL Divide by 2 |
| PLL Divide by 4 |
| PLL Divide by 8 |
| PLL Divide by 16 |
| PLL Divide by 32 |
| PLL Divide by 64 |
| |

| FPLLODIV = DIV_256 | PLL Divide by 256 |
|--------------------|-------------------|
|--------------------|-------------------|

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| <u> </u> | |
|----------------|----------|
| OSCIOFNC = ON | Enabled |
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| <u> </u> | |
|---------------|-------|
| WDTPS = PS1 | 1:1 |
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |

| WDTPS = PS512 | 1:512 |
|-------------------|-----------|
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| D | EBUG = ON | Debugger is enabled |
|---|------------|----------------------|
| D | EBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| <pre>ICESEL = ICS_PGx1</pre> | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|------------------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------------------------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| · · · · · · · · · · · · · · · · · · · | |

| PWP = PWP420K | First 420K |
|---------------|------------|
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| | First 356K |
| PWP = PWP356K | First 352K |
| PWP = PWP352K | |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| | <u> </u> |

| PWP = PWP232K | First 232K |
|--------------------------------|-----------------------|
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 124K |
| PWP = PWP124K PWP = PWP120K | First 124K First 120K |
| PWP = PWP120K PWP = PWP116K | First 116K |
| PWP = PWP110K | First 112K |
| PWP = PWP112K PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| | |

| PWP = PWP44K | First 44K |
|--------------|-----------|
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| I | BWP = ON | Protection Enabled |
|---|-----------|---------------------|
| 1 | BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

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SRS Select:

| SRS Priority 0 |
|----------------|
| SRS Priority 1 |
| SRS Priority 2 |
| SRS Priority 3 |
| SRS Priority 4 |
| SRS Priority 5 |
| SRS Priority 6 |
| SRS Priority 7 |
| |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|------------------|------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |

| FPLLIDIV = DIV_5 | 5x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEI | N = ON | Enabled |
|--------|---------|-----------------------|
| UPLLEI | V = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = | = OFF | Disabled |
|-----------|-------|----------|
| FSOSCEN = | = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled | |
|------------|----------|--|
| IESO = ON | Enabled | |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 | |
|------------------|----------|--|
| WDTPS = PS2 | 1:2 | |
| WDTPS = PS4 | 1:4 | |
| WDTPS = PS8 | 1:8 | |
| WDTPS = PS16 | 1:16 | |
| WDTPS = PS32 | 1:32 | |
| WDTPS = PS64 | 1:64 | |
| WDTPS = PS128 | 1:128 | |
| WDTPS = PS256 | 1:256 | |
| WDTPS = PS512 | 1:512 | |
| WDTPS = PS1024 | 1:1024 | |
| WDTPS = PS2048 | 1:2048 | |
| WDTPS = PS4096 | 1:4096 | |
| WDTPS = PS8192 | 1:8192 | |
| WDTPS = PS16384 | 1:16384 | |
| WDTPS = PS32768 | 1:32768 | |
| WDTPS = PS65536 | 1:65536 | |
| WDTPS = PS131072 | 1:131072 | |
| WDTPS = PS262144 | 1:262144 | |
| WDTPS = PS524288 | 1:524288 | |

| WDTPS = PS1048576 | 1:1048576 | |
|-------------------|-----------|--|
|-------------------|-----------|--|

Watchdog Timer Enable:

| F | WDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|---|-------------|------------------------------------|
| F | WDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| DWD - DWDE12W | First 512K |
|---------------|------------|
| PWP = PWP512K | |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| · | |

| PWP = PWP376K | First 376K |
|---------------|------------|
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |

| PWP = PWP188K | First 188K |
|-----------------|------------|
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| | First 152K |
| PWP = PWP152K | |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| | |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 64K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| TAAT - TAAT AT/ | 1 1100 110 |

| DWD = OFF | Disable | |
|-----------|-----------|--|
| PWP = OFF | l Disable | |
| | | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

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SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |

| FPLLMUL = MUL_17 | 17x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| • | |
|-------------------|-------------|
| UPLLIDIV = DIV_1 | 1x Divider |
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| • | |
|--------------------|-------------------|
| FPLLODIV = DIV_1 | PLL Divide by 1 |
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCE | N = OFF | Disabled |
|--------|---------|----------|
| FSOSCE | N = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| Waterlady Fillier 1 031300 | aici: |
|----------------------------|-----------|
| WDTPS = PS1 | 1:1 |
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |
| | |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| | DEBUG = ON | Debugger is enabled |
|--|------------|---------------------|
|--|------------|---------------------|

| DEBUG = OFF | Debugger is disabled |
|-------------|----------------------|
|-------------|----------------------|

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| Flogram Flash Write Flote | 1 |
|---------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| | • |

| | E: . 0.4014 |
|---------------|-------------|
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| | |

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|---------------|------------|
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| · | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON Protection Enabled |
|----------------------------|
|----------------------------|

| CP = OFF Protection Disabled |
|------------------------------|
|------------------------------|

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SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| <u> </u> | |
|------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = 0 | ON | Enabled |
|------------|-----|-----------------------|
| UPLLEN = 0 | OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|---------------|---------|

|--|

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| Waterlady Timer 1 detect | |
|--------------------------|-----------|
| WDTPS = PS1 | 1:1 |
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| <pre>ICESEL = ICS_PGx1</pre> | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|------------------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
|---------------|------------|

| | F: 5001/ |
|--------------------------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
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| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
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| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
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| PWP = PWP232K First 232K PWP = PWP228K First 228K PWP = PWP224K First 224K PWP = PWP220K First 220K PWP = PWP216K First 216K PWP = PWP212K First 212K PWP = PWP208K First 208K PWP = PWP204K First 204K PWP = PWP204K First 200K PWP = PWP204K First 196K PWP = PWP196K First 192K PWP = PWP198K First 184K PWP = PWP184K First 184K PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP164K First 164K PWP = PWP160K First 164K PWP = PWP156K First 160K PWP = PWP144K First 152K PWP = PWP144K First 152K PWP = PWP144K First 144K PWP = PWP140K First 144K PWP = PWP140K First 140K | PWP = PWP240K | First 240K |
| PWP = PWP228K First 224K PWP = PWP224K First 224K PWP = PWP220K First 220K PWP = PWP216K First 216K PWP = PWP212K First 212K PWP = PWP208K First 204K PWP = PWP204K First 204K PWP = PWP204K First 200K PWP = PWP196K First 196K PWP = PWP192K First 192K PWP = PWP184K First 184K PWP = PWP184K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP148K First 156K PWP = PWP144K First 152K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP236K | First 236K |
| PWP = PWP224K First 224K PWP = PWP216K First 216K PWP = PWP212K First 212K PWP = PWP208K First 208K PWP = PWP204K First 204K PWP = PWP200K First 200K PWP = PWP196K First 196K PWP = PWP192K First 192K PWP = PWP188K First 184K PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP168K First 172K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP152K First 156K PWP = PWP144K First 152K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP140K First 144K PWP = PWP140K First 140K | PWP = PWP232K | First 232K |
| PWP = PWP220K First 220K PWP = PWP216K First 216K PWP = PWP212K First 212K PWP = PWP208K First 208K PWP = PWP204K First 204K PWP = PWP200K First 200K PWP = PWP196K First 196K PWP = PWP192K First 192K PWP = PWP188K First 184K PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP160K First 164K PWP = PWP156K First 156K PWP = PWP152K First 156K PWP = PWP144K First 148K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP228K | First 228K |
| PWP = PWP212K First 212K PWP = PWP208K First 208K PWP = PWP204K First 204K PWP = PWP200K First 200K PWP = PWP196K First 196K PWP = PWP192K First 192K PWP = PWP188K First 184K PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 164K PWP = PWP160K First 164K PWP = PWP156K First 156K PWP = PWP152K First 156K PWP = PWP148K First 144K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP224K | First 224K |
| PWP = PWP212K First 212K PWP = PWP208K First 208K PWP = PWP204K First 204K PWP = PWP200K First 200K PWP = PWP196K First 196K PWP = PWP198K First 192K PWP = PWP188K First 188K PWP = PWP184K First 184K PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP164K First 164K PWP = PWP166K First 164K PWP = PWP156K First 156K PWP = PWP152K First 156K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP220K | First 220K |
| PWP = PWP208K First 204K PWP = PWP204K First 204K PWP = PWP200K First 200K PWP = PWP196K First 196K PWP = PWP192K First 192K PWP = PWP184K First 184K PWP = PWP184K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 164K PWP = PWP164K First 164K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP216K | First 216K |
| PWP = PWP204K First 204K PWP = PWP200K First 200K PWP = PWP196K First 196K PWP = PWP192K First 192K PWP = PWP188K First 188K PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP212K | First 212K |
| PWP = PWP200K First 200K PWP = PWP196K First 196K PWP = PWP192K First 192K PWP = PWP188K First 188K PWP = PWP184K First 184K PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP140K First 140K | PWP = PWP208K | First 208K |
| PWP = PWP196K First 196K PWP = PWP192K First 192K PWP = PWP188K First 188K PWP = PWP184K First 184K PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP204K | First 204K |
| PWP = PWP192K First 192K PWP = PWP188K First 188K PWP = PWP184K First 184K PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP200K | First 200K |
| PWP = PWP188K First 188K PWP = PWP184K First 184K PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP196K | First 196K |
| PWP = PWP184K First 184K PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP192K | First 192K |
| PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 150K PWP = PWP156K First 156K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP188K | First 188K |
| PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP184K | First 184K |
| PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP180K | First 180K |
| PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP176K | First 176K |
| PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP172K | First 172K |
| PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP168K | First 168K |
| PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP164K | First 164K |
| PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP160K | First 160K |
| PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP156K | First 156K |
| PWP = PWP144K First 144K PWP = PWP140K First 140K | PWP = PWP152K | First 152K |
| PWP = PWP140K First 140K | PWP = PWP148K | First 148K |
| | PWP = PWP144K | First 144K |
| PWP = PWP136K First 136K | PWP = PWP140K | First 140K |
| | PWP = PWP136K | First 136K |

| PWP = PWP132K | First 132K |
|---------------|------------|
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX664F128H

SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |

| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| - | |
|------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |

| UPLLIDIV = DIV_12 | 12x Divider | |
|-------------------|-------------|--|
|-------------------|-------------|--|

USB PLL Enable:

| U | PLLEN = ON | Enabled |
|---|-------------|-----------------------|
| U | PLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | | |
|--|-------------------|-----------|
| WDTPS = PS4 | WDTPS = PS1 | 1:1 |
| WDTPS = PS8 | WDTPS = PS2 | 1:2 |
| WDTPS = PS16 | WDTPS = PS4 | 1:4 |
| WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 WDTPS = PS524288 1:524288 | WDTPS = PS8 | 1:8 |
| WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 WDTPS = PS524288 1:524288 | WDTPS = PS16 | 1:16 |
| WDTPS = PS128 | WDTPS = PS32 | 1:32 |
| WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS64 | 1:64 |
| WDTPS = PS512 | WDTPS = PS128 | 1:128 |
| WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS256 | 1:256 |
| WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS512 | 1:512 |
| WDTPS = PS4096 | WDTPS = PS1024 | 1:1024 |
| WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS2048 | 1:2048 |
| WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS4096 | 1:4096 |
| WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS8192 | 1:8192 |
| WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS16384 | 1:16384 |
| WDTPS = PS131072 | WDTPS = PS32768 | 1:32768 |
| WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS65536 | 1:65536 |
| WDTPS = PS524288 1:524288 | WDTPS = PS131072 | 1:131072 |
| | WDTPS = PS262144 | 1:262144 |
| WDTPS = PS1048576 1:1048576 | WDTPS = PS524288 | 1:524288 |
| <u> </u> | WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |

| PMP | | I= |
|---|---------------|------------|
| PWP = PWP468K First 468K PWP = PWP468K First 468K PWP = PWP460K First 460K PWP = PWP450K First 456K PWP = PWP450K First 456K PWP = PWP445K First 456K PWP = PWP444K First 446K PWP = PWP440K First 440K PWP = PWP440K First 440K PWP = PWP430K First 436K PWP = PWP430K First 436K PWP = PWP430K First 426K PWP = PWP420K First 426K PWP = PWP420K First 420K PWP = PWP410K First 420K PWP = PWP40R First 40K PWP = PWP39BK First 30K PWP = PWP39C First 30K PWP = PWP39BK First 38K PWP = PWP38BK First 38K PWP = PWP37CK First 37K PWP = PWP36K First 37K PWP = PWP36K< | PWP = PWP480K | First 480K |
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| PWP = PWP304K First 304K PWP = PWP300K First 300K | PWP = PWP312K | First 312K |
| PWP = PWP300K First 300K | PWP = PWP308K | First 308K |
| | PWP = PWP304K | First 304K |
| PWP = PWP296K First 296K | PWP = PWP300K | First 300K |
| | PWP = PWP296K | First 296K |

| PWP = PWP2 | 92K | First 292K |
|--------------|------|-------------|
| PWP = PWP2 | 88K | First 288K |
| PWP = PWP2 | 84K | First 284K |
| PWP = PWP2 | 80K | First 280K |
| PWP = PWP2 | 76K | First 276K |
| PWP = PWP2 | 72K | First 272K |
| PWP = PWP2 | 68K | First 268K |
| PWP = PWP2 | | First 264K |
| PWP = PWP2 | | First 260K |
| | | First 256K |
| PWP = PWP2 | | |
| PWP = PWP2 | | First 252K |
| PWP = PWP2 | | First 248K |
| PWP = PWP2 | 44K | First 244K |
| PWP = PWP2 | 40K | First 240K |
| PWP = PWP2 | 36K | First 236K |
| PWP = PWP2 | 32K | First 232K |
| PWP = PWP2 | 28K | First 228K |
| PWP = PWP2 | 24K | First 224K |
| PWP = PWP2 | 20K | First 220K |
| PWP = PWP2 | 16K | First 216K |
| PWP = PWP2 | 12K | First 212K |
| PWP = PWP2 | 08K | First 208K |
| PWP = PWP2 | 04K | First 204K |
| PWP = PWP2 | | First 200K |
| PWP = PWP1 | | First 196K |
| PWP = PWP1 | | First 192K |
| PWP = PWP1 | | First 188K |
| PWP = PWP1 | | First 184K |
| PWP = PWP1 | | First 180K |
| PWP = PWP1 | | First 176K |
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| PWP = PWP1 | | First 172K |
| PWP = PWP1 | | First 168K |
| PWP = PWP1 | | First 164K |
| PWP = PWP1 | | First 160K |
| PWP = PWP1 | | First 156K |
| PWP = PWP1 | 52K | First 152K |
| PWP = PWP1 | 48K | First 148K |
| PWP = PWP1 | 44K | First 144K |
| PWP = PWP1 | 40K | First 140K |
| PWP = PWP1 | 36K | First 136K |
| PWP = PWP1 | 32K | First 132K |
| PWP = PWP1 | 28K | First 128K |
| PWP = PWP1 | 24K | First 124K |
| PWP = PWP1 | 20K | First 120K |
| PWP = PWP1 | | First 116K |
| PWP = PWP1 | | First 112K |
| PWP = PWP1 | | First 108K |
| TANT - EAABT | 0010 | 11100 10010 |

| PWP = PWP100K First 100K | |
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| PWP = PWP96K First 96K | |
| PWP = PWP92K First 92K | |
| PWP = PWP88K First 88K | |
| PWP = PWP84K First 84K | |
| PWP = PWP80K First 80K | |
| PWP = PWP76K First 76K | |
| PWP = PWP72K First 72K | |
| PWP = PWP68K First 68K | |
| PWP = PWP64K First 64K | |
| PWP = PWP60K First 60K | |
| PWP = PWP56K First 56K | |
| PWP = PWP52K First 52K | |
| PWP = PWP48K First 48K | |
| PWP = PWP44K First 44K | |
| PWP = PWP40K First 40K | |
| PWP = PWP36K First 36K | |
| PWP = PWP32K First 32K | |
| PWP = PWP28K First 28K | |
| PWP = PWP24K First 24K | |
| PWP = PWP20K First 20K | |
| PWP = PWP16K First 16K | |
| PWP = PWP12K First 12K | |
| PWP = PWP8K First 8K | |
| PWP = PWP4K First 4K | |
| PWP = OFF Disable | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX664F128L

SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| 15x Multiplier |
|----------------|
| 16x Multiplier |
| 17x Multiplier |
| 18x Multiplier |
| 19x Multiplier |
| 20x Multiplier |
| 21x Multiplier |
| 24x Multiplier |
| |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------|-----|

| WDTPS = PS2 | 1:2 |
|-------------------|-----------|
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K First 512K PWP = PWP508K First 508K PWP = PWP504K First 504K PWP = PWP500K First 500K PWP = PWP496K First 496K PWP = PWP492K First 492K PWP = PWP488K First 488K PWP = PWP484K First 484K PWP = PWP480K First 480K PWP = PWP476K First 476K PWP = PWP472K First 472K PWP = PWP468K First 468K PWP = PWP464K First 464K PWP = PWP460K First 460K PWP = PWP456K First 456K | | |
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| PWP = PWP500K First 500K PWP = PWP496K First 496K PWP = PWP492K First 492K PWP = PWP488K First 488K PWP = PWP484K First 484K PWP = PWP480K First 480K PWP = PWP476K First 476K PWP = PWP472K First 472K PWP = PWP468K First 468K PWP = PWP464K First 464K PWP = PWP460K First 460K | PWP = PWP508K | First 508K |
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| 1 | PWP = PWP464K | First 464K |
| PWP = PWP456K First 456K | PWP = PWP460K | First 460K |
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| PWP = PWP452K First 448K PWP = PWP444K First 444K PWP = PWP440K First 444K PWP = PWP42K First 440K PWP = PWP432K First 436K PWP = PWP42K First 42K PWP = PWP416K First 416K PWP = PWP416K First 416K PWP = PWP40K First 40K PWP = PWP40K First 40K PWP = PWP40K First 40K PWP = PWP39K First 396K PWP = PWP39K First 396K PWP = PWP39K First 396K PWP = PWP38K First 396K PWP = PWP38K First 38K PWP = PWP36K First 37CK PWP = PWP372K First 37CK PWP = PWP36K First 36K PWP = PWP36K < | _ | | |
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| PWP = PWP444K First 444K PWP = PWP436K First 436K PWP = PWP432K First 432K PWP = PWP428K First 424K PWP = PWP424K First 424K PWP = PWP420K First 420K PWP = PWP416K First 416K PWP = PWP412K First 410K PWP = PWP408K First 40K PWP = PWP408K First 40K PWP = PWP396K First 40K PWP = PWP396K First 30K PWP = PWP398K First 39ZK PWP = PWP388K First 384K PWP = PWP388K First 386K PWP = PWP376K First 380K PWP = PWP36AK First 360K PWP = PWP34AK First 360K <td< td=""><th>PWP =</th><td>PWP452K</td><td>First 452K</td></td<> | PWP = | PWP452K | First 452K |
| PWP = PWP440K First 440K PWP = PWP436K First 436K PWP = PWP432K First 432K PWP = PWP428K First 428K PWP = PWP420K First 420K PWP = PWP410K First 420K PWP = PWP410K First 416K PWP = PWP408K First 406K PWP = PWP408K First 408K PWP = PWP400K First 400K PWP = PWP396K First 396K PWP = PWP392K First 396K PWP = PWP392K First 396K PWP = PWP388K First 384K PWP = PWP380K First 380K PWP = PWP376K First 376K PWP = PWP376K First 376K PWP = PWP368K First 366K PWP = PWP366K First 360K PWP = PWP360K First 360K PWP = PWP340K First 360K | PWP = | PWP448K | First 448K |
| PWP = PWP436K First 436K PWP = PWP432K First 432K PWP = PWP424K First 428K PWP = PWP424K First 424K PWP = PWP416K First 416K PWP = PWP412K First 416K PWP = PWP408K First 408K PWP = PWP400K First 404K PWP = PWP400K First 400K PWP = PWP396K First 396K PWP = PWP392K First 392K PWP = PWP392K First 392K PWP = PWP384K First 388K PWP = PWP380K First 380K PWP = PWP376K First 372K PWP = PWP372K First 372K PWP = PWP368K First 366K PWP = PWP36AK First 36K PWP = PWP36AK First 36K PWP = PWP36AK First 36K PWP = PWP34AK First 36K <td< td=""><th>PWP =</th><td>PWP444K</td><td>First 444K</td></td<> | PWP = | PWP444K | First 444K |
| PWP = PWP432K First 432K PWP = PWP428K First 428K PWP = PWP420K First 420K PWP = PWP420K First 420K PWP = PWP416K First 412K PWP = PWP412X First 412K PWP = PWP408K First 408K PWP = PWP400K First 404K PWP = PWP400K First 400K PWP = PWP396K First 396K PWP = PWP392X First 392K PWP = PWP398K First 38K PWP = PWP388K First 38K PWP = PWP380K First 38K PWP = PWP376K First 37CK PWP = PWP376K First 37CK PWP = PWP376K First 36K PWP = PWP36AK First 36K PWP = PWP34AK First 34K PWP = PW | PWP = | PWP440K | First 440K |
| PWP = PWP428K First 428K PWP = PWP424K First 424K PWP = PWP426K First 420K PWP = PWP416K First 416K PWP = PWP412K First 412K PWP = PWP408K First 404K PWP = PWP400K First 400K PWP = PWP400K First 396K PWP = PWP392K First 392K PWP = PWP388K First 392K PWP = PWP388K First 392K PWP = PWP388K First 38K PWP = PWP380K First 38K PWP = PWP380K First 380K PWP = PWP376K First 372K PWP = PWP376K First 372K PWP = PWP36K First 36K PWP = PWP36K First 36K PWP = PWP36K First 36K PWP = PWP36K First 35K PWP = PWP35EK First 35K PWP = PWP35EK First 35K PWP = PWP34AK First 35K PWP = PWP34AK First 34K PWP = PWP34AK First 34K PWP = PWP32AK First 32K PWP = PWP32 | PWP = | PWP436K | First 436K |
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| PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP20K | First 20K |
| PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP16K | First 16K |
| PWP = PWP4K First 4K | PWP = PWP12K | First 12K |
| | PWP = PWP8K | First 8K |
| PWP = OFF Disable | PWP = PWP4K | First 4K |
| | PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX675F256H

SRS Select:

| 0.10 00.000 | |
|----------------------|----------------|
| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |
| | |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|-------------------|------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |

| FPLLODIV = DIV_256 | PLL Divide by 256 | |
|--------------------|-------------------|--|
|--------------------|-------------------|--|

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| <u> </u> | |
|----------------|----------|
| OSCIOFNC = ON | Enabled |
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|---------------|-------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |

| WDTPS = PS512 | 1:512 |
|-------------------|-----------|
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FW | DTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|----|------------|------------------------------------|
| FW | DTEN = ON | WDT Enabled |

Background Debugger Enable:

| D | EBUG = ON | Debugger is enabled |
|---|------------|----------------------|
| D | EBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| | |

| PWP = PWP420K | First 420K |
|---------------|------------|
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| | First 356K |
| PWP = PWP356K | First 352K |
| PWP = PWP352K | |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| | <u> </u> |

| First 232K | | |
|--|---------------|------------|
| PWP PWP224K | PWP = PWP232K | First 232K |
| PWP = PWP220K First 220K PWP = PWP216K First 216K PWP = PWP212K First 212K PWP = PWP208K First 208K PWP = PWP200K First 200K PWP = PWP20DK First 200K PWP = PWP192K First 196K PWP = PWP192K First 192K PWP = PWP192K First 192K PWP = PWP184K First 184K PWP = PWP184K First 180K PWP = PWP180K First 180K PWP = PWP16K First 172K PWP = PWP172K First 172K PWP = PWP16K First 168K PWP = PWP16K First 168K PWP = PWP16K First 160K PWP = PWP15K First 160K PWP = PWP16K First 160K PWP = PWP15K First 160K PWP = PWP14K First 140K PWP = PWP12K First 132K PWP = PW | PWP = PWP228K | First 228K |
| PWP = PWP216K First 216K PWP = PWP212K First 212K PWP = PWP204K First 204K PWP = PWP200K First 204K PWP = PWP200K First 200K PWP = PWP196K First 196K PWP = PWP192K First 192K PWP = PWP184K First 192K PWP = PWP184K First 184K PWP = PWP180K First 180K PWP = PWP160K First 170K PWP = PWP172K First 172K PWP = PWP166K First 160K PWP = PWP166K First 156K PWP = PWP166K First 160K PWP = PWP144K First 160K PWP = PWP146K First 140K PWP = PWP126K First 122K PWP = PWP126K First 10K | PWP = PWP224K | First 224K |
| PWP = PWP212K First 212K PWP = PWP204K First 208K PWP = PWP200K First 200K PWP = PWP200K First 200K PWP = PWP196K First 192K PWP = PWP192K First 192K PWP = PWP184K First 188K PWP = PWP184K First 180K PWP = PWP16K First 180K PWP = PWP176K First 160K PWP = PWP172K First 172K PWP = PWP16AK First 160K PWP = PWP16AK First 160K PWP = PWP16AK First 160K PWP = PWP16AK First 150K PWP = PWP16AK First 152K PWP = PWP14AK First 140K PWP = PWP14AK First 140K PWP = PWP13AK First 132K PWP = PWP12AK First 132K PWP = PWP12AK First 120K PWP = PWP14AK First 120K | PWP = PWP220K | First 220K |
| PWP = PWP208K First 208K PWP = PWP204K First 204K PWP = PWP200R First 200K PWP = PWP196K First 196K PWP = PWP196K First 192K PWP = PWP192K First 192K PWP = PWP188K First 184K PWP = PWP188K First 184K PWP = PWP16K First 180K PWP = PWP172K First 176K PWP = PWP172K First 172K PWP = PWP16AK First 160K PWP = PWP16AK First 160K PWP = PWP16AK First 160K PWP = PWP15AK First 160K PWP = PWP15AK First 160K PWP = PWP14AK First 160K PWP = PWP14AK First 144K PWP = PWP14AK First 140K PWP = PWP13AK First 136K PWP = PWP13AK First 132K PWP = PWP12AK First 128K PWP = PWP12AK First 120K PWP = PWP12AK First 120K PWP = PWP10AK First 10K PWP = PWP10AK First 10K <t< th=""><th>PWP = PWP216K</th><th>First 216K</th></t<> | PWP = PWP216K | First 216K |
| PWP = PWP204K First 204K PWP = PWP190K First 200K PWP = PWP192K First 196K PWP = PWP192K First 192K PWP = PWP184K First 184K PWP = PWP184K First 184K PWP = PWP16AK First 176K PWP = PWP172K First 172K PWP = PWP16AK First 164K PWP = PWP16AK First 164K PWP = PWP16AK First 160K PWP = PWP16AK First 156K PWP = PWP14AK First 144K PWP = PWP14AK First 144K PWP = PWP13AK First 136K PWP = PWP13AK First 132K PWP = PWP12AK First 124K PWP = PWP12AK First 120K PWP = PWP12AK First 120K PWP = PWP10AK First 10K | PWP = PWP212K | First 212K |
| PWP = PWP200K First 200K PWP = PWP196K First 196K PWP = PWP196K First 192K PWP = PWP188K First 188K PWP = PWP188K First 184K PWP = PWP180K First 160K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP172K First 168K PWP = PWP164K First 168K PWP = PWP164K First 164K PWP = PWP160K First 166K PWP = PWP152K First 152K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP146K First 144K PWP = PWP136K First 136K PWP = PWP140K First 136K PWP = PWP140K First 136K PWP = PWP128K First 128K PWP = PWP128K First 128K PWP = PWP128K First 128K PWP = PWP128K First 120K PWP = PWP108K First 10K PWP = PWP108K First 10K PWP = PWP108K First 10K <t< th=""><th>PWP = PWP208K</th><th>First 208K</th></t<> | PWP = PWP208K | First 208K |
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| PWP = PWP184K First 184K PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP164K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP144K First 140K PWP = PWP136K First 132K PWP = PWP136K First 132K PWP = PWP132K First 132K PWP = PWP124K First 124K PWP = PWP122K First 120K PWP = PWP104K First 110K PWP = PWP104K First 110K PWP = PWP104K First 10K PWP = PWP26K First 92K PWP = PWP36K First 84K PWP = PWP66K First 80K PWP = | PWP = PWP192K | First 192K |
| PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP164K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP152K First 152K PWP = PWP144K First 152K PWP = PWP144K First 144K PWP = PWP144K First 140K PWP = PWP136K First 136K PWP = PWP136K First 132K PWP = PWP128K First 128K PWP = PWP128K First 124K PWP = PWP120K First 120K PWP = PWP116K First 110K PWP = PWP116K First 110K PWP = PWP108K First 100K PWP = PWP108K First 100K PWP = PWP108K First 100K PWP = PWP20K First 96K PWP = PWP36K First 80K PWP = PWP46K First 80K PWP = PWP6K First 70K PWP = PWP6K First 60K PWP = P | PWP = PWP188K | First 188K |
| PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP140K First 136K PWP = PWP136K First 132K PWP = PWP136K First 132K PWP = PWP128K First 128K PWP = PWP128K First 120K PWP = PWP120K First 120K PWP = PWP100K First 116K PWP = PWP100K First 100K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP98K First 90K PWP = PWP98K First 80K PWP = PWP98K First 80K PWP = PWP66K First 76K PWP = PWP66K First 76K PWP = PWP66K First 76K PWP = PWP66K First 66K PWP = PWP66 | PWP = PWP184K | First 184K |
| PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144R First 144K PWP = PWP144K First 144K PWP = PWP140R First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP124K First 124K PWP = PWP124K First 120K PWP = PWP124K First 120K PWP = PWP112K First 116K PWP = PWP104K First 110K PWP = PWP108K First 108K PWP = PWP104K First 100K PWP = PWP104K First 100K PWP = PWP24K First 96K PWP = PWP92K First 96K PWP = PWP98K First 84K PWP = PWP84K First 80K PWP = PWP76K First 80K PWP = PWP76K First 76K PWP = PWP68K First 68K PWP = PWP | PWP = PWP180K | First 180K |
| PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP120K First 112K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP108K First 108K PWP = PWP100K First 100K PWP = PWP94K First 96K PWP = PWP98K First 96K PWP = PWP88K First 84K PWP = PWP6K First 80K PWP = PWP68K First 80K PWP = PWP68K First 66K PWP = PWP66K First 60K PWP = PWP66K First 60K PWP = PWP56K First 60K PWP = PWP56K </th <th>PWP = PWP176K</th> <th>First 176K</th> | PWP = PWP176K | First 176K |
| PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP122K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP120K First 116K PWP = PWP112K First 112K PWP = PWP104K First 104K PWP = PWP105K First 96K PWP = PWP24K First 96K PWP = PWP34K First 84K PWP = PWP66K First 80K PWP = PWP66K First 66K PWP = PWP66K First 66K PWP = PWP56K First 60K PWP = PW | PWP = PWP172K | First 172K |
| PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP112K First 116K PWP = PWP10AK First 10AK PWP = PWP10AK First 36K PWP = PWP2AK First 8AK PWP = PWP3AK First 8AK PWP = PWP3AK First 8AK PWP = PWP6AK First 76K PWP = PWP7AK First 72K PWP = PWP6AK First 6AK PWP = PW | PWP = PWP168K | First 168K |
| PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP112K First 10K PWP = PWP10AK First 10K PWP = PWP2K First 20K PWP = PWP3AK First 84K PWP = PWP8AK First 84K PWP = PWP8AK First 80K PWP = PWP7AK First 76K PWP = PWP7AK First 72K PWP = PWP6AK First 64K PWP = PWP6AK First 64K PWP = PWP6AK | PWP = PWP164K | First 164K |
| PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP120K First 124K PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP100K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP98K First 98K PWP = PWP88K First 88K PWP = PWP80K First 80K PWP = PWP6K First 76K PWP = PWP72K First 72K PWP = PWP6AK First 68K PWP = PWP6AK First 60K PWP = PWP6AK First 60K PWP = PWP56K First 56K PWP = PWP56K First 50K | PWP = PWP160K | First 160K |
| PWP = PWP148K First 144K PWP = PWP140K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP120K First 124K PWP = PWP120K First 116K PWP = PWP116K First 116K PWP = PWP112K First 108K PWP = PWP108K First 104K PWP = PWP100K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP98K First 92K PWP = PWP88K First 88K PWP = PWP84K First 80K PWP = PWP66K First 76K PWP = PWP72K First 72K PWP = PWP64K First 64K PWP = PWP66K First 60K PWP = PWP56K First 56K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP156K | First 156K |
| PWP = PWP144K First 144K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP124K First 120K PWP = PWP120K First 110K PWP = PWP116K First 112K PWP = PWP108K First 108K PWP = PWP104K First 100K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP84K First 84K PWP = PWP86K First 80K PWP = PWP76K First 80K PWP = PWP66K First 72K PWP = PWP64K First 64K PWP = PWP66K First 64K PWP = PWP66K First 60K PWP = PWP66K First 60K PWP = PWP66K First 56K PWP = PWP52K First 55K | PWP = PWP152K | First 152K |
| PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP122K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP108K First 108K PWP = PWP108K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP98K First 92K PWP = PWP88K First 88K PWP = PWP84K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP66K First 64K PWP = PWP56K First 56K PWP = PWP56K First 56K | PWP = PWP148K | First 148K |
| PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP108K First 108K PWP = PWP108K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP66K First 64K PWP = PWP66K First 64K PWP = PWP66K First 64K PWP = PWP66K First 66K PWP = PWP56K First 56K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP144K | First 144K |
| PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP88K First 88K PWP = PWP88K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP66K First 64K PWP = PWP66K First 66K PWP = PWP56K First 56K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP140K | First 140K |
| PWP = PWP128K First 124K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP60K First 64K PWP = PWP56K First 56K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP136K | First 136K |
| PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 90K PWP = PWP96K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 76K PWP = PWP76K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP66K First 64K PWP = PWP56K First 56K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP132K | First 132K |
| PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP56K First 50K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP128K | First 128K |
| PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP64K First 68K PWP = PWP64K First 64K PWP = PWP56K First 50K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP124K | First 124K |
| PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP120K | First 120K |
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| PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP108K | First 108K |
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| PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP100K | First 100K |
| PWP = PWP88K First 88K PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP96K | First 96K |
| PWP = PWP84K First 84K PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP92K | First 92K |
| PWP = PWP80K First 80K PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP88K | First 88K |
| PWP = PWP76K First 76K PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP84K | First 84K |
| PWP = PWP72K First 72K PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP80K | First 80K |
| PWP = PWP68K First 68K PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP76K | First 76K |
| PWP = PWP64K First 64K PWP = PWP60K First 60K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP72K | First 72K |
| PWP = PWP60K First 60K PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP68K | First 68K |
| PWP = PWP56K First 56K PWP = PWP52K First 52K | PWP = PWP64K | First 64K |
| PWP = PWP52K First 52K | PWP = PWP60K | First 60K |
| | PWP = PWP56K | First 56K |
| PWP = PWP48K First 48K | PWP = PWP52K | First 52K |
| | PWP = PWP48K | First 48K |

| PWP = PWP44K | First 44K |
|--------------|-----------|
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| I | BWP = ON | Protection Enabled |
|---|-----------|---------------------|
| 1 | BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX675F256L

SRS Select:

| SRS Priority 0 |
|----------------|
| SRS Priority 1 |
| SRS Priority 2 |
| SRS Priority 3 |
| SRS Priority 4 |
| SRS Priority 5 |
| SRS Priority 6 |
| SRS Priority 7 |
| |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 1x Divider |
|-------------------------------|
|-------------------------------|

| FPLLIDIV = DIV_2 | 2x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| • | |
|-------------------|-------------|
| UPLLIDIV = DIV_1 | 1x Divider |
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|----------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |

| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
|------------------|----------------------------------|
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-----------------|---------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |

| WDTPS = PS131072 | 1:131072 |
|-------------------|-----------|
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| | |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| | I |

| PWP = PWP388K First 388K PWP = PWP380K First 380K PWP = PWP376K First 376K PWP = PWP372K First 376K PWP = PWP368K First 368K PWP = PWP364K First 368K PWP = PWP360K First 360K PWP = PWP356K First 350K PWP = PWP356K First 352K PWP = PWP356K First 352K PWP = PWP356K First 352K PWP = PWP348K First 348K PWP = PWP348K First 344K PWP = PWP340K First 344K PWP = PWP336K First 336K PWP = PWP332K First 336K PWP = PWP328K First 336K PWP = PWP328K First 322K PWP = PWP328K First 322K PWP = PWP320K First 320K PWP = PWP320K First 320K PWP = PWP312K First 316K PWP = PWP312K First 316K PWP = PWP300K First 30K PWP = PWP300K First 30K PWP = PWP300K First 20K <t< th=""><th></th><th></th></t<> | | |
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| PWP = PWP232K First 232K PWP = PWP228K First 228K PWP = PWP224K First 224K PWP = PWP220K First 220K PWP = PWP216K First 216K PWP = PWP212K First 212K PWP = PWP208K First 208K | PWP = PWP240K | First 240K |
| PWP = PWP228K First 228K PWP = PWP224K First 224K PWP = PWP220K First 220K PWP = PWP216K First 216K PWP = PWP212K First 212K PWP = PWP208K First 208K | PWP = PWP236K | First 236K |
| PWP = PWP224K First 224K PWP = PWP220K First 220K PWP = PWP216K First 216K PWP = PWP212K First 212K PWP = PWP208K First 208K | PWP = PWP232K | First 232K |
| PWP = PWP220K First 220K PWP = PWP216K First 216K PWP = PWP212K First 212K PWP = PWP208K First 208K | PWP = PWP228K | First 228K |
| PWP = PWP216K First 216K PWP = PWP212K First 212K PWP = PWP208K First 208K | PWP = PWP224K | First 224K |
| PWP = PWP212K First 212K PWP = PWP208K First 208K | PWP = PWP220K | First 220K |
| PWP = PWP208K First 208K | PWP = PWP216K | First 216K |
| | PWP = PWP212K | First 212K |
| PWP = PWP204K First 204K | PWP = PWP208K | First 208K |
| | PWP = PWP204K | First 204K |

| | T |
|---------------|------------|
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |

| PWP = PWP12K | First 12K |
|--------------|-----------|
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX675F512H

SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETH | IO = OFF | Alternate Ethernet I/O |
|------|----------|------------------------|
| FETH | IO = ON | Default Ethernet I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| Cycloin i == Calpat Cicon | 211.001 |
|---------------------------|-------------------|
| FPLLODIV = DIV_1 | PLL Divide by 1 |
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |
| | |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|

| IESO = ON Enabled | IESO = ON | Enabled |
|-------------------|-----------|---------|
|-------------------|-----------|---------|

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |
| | |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| <pre>ICESEL = ICS_PGx1</pre> | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|------------------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| - 10 g. a 1 10.011 111110 1 1 0 100 | |
|-------------------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| | |

| <u> </u> | T=1 |
|---------------|------------|
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| | |

| | E: |
|---------------|------------|
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX675F512L

SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |

| EDITMII - MIII 24 | 24x Multiplion |
|-------------------|----------------|
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| <u> </u> | |
|--------------------|-------------------|
| FPLLODIV = DIV_1 | PLL Divide by 1 |
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| , | • | |
|-----------|-----|----------------------|
| POSCMOD = | EC | External clock mode |
| POSCMOD = | XT | XT osc mode |
| POSCMOD = | HS | HS osc mode |
| POSCMOD = | OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | waterideg rillier i estace | |
|--|----------------------------|-----------|
| WDTPS = PS4 | WDTPS = PS1 | 1:1 |
| WDTPS = PS8 | WDTPS = PS2 | 1:2 |
| WDTPS = PS16 | WDTPS = PS4 | 1:4 |
| WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 WDTPS = PS524288 1:524288 | WDTPS = PS8 | 1:8 |
| WDTPS = PS64 | WDTPS = PS16 | 1:16 |
| WDTPS = PS128 | WDTPS = PS32 | 1:32 |
| WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS64 | 1:64 |
| WDTPS = PS512 | WDTPS = PS128 | 1:128 |
| WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS256 | 1:256 |
| WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS512 | 1:512 |
| WDTPS = PS4096 | WDTPS = PS1024 | 1:1024 |
| WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS2048 | 1:2048 |
| WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS4096 | 1:4096 |
| WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS8192 | 1:8192 |
| WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS16384 | 1:16384 |
| WDTPS = PS131072 | WDTPS = PS32768 | 1:32768 |
| WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS65536 | 1:65536 |
| WDTPS = PS524288 1:524288 | WDTPS = PS131072 | 1:131072 |
| | WDTPS = PS262144 | 1:262144 |
| WDTPS = PS1048576 1:1048576 | WDTPS = PS524288 | 1:524288 |
| | WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| 1 Togram Flash Write Frote | / \ 1 |
|----------------------------|--------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| | |

| | I = |
|---------------|------------|
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP146K | First 144K |

| | _ _ |
|---------------|----------------|
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX695F512H

SRS Select:

| TGDGGTI DDTGDTTII A | CDC Driewith O |
|----------------------|----------------|
| FSRSSEL = PRIORITY_0 | SRS Priority 0 |

| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| • | |
|------------------|----------------|
| FPLLMUL = MUL_15 | 15x Multiplier |
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|------------------|------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |

| UPLLIDIV = DIV_ | _6 | 6x Divider |
|-----------------|-----|-------------|
| UPLLIDIV = DIV_ | _10 | 10x Divider |
| UPLLIDIV = DIV_ | _12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCK | SM = CSECME | Clock Switch Enable, FSCM Enabled |
|-----|-------------|-------------------------------------|
| FCK | SM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCK | SM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | | | |
|--|-------------------|-----------|--|
| WDTPS = PS4 | WDTPS = PS1 | 1:1 | |
| WDTPS = PS8 | WDTPS = PS2 | 1:2 | |
| WDTPS = PS16 | WDTPS = PS4 | 1:4 | |
| WDTPS = PS32 1:32 WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 WDTPS = PS524288 1:524288 | WDTPS = PS8 | 1:8 | |
| WDTPS = PS64 1:64 WDTPS = PS128 1:128 WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 WDTPS = PS524288 1:524288 | WDTPS = PS16 | 1:16 | |
| WDTPS = PS128 | WDTPS = PS32 | 1:32 | |
| WDTPS = PS256 1:256 WDTPS = PS512 1:512 WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS64 | 1:64 | |
| WDTPS = PS512 | WDTPS = PS128 | 1:128 | |
| WDTPS = PS1024 1:1024 WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS256 | 1:256 | |
| WDTPS = PS2048 1:2048 WDTPS = PS4096 1:4096 WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS512 | 1:512 | |
| WDTPS = PS4096 | WDTPS = PS1024 | 1:1024 | |
| WDTPS = PS8192 1:8192 WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS2048 | 1:2048 | |
| WDTPS = PS16384 1:16384 WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS4096 | 1:4096 | |
| WDTPS = PS32768 1:32768 WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS8192 | 1:8192 | |
| WDTPS = PS65536 1:65536 WDTPS = PS131072 1:131072 WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS16384 | 1:16384 | |
| WDTPS = PS131072 | WDTPS = PS32768 | 1:32768 | |
| WDTPS = PS262144 1:262144 WDTPS = PS524288 1:524288 | WDTPS = PS65536 | 1:65536 | |
| WDTPS = PS524288 1:524288 | WDTPS = PS131072 | 1:131072 | |
| | WDTPS = PS262144 | 1:262144 | |
| WDTPS = PS1048576 1:1048576 | WDTPS = PS524288 | 1:524288 | |
| <u> </u> | WDTPS = PS1048576 | 1:1048576 | |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |

| PMP | | I= |
|---|---------------|------------|
| PWP = PWP468K First 468K PWP = PWP468K First 468K PWP = PWP460K First 460K PWP = PWP450K First 456K PWP = PWP450K First 456K PWP = PWP445K First 456K PWP = PWP444K First 446K PWP = PWP440K First 440K PWP = PWP440K First 440K PWP = PWP430K First 436K PWP = PWP430K First 436K PWP = PWP430K First 426K PWP = PWP420K First 426K PWP = PWP420K First 420K PWP = PWP410K First 420K PWP = PWP40R First 40K PWP = PWP39BK First 30K PWP = PWP39C First 30K PWP = PWP39BK First 38K PWP = PWP38BK First 38K PWP = PWP37CK First 37K PWP = PWP36K First 37K PWP = PWP36K< | PWP = PWP480K | First 480K |
| PWP = PWP468K First 468K PWP = PWP464K First 464K PWP = PWP460K First 460K PWP = PWP456K First 456K PWP = PWP452X First 456K PWP = PWP448K First 452K PWP = PWP444K First 440K PWP = PWP440K First 440K PWP = PWP436K First 436K PWP = PWP432K First 436K PWP = PWP428K First 426K PWP = PWP424K First 426K PWP = PWP420K First 420K PWP = PWP420K First 420K PWP = PWP410K First 40K PWP = PWP410K First 40K PWP = PWP40R First 40K PWP = PWP40R First 40K PWP = PWP40R First 40K PWP = PWP30R First 40K PWP = PWP39R First 40K PWP = PWP39R First 39CK PWP = PWP39R First 38K PWP = PWP39R First 38K PWP = PWP38R First 38K PWP = PWP37AK First 36K PWP = PWP37AK </td <td>PWP = PWP476K</td> <td></td> | PWP = PWP476K | |
| PWP = PWP464K First 460K PWP = PWP450K First 450K PWP = PWP452K First 450K PWP = PWP452K First 450K PWP = PWP448K First 444K PWP = PWP440K First 440K PWP = PWP430K First 430K PWP = PWP424K First 432K PWP = PWP424K First 422K PWP = PWP424K First 426K PWP = PWP420K First 420K PWP = PWP410K First 420K PWP = PWP410K First 420K PWP = PWP410K First 40K PWP = PWP40AK First 40K PWP = PWP40AK First 40K PWP = PWP40AK First 40K PWP = PWP39AK First 390K PWP = PWP39AK First 390K PWP = PWP38AK First 380K PWP = PWP38AK First 380K PWP = PWP36AK First 370K PWP = PWP36AK First 36K PWP | PWP = PWP472K | First 472K |
| PWP = PWP460K First 456K PWP = PWP456K First 456K PWP = PWP452K First 452K PWP = PWP448K First 444K PWP = PWP444K First 444K PWP = PWP440K First 440K PWP = PWP436K First 436K PWP = PWP428K First 432K PWP = PWP428K First 424K PWP = PWP420K First 420K PWP = PWP41CK First 420K PWP = PWP41CK First 416K PWP = PWP41CK First 40K PWP = PWP40K First 40K PWP = PWP40K First 40K PWP = PWP40K First 40K PWP = PWP396K First 396K PWP = PWP396K First 396K PWP = PWP392K First 38K PWP = PWP392K First 38K PWP = PWP386K First 38K PWP = PWP397E First 38K PWP = PWP386K First 38K PWP = PWP376K First 36K PWP = PWP376K First 36K PWP = PWP36K First 36K PWP = PWP36 | PWP = PWP468K | First 468K |
| PWB = PWP456K First 456K PWP = PWP452K First 452K PWB = PWP444K First 444K PWP = PWP440K First 444K PWP = PWP440K First 444K PWP = PWP436K First 436K PWP = PWP428K First 432K PWP = PWP428K First 426K PWP = PWP420K First 420K PWP = PWP416K First 420K PWP = PWP416K First 416K PWP = PWP410K First 40K PWP = PWP40K First 40K PWP = PWP40K First 40K PWP = PWP40K First 30K PWP = PWP40K First 40K PWP = PWP40K First 40K PWP = PWP40K First 30K PWP = PWP39EK First 39K PWP = PWP39EK First 39K PWP = PWP39EK First 38K PWP = PWP38EK First 38K PWP = PWP38EK First 38K PWP = PWP38EK First 37K PWP = PWP37EK First 37K PWP = PWP36EK First 36K PWP = PWP36EK <th>PWP = PWP464K</th> <th>First 464K</th> | PWP = PWP464K | First 464K |
| PWP = PWP452K First 452K PWP = PWP444K First 444K PWP = PWP440K First 444K PWP = PWP440K First 440K PWP = PWP436K First 436K PWP = PWP422K First 432K PWP = PWP428K First 428K PWP = PWP424K First 420K PWP = PWP416K First 420K PWP = PWP416K First 412K PWP = PWP408K First 40K PWP = PWP404K First 40K PWP = PWP400K First 40K PWP = PWP400K First 396K PWP = PWP396K First 396K PWP = PWP392K First 396K PWP = PWP384K First 38K PWP = PWP384K First 38K PWP = PWP376K First 376K PWP = PWP368K First 376K PWP = PWP368K First 36K PWP = PWP344K First 36K PWP = | PWP = PWP460K | First 460K |
| PWP = PWP448K First 444K PWP = PWP440K First 444K PWP = PWP440K First 440K PWP = PWP432K First 436K PWP = PWP432K First 432K PWP = PWP428K First 426K PWP = PWP420K First 426K PWP = PWP410K First 420K PWP = PWP410K First 416K PWP = PWP408K First 408K PWP = PWP408K First 408K PWP = PWP408K First 400K PWP = PWP396K First 396K PWP = PWP3976K First 396K PWP = PWP398K First 388K PWP = PWP388K First 386K PWP = PWP380K First 386K PWP = PWP376K First 376K PWP = PW936K First 36K PWP = PW93 | PWP = PWP456K | First 456K |
| PWP = PWP444K First 444K PWP = PWP440K First 440K PWP = PWP436K First 436K PWP = PWP428K First 432K PWP = PWP428K First 428K PWP = PWP424K First 424K PWP = PWP420K First 420K PWP = PWP416K First 416K PWP = PWP412K First 412K PWP = PWP40AK First 40K PWP = PWP40AK First 40K PWP = PWP40AK First 40K PWP = PWP39AK First 396K PWP = PWP39AK First 396K PWP = PWP39AK First 38K PWP = PWP38AK First 37K PWP = PWP37CK First 37K PWP = PWP36AK First 36K PWP = PWP35AK First 36K PWP = PWP3 | PWP = PWP452K | First 452K |
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| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |

| PWP = PV | WP104K | First 104K |
|----------|--------|------------|
| PWP = PV | WP100K | First 100K |
| PWP = PV | WP96K | First 96K |
| PWP = PV | WP92K | First 92K |
| PWP = PI | WP88K | First 88K |
| PWP = PI | WP84K | First 84K |
| PWP = PV | WP80K | First 80K |
| PWP = PV | WP76K | First 76K |
| PWP = PV | WP72K | First 72K |
| PWP = PV | WP68K | First 68K |
| PWP = PV | WP64K | First 64K |
| PWP = PV | WP60K | First 60K |
| PWP = PV | WP56K | First 56K |
| PWP = PV | WP52K | First 52K |
| PWP = PV | WP48K | First 48K |
| PWP = PV | WP44K | First 44K |
| PWP = PV | WP40K | First 40K |
| PWP = PV | WP36K | First 36K |
| PWP = PV | WP32K | First 32K |
| PWP = PV | WP28K | First 28K |
| PWP = PV | WP24K | First 24K |
| PWP = PV | WP20K | First 20K |
| PWP = PV | WP16K | First 16K |
| PWP = PV | WP12K | First 12K |
| PWP = PV | WP8K | First 8K |
| PWP = PV | WP4K | First 4K |
| PWP = O | FF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX695F512L

SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| 15x Multiplier |
|----------------|
| 16x Multiplier |
| 17x Multiplier |
| 18x Multiplier |
| 19x Multiplier |
| 20x Multiplier |
| 21x Multiplier |
| 24x Multiplier |
| |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------|-----|

| WDTPS = PS2 | 1:2 |
|-------------------|-----------|
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| | |

| PWP = PWP452K | First 452K |
|---------------|-------------|
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
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| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| | First 336K |
| PWP = PWP336K | |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| INT - LMLZOOV | 1 1131 2001 |

| PMP | | TE: 400.00 |
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| PWP = PWP256K First 256K PWP = PWP248K First 252K PWP = PWP248K First 244K PWP = PWP240K First 240K PWP = PWP240K First 240K PWP = PWP236K First 236K PWP = PWP232K First 232K PWP = PWP232K First 228K PWP = PWP220K First 224K PWP = PWP220K First 220K PWP = PWP21CK First 216K PWP = PWP21CK First 20K PWP = PWP20CK First 192K PWP = PWP20CK First 192K PWP = PWP196K First 192K PWP = PWP192CK First 184K PWP = PWP184K First 180K PWP = PWP16K First 164K PWP | PWP = PWP264K | First 264K |
| PWP = PWP252K First 252K PWP = PWP248K First 248K PWP = PWP244K First 240K PWP = PWP236K First 240K PWP = PWP232K First 236K PWP = PWP232K First 232K PWP = PWP22BK First 224K PWP = PWP22BK First 224K PWP = PWP22BK First 220K PWP = PWP21BK First 20K PWP = PWP20BK First 20K PWP = PWP19EK First 19EK PWP = PWP19EK First 18EK PWP = PWP19EK First 18EK PWP = PWP19EK First 18EK PWP = PWP18EK First 18EK PWP = PWP18EK First 17EK PWP = PWP17EK First 17EK PWP = PWP16EK First 16EK PWP = PWP15EK First 16EK PWP | | |
| PWP = PWP244K First 244K PWP = PWP244K First 244K PWP = PWP236K First 240K PWP = PWP236K First 236K PWP = PWP232K First 232K PWP = PWP222K First 22K PWP = PWP224K First 22K PWP = PWP220K First 22K PWP = PWP220K First 22K PWP = PWP220K First 216K PWP = PWP220K First 216K PWP = PWP20AK First 20K PWP = PWP19AK First 19CK PWP = PWP19AK First 19CK PWP = PWP19AK First 18K PWP = PWP18AK First 18K PWP = PWP17AK First 17CK PWP = PWP16AK First 16K PWP = PWP16AK First 16K PWP = PWP16 | PWP = PWP256K | |
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| PWP = PWP236K First 236K PWP = PWP232K First 232K PWP = PWP228K First 228K PWP = PWP224K First 224K PWP = PWP220K First 220K PWP = PWP216K First 216K PWP = PWP212K First 212K PWP = PWP20K First 20K PWP = PWP196K First 196K PWP = PWP196K First 192K PWP = PWP188K First 192K PWP = PWP188K First 180K PWP = PWP180K First 180K PWP = PWP172K First 172K PWP = PWP168K First 160K PWP = PWP168K First 166K PWP = PWP168K First 160K PWP = PWP168K First 144K PWP = | PWP = PWP244K | First 244K |
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| PWP = PWP188K First 188K PWP = PWP184K First 184K PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP140K First 136K PWP = PWP132K First 132K PWP = PWP132K First 132K PWP = PWP124K First 124K PWP = PWP124K First 124K PWP = PWP104K First 120K PWP = PWP104K First 10K PWP = PWP104K First 104K PWP = PWP104K First 104K PWP = PWP104K First 104K PWP = PWP20K First 104K | PWP = PWP196K | First 196K |
| PWP = PWP184K First 184K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 172K PWP = PWP164K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP124K First 124K PWP = PWP124K First 124K PWP = PWP116K First 116K PWP = PWP116K First 116K PWP = PWP100K First 100K PWP = PWP104K First 100K PWP = PWP100K First 100K PWP = PWP20K First 96K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP192K | First 192K |
| PWP = PWP180K First 180K PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 144K PWP = PWP144K First 144K PWP = PWP140K First 136K PWP = PWP136K First 132K PWP = PWP128K First 132K PWP = PWP128K First 124K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP104K First 100K PWP = PWP104K First 104K PWP = PWP104K First 100K PWP = PWP20K First 96K PWP = PWP98K First 98K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP188K | First 188K |
| PWP = PWP176K First 176K PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 152K PWP = PWP152K First 152K PWP = PWP144K First 148K PWP = PWP144K First 144K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP104K First 104K | PWP = PWP184K | First 184K |
| PWP = PWP172K First 172K PWP = PWP168K First 168K PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP144K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 128K PWP = PWP128K First 124K PWP = PWP124K First 120K PWP = PWP120K First 116K PWP = PWP116K First 116K PWP = PWP108K First 108K PWP = PWP100K First 10K PWP = PWP20K First 92K PWP = PWP84K First 84K | PWP = PWP180K | First 180K |
| PWP = PWP168K First 168K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 136K PWP = PWP136K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP124K First 120K PWP = PWP116K First 116K PWP = PWP116K First 116K PWP = PWP108K First 108K PWP = PWP108K First 108K PWP = PWP108K First 100K PWP = PWP108K First 96K PWP = PWP98K First 98K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP176K | First 176K |
| PWP = PWP164K First 164K PWP = PWP160K First 160K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP124K First 124K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP108K First 10K PWP = PWP104K First 104K PWP = PWP104K First 10K PWP = PWP96K First 96K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP172K | First 172K |
| PWP = PWP160K First 150K PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP100K First 110K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP98K First 92K PWP = PWP84K First 88K PWP = PWP84K First 84K | PWP = PWP168K | First 168K |
| PWP = PWP156K First 156K PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP84K First 88K PWP = PWP84K First 84K | PWP = PWP164K | First 164K |
| PWP = PWP152K First 152K PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP160K | First 160K |
| PWP = PWP148K First 148K PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP156K | First 156K |
| PWP = PWP144K First 144K PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP100K First 104K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP152K | First 152K |
| PWP = PWP140K First 140K PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP108K First 108K PWP = PWP108K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP84K First 84K PWP = PWP84K First 84K | PWP = PWP148K | First 148K |
| PWP = PWP136K First 136K PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP84K First 88K PWP = PWP84K First 84K | | |
| PWP = PWP132K First 132K PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP108K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP140K | First 140K |
| PWP = PWP128K First 128K PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP84K First 88K PWP = PWP84K First 84K | PWP = PWP136K | First 136K |
| PWP = PWP124K First 124K PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP132K | First 132K |
| PWP = PWP120K First 120K PWP = PWP116K First 116K PWP = PWP12K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP128K | First 128K |
| PWP = PWP116K First 116K PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP124K | First 124K |
| PWP = PWP112K First 112K PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP120K | First 120K |
| PWP = PWP108K First 108K PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP116K | First 116K |
| PWP = PWP104K First 104K PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP112K | First 112K |
| PWP = PWP100K First 100K PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP108K | |
| PWP = PWP96K First 96K PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP104K | First 104K |
| PWP = PWP92K First 92K PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP100K | First 100K |
| PWP = PWP88K First 88K PWP = PWP84K First 84K | PWP = PWP96K | |
| PWP = PWP84K First 84K | PWP = PWP92K | First 92K |
| | PWP = PWP88K | First 88K |
| PWP = PWP80K First 80K | PWP = PWP84K | First 84K |
| | PWP = PWP80K | First 80K |

| First 76K |
|-----------|
| First 72K |
| First 68K |
| First 64K |
| First 60K |
| First 56K |
| First 52K |
| First 48K |
| First 44K |
| First 40K |
| First 36K |
| First 32K |
| First 28K |
| First 24K |
| First 20K |
| First 16K |
| First 12K |
| First 8K |
| First 4K |
| Disable |
| |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX764F128H

SRS Select:

| 0110 0010011 | |
|----------------------|----------------|
| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|------------------|-----------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |

| FPLLODIV = DIV_8 | PLL Divide by 8 |
|--------------------|-------------------|
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|--------------|------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |

| WDTPS = PS32 | 1:32 |
|-------------------|-----------|
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| irst 512K |
|------------|
| iirst 508K |
| irst 504K |
| irst 500K |
| irst 496K |
| irst 492K |
| irst 488K |
| irst 484K |
| irst 480K |
| irst 476K |
| irst 472K |
| irst 468K |
| irst 464K |
| iirst 460K |
| irst 456K |
| irst 452K |
| irst 448K |
| irst 444K |
| irst 440K |
| |

| PWP = PWP436K | First 436K |
|---------------------------------------|------------|
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
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| PWP = PWP248K | First 248K |
|---------------------------------------|------------|
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
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| PWP = PWP56K First 50K PWP = PWP52K First 52K PWP = PWP48K First 48K PWP = PWP44K First 44K PWP = PWP40K First 40K PWP = PWP36K First 36K PWP = PWP32K First 32K PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K PWP = PWP4K First 4K | | |
|--|--------------|-----------|
| PWP = PWP52K First 52K PWP = PWP48K First 48K PWP = PWP44K First 44K PWP = PWP40K First 40K PWP = PWP36K First 36K PWP = PWP32K First 32K PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP4K First 8K PWP = PWP4K First 4K | PWP = PWP60K | First 60K |
| PWP = PWP48K First 48K PWP = PWP44K First 44K PWP = PWP40K First 40K PWP = PWP36K First 36K PWP = PWP32K First 32K PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP4K First 8K PWP = PWP4K First 4K | PWP = PWP56K | First 56K |
| PWP = PWP44K First 44K PWP = PWP40K First 40K PWP = PWP36K First 36K PWP = PWP32K First 32K PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP52K | First 52K |
| PWP = PWP40K First 40K PWP = PWP36K First 36K PWP = PWP32K First 32K PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP48K | First 48K |
| PWP = PWP36K First 36K PWP = PWP32K First 32K PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP44K | First 44K |
| PWP = PWP32K First 32K PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP40K | First 40K |
| PWP = PWP28K First 28K PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP36K | First 36K |
| PWP = PWP24K First 24K PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP32K | First 32K |
| PWP = PWP20K First 20K PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP28K | First 28K |
| PWP = PWP16K First 16K PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP24K | First 24K |
| PWP = PWP12K First 12K PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP20K | First 20K |
| PWP = PWP8K First 8K PWP = PWP4K First 4K | PWP = PWP16K | First 16K |
| PWP = PWP4K First 4K | PWP = PWP12K | First 12K |
| | PWP = PWP8K | First 8K |
| PWP = OFF Disable | PWP = PWP4K | First 4K |
| | PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX764F128L

SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|-------------------|------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |

| FPLLODIV = DIV_256 | PLL Divide by 256 |
|--------------------|-------------------|
|--------------------|-------------------|

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| <u> </u> | |
|----------------|----------|
| OSCIOFNC = ON | Enabled |
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|---------------|-------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |

| WDTPS = PS512 | 1:512 |
|-------------------|-----------|
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FW | DTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|----|------------|------------------------------------|
| FW | DTEN = ON | WDT Enabled |

Background Debugger Enable:

| D | EBUG = ON | Debugger is enabled |
|---|------------|----------------------|
| D | EBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| <pre>ICESEL = ICS_PGx1</pre> | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|------------------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------------------------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| · · · · · · · · · · · · · · · · · · · | |

| PWP = PWP420K | First 420K |
|---------------|--------------|
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| | First 384K |
| PWP = PWP384K | |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| | |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| INT - EMEZZOV | 1 1131 2001X |

| | 1 |
|---------------|------------|
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| | First 196K |
| PWP = PWP196K | |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| | |
| PWP = PWP112K | First 109K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| IWI TOIL | |

| PWP = PWP44K | First 44K |
|--------------|-----------|
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| I | BWP = ON | Protection Enabled |
|---|-----------|---------------------|
| 1 | BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX775F256H

SRS Select:

| SRS Priority 0 |
|----------------|
| SRS Priority 1 |
| SRS Priority 2 |
| SRS Priority 3 |
| SRS Priority 4 |
| SRS Priority 5 |
| SRS Priority 6 |
| SRS Priority 7 |
| |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
|-----------------|-----------------------------|

| FVBUSONIO = ON | Controlled by USB Module |
|----------------|--------------------------|

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| • | |
|--------------------|-------------------|
| FPLLODIV = DIV_1 | PLL Divide by 1 |
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|----------------|------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |

| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
|------------------|------------------------------------|
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|----------------|--------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |

| WDTPS = PS16384 | 1:16384 |
|-------------------|-----------|
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| | - |
|---------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| | |

| PWP = PWP400K | First 400K |
|---------------|---------------|
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| | First 364K |
| PWP = PWP364K | |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| | |
| PWP = PWP280K | First 270K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| INT - EMESTOU | 1 1130 2 1010 |

| PWP = PWP212K | First 212K |
|---------------|-------------|
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| | First 176K |
| PWP = PWP176K | |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| | |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| INVI - EMEZOK | 1 1101 2011 |

| PWP = PWP24K | First 24K |
|--------------|-----------|
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX775F256L

SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|------------------|------------|
| FPLLIDIV = DIV_2 | 2x Divider |

| FPLLIDIV = DIV_3 | 3x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |

| I | FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |
|---|----------------|----------------------------------|
| - | FNOSC = FRCDIV | Fast RC Osc w/Div-by-in (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| 1:1 |
|----------|
| 1:2 |
| 1:4 |
| 1:8 |
| 1:16 |
| 1:32 |
| 1:64 |
| 1:128 |
| 1:256 |
| 1:512 |
| 1:1024 |
| 1:2048 |
| 1:4096 |
| 1:8192 |
| 1:16384 |
| 1:32768 |
| 1:65536 |
| 1:131072 |
| |

| WDTPS = PS262144 | 1:262144 |
|-------------------|-----------|
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| Flogram Flash Wille Flote | , i. |
|---------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| | |

| PWP = PWP384K | First 384K |
|--------------------------------|------------|
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP332K PWP = PWP328K | First 328K |
| | First 324K |
| PWP = PWP324K | First 320K |
| PWP = PWP320K | First 316K |
| PWP = PWP316K | |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| | |

| | I = 1 |
|---------------|------------|
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| · | |

| PWP = PWP8K | First 8K |
|-------------|----------|
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX775F512H

SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | י | Controlled by Port Function |
|----------------|---|------------------------------|
| FUSBIDIO = ON | | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|------------------|------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |

| FPLLIDIV = DIV_10 | 10x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

|] | IESO = OFF | Disabled |
|---|------------|----------|
| | IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| WDTPS = PS1 | 1:1 |
|-------------------|-----------|
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |
| | |

Watchdog Timer Enable:

| FWDTEN = OFF WDT Disabled (SWDTEN Bit Controls) |
|---|
|---|

| FWDTEN = ON | WDT Enabled | |
|-------------|-------------|--|
|-------------|-------------|--|

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| - 10 gram 1 mon 111110 1 10100 | <u></u> |
|--------------------------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| | |

| PWP = PWP360K | First 360K |
|---------------|------------|
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| | First 296K |
| PWP = PWP296K | First 292K |
| PWP = PWP292K | |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| <u> </u> | |

| PWP = PWP172K | First 172K |
|---------------|------------|
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX775F512L

SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |

| FPLLMUL = MUL_19 | 19x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| Ţ | JPLLEN = ON | Enabled |
|---|--------------|-----------------------|
| Ţ | JPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |
| | |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|--------------|---------------------|
| POSCMOD = XT | XT osc mode |

| POSCMOD = HS | HS osc mode |
|---------------|----------------------|
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| watching filler Postsca | ici. |
|-------------------------|-----------|
| WDTPS = PS1 | 1:1 |
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|--------------------------------|--------------------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP490K | First 490K |
| | First 488K |
| PWP = PWP488K PWP = PWP484K | First 484K |
| | First 480K |
| PWP = PWP480K | First 476K |
| PWP = PWP476K | |
| PWP = PWP472K | First 472K First 468K |
| PWP = PWP468K | First 464K |
| PWP = PWP464K | |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K First 452K |
| PWP = PWP452K | |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| | |

| | T=1 |
|---------------|------------|
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| | |

| | T= |
|---------------|------------|
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX795F512H

SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |
| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| FPLLMUL = MUL_15 | 15x Multiplier |
|------------------|----------------|
| FPLLMUL = MUL_16 | 16x Multiplier |
| FPLLMUL = MUL_17 | 17x Multiplier |
| FPLLMUL = MUL_18 | 18x Multiplier |
| FPLLMUL = MUL_19 | 19x Multiplier |
| FPLLMUL = MUL_20 | 20x Multiplier |
| FPLLMUL = MUL_21 | 21x Multiplier |
| FPLLMUL = MUL_24 | 24x Multiplier |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |
| UPLLIDIV = DIV_5 | 5x Divider |
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = 0 | ON | Enabled |
|------------|-----|-----------------------|
| UPLLEN = 0 | OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|---------------|---------|

| OSCIOFNC = OFF | Disabled |
|----------------|----------|

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |
| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| Waterland Time Tooleourer. | | |
|----------------------------|-----------|--|
| WDTPS = PS1 | 1:1 | |
| WDTPS = PS2 | 1:2 | |
| WDTPS = PS4 | 1:4 | |
| WDTPS = PS8 | 1:8 | |
| WDTPS = PS16 | 1:16 | |
| WDTPS = PS32 | 1:32 | |
| WDTPS = PS64 | 1:64 | |
| WDTPS = PS128 | 1:128 | |
| WDTPS = PS256 | 1:256 | |
| WDTPS = PS512 | 1:512 | |
| WDTPS = PS1024 | 1:1024 | |
| WDTPS = PS2048 | 1:2048 | |
| WDTPS = PS4096 | 1:4096 | |
| WDTPS = PS8192 | 1:8192 | |
| WDTPS = PS16384 | 1:16384 | |
| WDTPS = PS32768 | 1:32768 | |
| WDTPS = PS65536 | 1:65536 | |
| WDTPS = PS131072 | 1:131072 | |
| WDTPS = PS262144 | 1:262144 | |
| WDTPS = PS524288 | 1:524288 | |
| WDTPS = PS1048576 | 1:1048576 | |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| <pre>ICESEL = ICS_PGx1</pre> | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|------------------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| PWP = PWP512K | First 512K |
|---------------|------------|
|---------------|------------|

| | 1 |
|---------------|------------|
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |
| PWP = PWP484K | First 484K |
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| L | |

| | E: |
|---------------|------------|
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| PWP = PWP296K | First 296K |
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| | |

| PWP = PWP132K | First 132K |
|---------------|------------|
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MX795F512L

SRS Select:

| FSRSSEL = PRIORITY_0 | SRS Priority 0 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_1 | SRS Priority 1 |
| FSRSSEL = PRIORITY_2 | SRS Priority 2 |

| FSRSSEL = PRIORITY_3 | SRS Priority 3 |
|----------------------|----------------|
| FSRSSEL = PRIORITY_4 | SRS Priority 4 |
| FSRSSEL = PRIORITY_5 | SRS Priority 5 |
| FSRSSEL = PRIORITY_6 | SRS Priority 6 |
| FSRSSEL = PRIORITY_7 | SRS Priority 7 |

Ethernet RMII/MII Enable:

| FMIIEN = OFF | RMII Enabled |
|--------------|--------------|
| FMIIEN = ON | MII Enabled |

Ethernet I/O Pin Select:

| FETHIO = OFF | Alternate Ethernet I/O |
|--------------|------------------------|
| FETHIO = ON | Default Ethernet I/O |

CAN I/O Pin Select:

| FCANIO = OFF | Alternate CAN I/O |
|--------------|-------------------|
| FCANIO = ON | Default CAN I/O |

USB USID Selection:

| FUSBIDIO = OFF | Controlled by Port Function |
|----------------|------------------------------|
| FUSBIDIO = ON | Controlled by the USB Module |

USB VBUS ON Selection:

| FVBUSONIO = OFF | Controlled by Port Function |
|-----------------|-----------------------------|
| FVBUSONIO = ON | Controlled by USB Module |

PLL Input Divider:

| FPLLIDIV = DIV_1 | 1x Divider |
|-------------------|-------------|
| FPLLIDIV = DIV_2 | 2x Divider |
| FPLLIDIV = DIV_3 | 3x Divider |
| FPLLIDIV = DIV_4 | 4x Divider |
| FPLLIDIV = DIV_5 | 5x Divider |
| FPLLIDIV = DIV_6 | 6x Divider |
| FPLLIDIV = DIV_10 | 10x Divider |
| FPLLIDIV = DIV_12 | 12x Divider |

PLL Multiplier:

| 15x Multiplier |
|----------------|
| 16x Multiplier |
| 17x Multiplier |
| 18x Multiplier |
| 19x Multiplier |
| 20x Multiplier |
| 21x Multiplier |
| 24x Multiplier |
| |

USB PLL Input Divider:

| UPLLIDIV = DIV_1 | 1x Divider |
|------------------|------------|
| UPLLIDIV = DIV_2 | 2x Divider |
| UPLLIDIV = DIV_3 | 3x Divider |
| UPLLIDIV = DIV_4 | 4x Divider |

| UPLLIDIV = DIV_5 | 5x Divider |
|-------------------|-------------|
| UPLLIDIV = DIV_6 | 6x Divider |
| UPLLIDIV = DIV_10 | 10x Divider |
| UPLLIDIV = DIV_12 | 12x Divider |

USB PLL Enable:

| UPLLEN = ON | Enabled |
|--------------|-----------------------|
| UPLLEN = OFF | Disabled and Bypassed |

System PLL Output Clock Divider:

| FPLLODIV = DIV_1 | PLL Divide by 1 |
|--------------------|-------------------|
| FPLLODIV = DIV_2 | PLL Divide by 2 |
| FPLLODIV = DIV_4 | PLL Divide by 4 |
| FPLLODIV = DIV_8 | PLL Divide by 8 |
| FPLLODIV = DIV_16 | PLL Divide by 16 |
| FPLLODIV = DIV_32 | PLL Divide by 32 |
| FPLLODIV = DIV_64 | PLL Divide by 64 |
| FPLLODIV = DIV_256 | PLL Divide by 256 |

Oscillator Selection Bits:

| FNOSC = FRC | Fast RC Osc (FRC) |
|------------------|------------------------------------|
| FNOSC = FRCPLL | Fast RC Osc with PLL |
| FNOSC = PRI | Primary Osc (XT,HS,EC) |
| FNOSC = PRIPLL | Primary Osc w/PLL (XT+,HS+,EC+PLL) |
| FNOSC = SOSC | Low Power Secondary Osc (SOSC) |
| FNOSC = LPRC | Low Power RC Osc (LPRC) |
| FNOSC = FRCDIV16 | Fast RC Osc w/Div-by-16 (FRC/16) |
| FNOSC = FRCDIV | Fast RC Osc w/Div-by-N (FRCDIV) |

Secondary Oscillator Enable:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Internal/External Switch Over:

| IES | O = OFF | Disabled |
|-----|---------|----------|
| IES | O = ON | Enabled |

Primary Oscillator Configuration:

| POSCMOD = EC | External clock mode |
|---------------|----------------------|
| POSCMOD = XT | XT osc mode |
| POSCMOD = HS | HS osc mode |
| POSCMOD = OFF | Primary osc disabled |

CLKO Output Signal Active on the OSCO Pin:

| OSCIOFNC = ON | Enabled |
|----------------|----------|
| OSCIOFNC = OFF | Disabled |

Peripheral Clock Divisor:

| FPBDIV = DIV_1 | Pb_Clk is Sys_Clk/1 |
|----------------|---------------------|
| FPBDIV = DIV_2 | Pb_Clk is Sys_Clk/2 |
| FPBDIV = DIV_4 | Pb_Clk is Sys_Clk/4 |

| FPBDIV = DIV_8 | Pb_Clk is Sys_Clk/8 |
|----------------|---------------------|
|----------------|---------------------|

Clock Switching and Monitor Selection:

| FCKSM = CSECME | Clock Switch Enable, FSCM Enabled |
|----------------|-------------------------------------|
| FCKSM = CSECMD | Clock Switch Enable, FSCM Disabled |
| FCKSM = CSDCMD | Clock Switch Disable, FSCM Disabled |

Watchdog Timer Postscaler:

| Tratoridog Timor i Gotobalor. | |
|-------------------------------|-----------|
| WDTPS = PS1 | 1:1 |
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |
| WDTPS = PS1024 | 1:1024 |
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8192 |
| WDTPS = PS16384 | 1:16384 |
| WDTPS = PS32768 | 1:32768 |
| WDTPS = PS65536 | 1:65536 |
| WDTPS = PS131072 | 1:131072 |
| WDTPS = PS262144 | 1:262144 |
| WDTPS = PS524288 | 1:524288 |
| WDTPS = PS1048576 | 1:1048576 |
| | |

Watchdog Timer Enable:

| FWDTEN = OFF | WDT Disabled (SWDTEN Bit Controls) |
|--------------|------------------------------------|
| FWDTEN = ON | WDT Enabled |

Background Debugger Enable:

| DEBUG = ON | Debugger is enabled |
|-------------|----------------------|
| DEBUG = OFF | Debugger is disabled |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE EMUC1/EMUD1 pins shared with PGC1/PGD1 |
|-------------------|--|
| ICESEL = ICS_PGx2 | ICE EMUC2/EMUD2 pins shared with PGC2/PGD2 |

Program Flash Write Protect:

| • | |
|---------------|------------|
| PWP = PWP512K | First 512K |
| PWP = PWP508K | First 508K |
| PWP = PWP504K | First 504K |
| PWP = PWP500K | First 500K |
| PWP = PWP496K | First 496K |
| PWP = PWP492K | First 492K |
| PWP = PWP488K | First 488K |

| PWP = PWP484K | First 484K |
|---------------|------------|
| PWP = PWP480K | First 480K |
| PWP = PWP476K | First 476K |
| PWP = PWP472K | First 472K |
| PWP = PWP468K | First 468K |
| PWP = PWP464K | First 464K |
| PWP = PWP460K | First 460K |
| PWP = PWP456K | First 456K |
| PWP = PWP452K | First 452K |
| PWP = PWP448K | First 448K |
| PWP = PWP444K | First 444K |
| PWP = PWP440K | First 440K |
| PWP = PWP436K | First 436K |
| PWP = PWP432K | First 432K |
| PWP = PWP428K | First 428K |
| PWP = PWP424K | First 424K |
| PWP = PWP420K | First 420K |
| PWP = PWP416K | First 416K |
| PWP = PWP412K | First 412K |
| PWP = PWP408K | First 408K |
| PWP = PWP404K | First 404K |
| PWP = PWP400K | First 400K |
| PWP = PWP396K | First 396K |
| PWP = PWP392K | First 392K |
| PWP = PWP388K | First 388K |
| PWP = PWP384K | First 384K |
| PWP = PWP380K | First 380K |
| PWP = PWP376K | First 376K |
| PWP = PWP372K | First 372K |
| PWP = PWP368K | First 368K |
| PWP = PWP364K | First 364K |
| PWP = PWP360K | First 360K |
| PWP = PWP356K | First 356K |
| PWP = PWP352K | First 352K |
| PWP = PWP348K | First 348K |
| PWP = PWP344K | First 344K |
| PWP = PWP340K | First 340K |
| PWP = PWP336K | First 336K |
| PWP = PWP332K | First 332K |
| PWP = PWP328K | First 328K |
| PWP = PWP324K | First 324K |
| PWP = PWP320K | First 320K |
| PWP = PWP316K | First 316K |
| PWP = PWP312K | First 312K |
| PWP = PWP308K | First 308K |
| PWP = PWP304K | First 304K |
| PWP = PWP300K | First 300K |
| - | • |

| PWP = PWP296K | First 296K |
|---------------|------------|
| PWP = PWP292K | First 292K |
| PWP = PWP288K | First 288K |
| PWP = PWP284K | First 284K |
| PWP = PWP280K | First 280K |
| PWP = PWP276K | First 276K |
| PWP = PWP272K | First 272K |
| PWP = PWP268K | First 268K |
| PWP = PWP264K | First 264K |
| PWP = PWP260K | First 260K |
| PWP = PWP256K | First 256K |
| PWP = PWP252K | First 252K |
| PWP = PWP248K | First 248K |
| PWP = PWP244K | First 244K |
| PWP = PWP240K | First 240K |
| PWP = PWP236K | First 236K |
| PWP = PWP232K | First 232K |
| PWP = PWP228K | First 228K |
| PWP = PWP224K | First 224K |
| PWP = PWP220K | First 220K |
| PWP = PWP216K | First 216K |
| PWP = PWP212K | First 212K |
| PWP = PWP208K | First 208K |
| PWP = PWP204K | First 204K |
| PWP = PWP200K | First 200K |
| PWP = PWP196K | First 196K |
| PWP = PWP192K | First 192K |
| PWP = PWP188K | First 188K |
| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
| PWP = PWP168K | First 168K |
| PWP = PWP164K | First 164K |
| PWP = PWP160K | First 160K |
| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
| PWP = PWP148K | First 148K |
| PWP = PWP144K | First 144K |
| PWP = PWP140K | First 140K |
| PWP = PWP136K | First 136K |
| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |

| PWP = PWP108K | First 108K |
|---------------|------------|
| PWP = PWP104K | First 104K |
| PWP = PWP100K | First 100K |
| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
| PWP = PWP60K | First 60K |
| PWP = PWP56K | First 56K |
| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
| PWP = PWP4K | First 4K |
| PWP = OFF | Disable |
| | |

Boot Flash Write Protect bit:

| BWP = ON | Protection Enabled |
|-----------|---------------------|
| BWP = OFF | Protection Disabled |

Code Protect:

| CP = ON | Protection Enabled |
|----------|---------------------|
| CP = OFF | Protection Disabled |

PIC32MXGENERIC

PLL Output Divider Value:

| FPLLODIV = DIV_1 | Divide by 1 |
|--------------------|---------------|
| FPLLODIV = DIV_2 | Divide by 2 |
| FPLLODIV = DIV_4 | Divide by 4 |
| FPLLODIV = DIV_8 | Divide by 8 |
| FPLLODIV = DIV_16 | Divide by 16 |
| FPLLODIV = DIV_32 | Divide by 32 |
| FPLLODIV = DIV_64 | Divide by 64 |
| FPLLODIV = DIV_256 | Divide by 256 |

USB PLL Enable bit:

| UPLLEN = ON | Enabled |
|--------------|----------|
| UPLLEN = OFF | Disabled |

USB PLL Input Divider bits:

| UPLLIDIV = DIV_1 | Divide by 1 |
|-------------------|--------------|
| UPLLIDIV = DIV_2 | Divide by 2 |
| UPLLIDIV = DIV_3 | Divide by 3 |
| UPLLIDIV = DIV_4 | Divide by 4 |
| UPLLIDIV = DIV_5 | Divide by 5 |
| UPLLIDIV = DIV_6 | Divide by 6 |
| UPLLIDIV = DIV_10 | Divide by 10 |
| UPLLIDIV = DIV_12 | Divide by 12 |

PLL Multiplier bits:

| FPLLMUL = MUL_15 | Multiply by 15 |
|------------------|----------------|
| FPLLMUL = MUL_16 | Multiply by 16 |
| FPLLMUL = MUL_17 | Multiply by 17 |
| FPLLMUL = MUL_18 | Multiply by 18 |
| FPLLMUL = MUL_19 | Multiply by 19 |
| FPLLMUL = MUL_20 | Multiply by 20 |
| FPLLMUL = MUL_21 | Multiply by 21 |
| FPLLMUL = MUL_24 | Multiply by 24 |

PLL Input Divider bits:

| FPLLIDIV = DIV_1 | Divide by 1 |
|-------------------|--------------|
| FPLLIDIV = DIV_2 | Divide by 2 |
| FPLLIDIV = DIV_3 | Divide by 3 |
| FPLLIDIV = DIV_4 | Divide by 4 |
| FPLLIDIV = DIV_5 | Divide by 5 |
| FPLLIDIV = DIV_6 | Divide by 6 |
| FPLLIDIV = DIV_10 | Divide by 10 |
| FPLLIDIV = DIV_12 | Divide by 12 |

Watchdog Timer Enable bit:

| FWDTEN = OFF | Disabled |
|--------------|----------|
| FWDTEN = ON | Enabled |

Watchdog Timer Postscale Select bits:

| raterial grant in the control of the | |
|--|-------|
| WDTPS = PS1 | 1:1 |
| WDTPS = PS2 | 1:2 |
| WDTPS = PS4 | 1:4 |
| WDTPS = PS8 | 1:8 |
| WDTPS = PS16 | 1:16 |
| WDTPS = PS32 | 1:32 |
| WDTPS = PS64 | 1:64 |
| WDTPS = PS128 | 1:128 |
| WDTPS = PS256 | 1:256 |
| WDTPS = PS512 | 1:512 |

| WDTPS = PS1024 | 1:1024 |
|-------------------|-------------|
| WDTPS = PS2048 | 1:2048 |
| WDTPS = PS4096 | 1:4096 |
| WDTPS = PS8192 | 1:8,192 |
| WDTPS = PS16384 | 1:16,384 |
| WDTPS = PS32768 | 1:32,768 |
| WDTPS = PS65536 | 1:65,536 |
| WDTPS = PS131072 | 1:131,072 |
| WDTPS = PS262144 | 1:262,144 |
| WDTPS = PS524288 | 1:524,288 |
| WDTPS = PS1048576 | 1:1,048,576 |

Clock Switching and Monitor Selection bits:

| FCKSM = CSECME | Clock Switching Enabled, Clock Monitoring Enabled |
|----------------|---|
| FCKSM = CSECMD | Clock Switching Enabled, Clock Monitoring Disabled |
| FCKSM = CSDCMD | Clock Switching Disabled, Clock Monitoring Disabled |

Bootup PBCLK divider:

| FPBDIV = DIV_1 | Divide by 1 |
|----------------|-------------|
| FPBDIV = DIV_2 | Divide by 2 |
| FPBDIV = DIV_4 | Divide by 4 |
| FPBDIV = DIV_8 | Divide by 8 |

CLKO Enable bit:

| OSCIOFNC = OFF | Disabled |
|----------------|----------|
| OSCIOFNC = ON | Enabled |

Primary Oscillator bits:

| POSCMOD = EC | EC oscillator |
|---------------|---------------|
| POSCMOD = XT | XT oscillator |
| POSCMOD = HS | HS oscillator |
| POSCMOD = OFF | Disabled |

Internal External Switch Over bit:

| IESO = OFF | Disabled |
|------------|----------|
| IESO = ON | Enabled |

Secondary oscillator Enable bit:

| FSOSCEN = OFF | Disabled |
|---------------|----------|
| FSOSCEN = ON | Enabled |

Oscillator Selection bits:

| FNOSC = FRC | Fast RC oscillator |
|------------------|--|
| FNOSC = FRCPLL | Fast RC oscillator w/ PLL |
| FNOSC = PRI | Primary oscillator (XT, HS, EC) |
| FNOSC = PRIPLL | Primary oscillator (XT, HS, EC) w/ PLL |
| FNOSC = SOSC | Secondary oscillator |
| FNOSC = LPRC | Low power RC oscillator |
| FNOSC = FRCDIV16 | Fast RC oscillator with divide by 16 |
| FNOSC = FRCDIV | Fast RC oscillator with divide |

Code Protect Enable bit:

| CP = ON | Enabled |
|----------|----------|
| CP = OFF | Disabled |

Boot Flash Write Protect bit:

| BWP = ON | Enabled |
|-----------|----------|
| BWP = OFF | Disabled |

Program Flash Write Protect:

| PWP | | |
|--|---------------|------------|
| PWP = PWP504K First 504K PWP = PWP496K First 496K PWP = PWP492K First 492K PWP = PWP488K First 488K PWP = PWP484K First 488K PWP = PWP480K First 480K PWP = PWP476K First 476K PWP = PWP472K First 472K PWP = PWP472K First 468K PWP = PWP468K First 468K PWP = PWP460K First 460K PWP = PWP456K First 450K PWP = PWP456K First 452K PWP = PWP456K First 452K PWP = PWP446K First 452K PWP = PWP446K First 448K PWP = PWP446K First 448K PWP = PWP446K First 448K PWP = PWP436K First 436K PWP = PWP436K First 436K PWP = PWP432K First 428K PWP = PWP424K First 428K PWP = PWP420K First 420K PWP = PWP412K First 40K PWP = PWP408K First 40K PWP = PWP408K First 40K <t< th=""><th>PWP = PWP512K</th><th>First 512K</th></t<> | PWP = PWP512K | First 512K |
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| PWP = PWP184K | First 184K |
| PWP = PWP180K | First 180K |
| PWP = PWP176K | First 176K |
| PWP = PWP172K | First 172K |
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| PWP = PWP168K | First 168K |
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| PWP = PWP156K | First 156K |
| PWP = PWP152K | First 152K |
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| PWP = PWP132K | First 132K |
| PWP = PWP128K | First 128K |
| PWP = PWP124K | First 124K |
| PWP = PWP120K | First 120K |
| PWP = PWP116K | First 116K |
| PWP = PWP112K | First 112K |
| PWP = PWP108K | First 108K |
| PWP = PWP104K | First 104K |
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| PWP = PWP96K | First 96K |
| PWP = PWP92K | First 92K |
| PWP = PWP88K | First 88K |
| PWP = PWP84K | First 84K |
| PWP = PWP80K | First 80K |
| PWP = PWP76K | First 76K |
| PWP = PWP72K | First 72K |
| PWP = PWP68K | First 68K |
| PWP = PWP64K | First 64K |
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| PWP = PWP52K | First 52K |
| PWP = PWP48K | First 48K |
| PWP = PWP44K | First 44K |
| PWP = PWP40K | First 40K |
| PWP = PWP36K | First 36K |
| PWP = PWP32K | First 32K |
| PWP = PWP28K | First 28K |
| PWP = PWP24K | First 24K |
| PWP = PWP20K | First 20K |
| PWP = PWP16K | First 16K |
| PWP = PWP12K | First 12K |
| PWP = PWP8K | First 8K |
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| PWP = PWP4K | First 4K |

ICE/ICD Comm Channel Select:

| ICESEL = ICS_PGx1 | ICE pins are shared with PGC1, PGD1 |
|-------------------|-------------------------------------|
| ICESEL = ICS_PGx2 | ICE pins are shared with PGC2, PGD2 |

Background Debugger Enable bit:

| DEBUG = ON | Enabled |
|-------------|----------|
| DEBUG = OFF | Disabled |