

PIC32 CONFIGURATION SETTINGS

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PIC32 Configuration Settings

PIC32 CONFIGURATION SETTINGS

Configuration Settings

This document lists the configuration settings available for each of the PIC32 MCU devices for use with MPLAB XC32's `#pragma config` directive.

32M4KCORE

PIC32MX110F016B

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
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WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX110F016C

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)

Configuration Settings

FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)
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Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072

Configuration Settings

WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K

Configuration Settings

PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX110F016D

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider

Configuration Settings

FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
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Configuration Settings

DEBUG = OFF	Debugger is Disabled
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JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
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Configuration Settings

BWP = OFF	Protection Disabled
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Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX120F032B

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32

Configuration Settings

FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128

Configuration Settings

WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K

Configuration Settings

PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
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Configuration Settings

IESO = ON	Enabled
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Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K

PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier

Configuration Settings

FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
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Configuration Settings

FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
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ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL

Configuration Settings

FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096

Configuration Settings

WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K

Configuration Settings

PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
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FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode

Configuration Settings

POSCMOD = OFF	Primary osc disabled
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CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
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Configuration Settings

FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx4	Communicate on PGEC4/PGED4
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K

Configuration Settings

PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX130F064D

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
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WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx4	Communicate on PGEC4/PGED4
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX150F128B

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)

Configuration Settings

FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)
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Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072

Configuration Settings

WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K

Configuration Settings

PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX150F128C

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider

FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
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Configuration Settings

DEBUG = OFF	Debugger is Disabled
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JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx4	Communicate on PGEC4/PGED4
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
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Configuration Settings

BWP = OFF	Protection Disabled
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Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX150F128D

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32

Configuration Settings

FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128

WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx4	Communicate on PGEC4/PGED4
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K

Configuration Settings

PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX210F016B

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLIDIV = DIV_1	PLL Divide by 1
FPLLIDIV = DIV_2	PLL Divide by 2
FPLLIDIV = DIV_4	PLL Divide by 4
FPLLIDIV = DIV_8	PLL Divide by 8
FPLLIDIV = DIV_16	PLL Divide by 16
FPLLIDIV = DIV_32	PLL Divide by 32
FPLLIDIV = DIV_64	PLL Divide by 64
FPLLIDIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
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Configuration Settings

FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048

Configuration Settings

WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K

Configuration Settings

PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX210F016C

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLIDIV = DIV_1	PLL Divide by 1
FPLLIDIV = DIV_2	PLL Divide by 2
FPLLIDIV = DIV_4	PLL Divide by 4
FPLLIDIV = DIV_8	PLL Divide by 8
FPLLIDIV = DIV_16	PLL Divide by 16
FPLLIDIV = DIV_32	PLL Divide by 32
FPLLIDIV = DIV_64	PLL Divide by 64
FPLLIDIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)

Configuration Settings

FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384

Configuration Settings

WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K

PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX210F016D

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider

FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288

Configuration Settings

WDTPS = PS1048576	1:1048576
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Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K

Configuration Settings

PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
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Configuration Settings

IESO = ON	Enabled
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Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K

PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier

FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode

Configuration Settings

POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K

Configuration Settings

PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX220F032D

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
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Configuration Settings

OSCIOFNC = OFF	Disabled
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Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WISZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider

Configuration Settings

UPLLDIV = DIV_5	5x Divider
UPLLDIV = DIV_6	6x Divider
UPLLDIV = DIV_10	10x Divider
UPLLDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4

Configuration Settings

FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8
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Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
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Configuration Settings

JTAGEN = ON	JTAG Port Enabled
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ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
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CP = OFF	Protection Disabled
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PIC32MX230F064C

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled

FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled
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Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx4	Communicate on PGEC4/PGED4
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3

ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX230F064D

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
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Configuration Settings

WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx4	Communicate on PGEC4/PGED4
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX250F128B

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLIDIV = DIV_1	PLL Divide by 1
FPLLIDIV = DIV_2	PLL Divide by 2
FPLLIDIV = DIV_4	PLL Divide by 4

Configuration Settings

FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16

WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = RESERVED	Reserved
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K

PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX250F128C**Peripheral Module Disable Configuration:**

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64

Configuration Settings

FPLLLODIV = DIV_256	PLL Divide by 256
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Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256

WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx4	Communicate on PGEC4/PGED4
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K

Configuration Settings

PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K
PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
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FVBUSONIO = ON	Controlled by USB Module
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PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLIDIV = DIV_1	PLL Divide by 1
FPLLIDIV = DIV_2	PLL Divide by 2
FPLLIDIV = DIV_4	PLL Divide by 4
FPLLIDIV = DIV_8	PLL Divide by 8
FPLLIDIV = DIV_16	PLL Divide by 16
FPLLIDIV = DIV_32	PLL Divide by 32
FPLLIDIV = DIV_64	PLL Divide by 64
FPLLIDIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)

Configuration Settings

FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192

Configuration Settings

WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx4	Communicate on PGEC4/PGED4
ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP32K	First 32K
PWP = PWP31K	First 31K
PWP = PWP30K	First 30K
PWP = PWP29K	First 29K
PWP = PWP28K	First 28K
PWP = PWP27K	First 27K
PWP = PWP26K	First 26K
PWP = PWP25K	First 25K
PWP = PWP24K	First 24K
PWP = PWP23K	First 23K
PWP = PWP22K	First 22K
PWP = PWP21K	First 21K
PWP = PWP20K	First 20K

PWP = PWP19K	First 19K
PWP = PWP18K	First 18K
PWP = PWP17K	First 17K
PWP = PWP16K	First 16K
PWP = PWP15K	First 15K
PWP = PWP14K	First 14K
PWP = PWP13K	First 13K
PWP = PWP12K	First 12K
PWP = PWP11K	First 11K
PWP = PWP10K	First 10K
PWP = PWP9K	First 9K
PWP = PWP8K	First 8K
PWP = PWP7K	First 7K
PWP = PWP6K	First 6K
PWP = PWP5K	First 5K
PWP = PWP4K	First 4K
PWP = PWP3K	First 3K
PWP = PWP2K	First 2K
PWP = PWP1K	First 1K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier

FPLLMUL = MUL_24	24x Multiplier
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System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K

Configuration Settings

PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K

Configuration Settings

PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K

PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX320F064H

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier

Configuration Settings

FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled

FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled
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Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K

Configuration Settings

PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K

Configuration Settings

PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K

Configuration Settings

PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX320F128H

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier

FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K

Configuration Settings

PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K

Configuration Settings

PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K

PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX320F128L

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
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Configuration Settings

FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K

Configuration Settings

PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K

Configuration Settings

PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K

Configuration Settings

PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Shadow Register Set Priority Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6

FSRSSEL = PRIORITY_7	SRS Priority 7
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Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288

Configuration Settings

WDTPS = PS1048576	1:1048576
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Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K

Configuration Settings

PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K

Configuration Settings

PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K

Configuration Settings

PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX330F064L

Shadow Register Set Priority Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider

Configuration Settings

FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
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Configuration Settings

OSCIOFNC = OFF	Disabled
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Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WISZ_25	Window Size is 25%

Configuration Settings

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K

Configuration Settings

PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K

Configuration Settings

PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX340F128H

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288

Configuration Settings

WDTPS = PS1048576	1:1048576
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Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K

Configuration Settings

PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K

Configuration Settings

PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K

Configuration Settings

PWP = OFF	Disable
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Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX340F128L

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)

Configuration Settings

FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)
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Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072

Configuration Settings

WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K

Configuration Settings

PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K

Configuration Settings

PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K

Configuration Settings

PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)

Configuration Settings

FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768

Configuration Settings

WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K

Configuration Settings

PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K

Configuration Settings

PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K

PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)

Configuration Settings

FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192

Configuration Settings

WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K

Configuration Settings

PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K

Configuration Settings

PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K

Configuration Settings

PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Shadow Register Set Priority Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier

FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WISZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K

Configuration Settings

PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K

Configuration Settings

PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Shadow Register Set Priority Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512

WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K

Configuration Settings

PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K

Configuration Settings

PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K

Configuration Settings

PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX350F256H

Shadow Register Set Priority Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
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Configuration Settings

IESO = ON	Enabled
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Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K

Configuration Settings

PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K

Configuration Settings

PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K

PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX350F256L**Shadow Register Set Priority Select:**

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
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Configuration Settings

FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K

Configuration Settings

PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K

Configuration Settings

PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX360F256L

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Configuration Settings

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
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Configuration Settings

FWDTEN = ON	WDT Enabled
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Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K

Configuration Settings

PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K

Configuration Settings

PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX360F512L

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Configuration Settings

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
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Configuration Settings

FWDTEN = ON	WDT Enabled
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Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K

Configuration Settings

PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K

Configuration Settings

PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Shadow Register Set Priority Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16

Configuration Settings

FPLLLODIV = DIV_32	PLL Divide by 32
FPLLLODIV = DIV_64	PLL Divide by 64
FPLLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64

WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K

Configuration Settings

PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K

Configuration Settings

PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K

PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Shadow Register Set Priority Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
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Configuration Settings

FSOSCEN = ON	Enabled
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Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K

Configuration Settings

PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K

Configuration Settings

PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K

PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider

Configuration Settings

UPLLDIV = DIV_5	5x Divider
UPLLDIV = DIV_6	6x Divider
UPLLDIV = DIV_10	10x Divider
UPLLDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4

Configuration Settings

FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8
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Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K

Configuration Settings

PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K

Configuration Settings

PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K

PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Shadow Register Set Priority Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
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WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
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Configuration Settings

PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K

Configuration Settings

PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K

Configuration Settings

PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Shadow Register Set Priority Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2

FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider

Configuration Settings

UPLLDIV = DIV_12	12x Divider
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USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WISZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K

Configuration Settings

PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K

Configuration Settings

PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPPLEN = ON	Enabled
UPPLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLIDIV = DIV_1	PLL Divide by 1
FPLLIDIV = DIV_2	PLL Divide by 2
FPLLIDIV = DIV_4	PLL Divide by 4
FPLLIDIV = DIV_8	PLL Divide by 8
FPLLIDIV = DIV_16	PLL Divide by 16
FPLLIDIV = DIV_32	PLL Divide by 32
FPLLIDIV = DIV_64	PLL Divide by 64
FPLLIDIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)

Configuration Settings

FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192

Configuration Settings

WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K

Configuration Settings

PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K

Configuration Settings

PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K

Configuration Settings

PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
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UPLLEN = OFF	Disabled and Bypassed
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System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K

Configuration Settings

PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K

Configuration Settings

PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K

PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier

FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode

Configuration Settings

POSCMOD = OFF	Primary osc disabled
---------------	----------------------

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
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ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2
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Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K

Configuration Settings

PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K

Configuration Settings

PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPPLEN = ON	Enabled
UPPLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLIDIV = DIV_1	PLL Divide by 1
FPLLIDIV = DIV_2	PLL Divide by 2
FPLLIDIV = DIV_4	PLL Divide by 4
FPLLIDIV = DIV_8	PLL Divide by 8
FPLLIDIV = DIV_16	PLL Divide by 16
FPLLIDIV = DIV_32	PLL Divide by 32
FPLLIDIV = DIV_64	PLL Divide by 64
FPLLIDIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)

Configuration Settings

FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192

Configuration Settings

WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K

Configuration Settings

PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K

Configuration Settings

PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K

PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Shadow Register Set Priority Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider

FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
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Configuration Settings

WINDIS = OFF	Watchdog Timer is in Non-Window Mode
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Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K

Configuration Settings

PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K

Configuration Settings

PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K

Configuration Settings

PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX450F128L

Shadow Register Set Priority Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider

FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)

Configuration Settings

FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)
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Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072

Configuration Settings

WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K

Configuration Settings

PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K

Configuration Settings

PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K

PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX450F256H**Shadow Register Set Priority Select:**

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
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Configuration Settings

FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048

WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K

Configuration Settings

PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K

Configuration Settings

PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K

PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Shadow Register Set Priority Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4

Configuration Settings

FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16

WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K

Configuration Settings

PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K

Configuration Settings

PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K

Configuration Settings

PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider

FPLLIDIV = DIV_12	12x Divider
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PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
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Configuration Settings

FWDTEN = ON	WDT Enabled
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Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K

Configuration Settings

PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K

Configuration Settings

PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512

WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K

Configuration Settings

PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K

Configuration Settings

PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K

Configuration Settings

PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Shadow Register Set Priority Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider

FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)

Configuration Settings

FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)
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Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072

Configuration Settings

WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K

Configuration Settings

PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K

Configuration Settings

PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K

Configuration Settings

PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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Shadow Register Set Priority Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Peripheral Module Disable Configuration:

PMDL1WAY = OFF	Allow multiple reconfigurations
PMDL1WAY = ON	Allow only one reconfiguration

Peripheral Pin Select Configuration:

IOL1WAY = OFF	Allow multiple reconfigurations
IOL1WAY = ON	Allow only one reconfiguration

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
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Configuration Settings

FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048

Configuration Settings

WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Window Enable:

WINDIS = ON	Watchdog Timer is in Window Mode
WINDIS = OFF	Watchdog Timer is in Non-Window Mode

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Watchdog Timer Window Size:

FWDTWINSZ = WINSZ_75	Window Size is 75%
FWDTWINSZ = WINSZ_50	Window Size is 50%
FWDTWINSZ = WINSZ_37	Window Size is 37.5%
FWDTWINSZ = WINSZ_25	Window Size is 25%

Background Debugger Enable:

DEBUG = ON	Debugger is Enabled
DEBUG = OFF	Debugger is Disabled

JTAG Enable:

JTAGEN = OFF	JTAG Disabled
JTAGEN = ON	JTAG Port Enabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx3	Communicate on PGEC3/PGED3
ICESEL = ICS_PGx2	Communicate on PGEC2/PGED2
ICESEL = ICS_PGx1	Communicate on PGEC1/PGED1

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K

Configuration Settings

PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K

Configuration Settings

PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K

Configuration Settings

PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64

Configuration Settings

FPLLODIV = DIV_256	PLL Divide by 256
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Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256

Configuration Settings

WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K

Configuration Settings

PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K

Configuration Settings

PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K

Configuration Settings

PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider

FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288

Configuration Settings

WDTPS = PS1048576	1:1048576
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Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K

Configuration Settings

PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K

Configuration Settings

PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K

Configuration Settings

PWP = OFF	Disable
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Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier

Configuration Settings

FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K

Configuration Settings

PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K

Configuration Settings

PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
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FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled

FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled
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Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K

Configuration Settings

PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K

Configuration Settings

PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K

Configuration Settings

PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64

Configuration Settings

FPLL0DIV = DIV_256	PLL Divide by 256
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Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256

Configuration Settings

WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K

Configuration Settings

PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K

Configuration Settings

PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K

Configuration Settings

PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider

FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288

Configuration Settings

WDTPS = PS1048576	1:1048576
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Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K

Configuration Settings

PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K

Configuration Settings

PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K

Configuration Settings

PWP = OFF	Disable
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Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier

Configuration Settings

FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K

Configuration Settings

PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K

Configuration Settings

PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
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FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled

Configuration Settings

FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled
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Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K

Configuration Settings

PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K

Configuration Settings

PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K

PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64

Configuration Settings

FPLL0DIV = DIV_256	PLL Divide by 256
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Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256

Configuration Settings

WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K

Configuration Settings

PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K

Configuration Settings

PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K

Configuration Settings

PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider

FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288

Configuration Settings

WDTPS = PS1048576	1:1048576
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Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K

Configuration Settings

PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K

Configuration Settings

PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K

Configuration Settings

PWP = OFF	Disable
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Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIIEN = OFF	RMII Enabled
FMIIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier

FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
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Configuration Settings

DEBUG = OFF	Debugger is disabled
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ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K

Configuration Settings

PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K

Configuration Settings

PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
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CP = OFF	Protection Disabled
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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
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Configuration Settings

OSCIOFNC = OFF	Disabled
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Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
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Configuration Settings

PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K

Configuration Settings

PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K

Configuration Settings

PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2

FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider

Configuration Settings

UPLLDIV = DIV_12	12x Divider
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USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K

Configuration Settings

PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K

Configuration Settings

PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K

PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
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WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K

Configuration Settings

PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K

Configuration Settings

PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K

PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MX675F256H**SRS Select:**

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64

Configuration Settings

FPLLODIV = DIV_256	PLL Divide by 256
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Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256

Configuration Settings

WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K

Configuration Settings

PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K

Configuration Settings

PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K

Configuration Settings

PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
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FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPPLEN = ON	Enabled
UPPLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)

Configuration Settings

FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536

Configuration Settings

WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K

Configuration Settings

PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K

Configuration Settings

PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K

Configuration Settings

PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
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Configuration Settings

IESO = ON	Enabled
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Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K

Configuration Settings

PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K

Configuration Settings

PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier

FPLLMUL = MUL_24	24x Multiplier
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USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K

Configuration Settings

PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K

Configuration Settings

PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
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FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider

Configuration Settings

UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K

Configuration Settings

PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K

Configuration Settings

PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K

PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
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WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K

Configuration Settings

PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K

Configuration Settings

PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K

Configuration Settings

PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4

FPLL0DIV = DIV_8	PLL Divide by 8
FPLL0DIV = DIV_16	PLL Divide by 16
FPLL0DIV = DIV_32	PLL Divide by 32
FPLL0DIV = DIV_64	PLL Divide by 64
FPLL0DIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16

Configuration Settings

WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K

Configuration Settings

PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K

Configuration Settings

PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K

Configuration Settings

PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64

Configuration Settings

FPLLODIV = DIV_256	PLL Divide by 256
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Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256

Configuration Settings

WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K

Configuration Settings

PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K

Configuration Settings

PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K

Configuration Settings

PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
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FVBUSONIO = ON	Controlled by USB Module
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PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLIDIV = DIV_1	PLL Divide by 1
FPLLIDIV = DIV_2	PLL Divide by 2
FPLLIDIV = DIV_4	PLL Divide by 4
FPLLIDIV = DIV_8	PLL Divide by 8
FPLLIDIV = DIV_16	PLL Divide by 16
FPLLIDIV = DIV_32	PLL Divide by 32
FPLLIDIV = DIV_64	PLL Divide by 64
FPLLIDIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)

Configuration Settings

FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192

Configuration Settings

WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K

Configuration Settings

PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K

Configuration Settings

PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K

Configuration Settings

PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider

FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)

Configuration Settings

FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)
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Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072

Configuration Settings

WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K

Configuration Settings

PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K

Configuration Settings

PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K

Configuration Settings

PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider

FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Configuration Settings

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
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Configuration Settings

FWDTEN = ON	WDT Enabled
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Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K

Configuration Settings

PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K

Configuration Settings

PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier

FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode

Configuration Settings

POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K

Configuration Settings

PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K

Configuration Settings

PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2
FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider
UPLLIDIV = DIV_5	5x Divider
UPLLIDIV = DIV_6	6x Divider
UPLLIDIV = DIV_10	10x Divider
UPLLIDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
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Configuration Settings

OSCIOFNC = OFF	Disabled
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Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4
FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8

Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
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Configuration Settings

PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K

Configuration Settings

PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K

Configuration Settings

PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

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SRS Select:

FSRSSEL = PRIORITY_0	SRS Priority 0
FSRSSEL = PRIORITY_1	SRS Priority 1
FSRSSEL = PRIORITY_2	SRS Priority 2

FSRSSEL = PRIORITY_3	SRS Priority 3
FSRSSEL = PRIORITY_4	SRS Priority 4
FSRSSEL = PRIORITY_5	SRS Priority 5
FSRSSEL = PRIORITY_6	SRS Priority 6
FSRSSEL = PRIORITY_7	SRS Priority 7

Ethernet RMII/MII Enable:

FMIEN = OFF	RMII Enabled
FMIEN = ON	MII Enabled

Ethernet I/O Pin Select:

FETHIO = OFF	Alternate Ethernet I/O
FETHIO = ON	Default Ethernet I/O

CAN I/O Pin Select:

FCANIO = OFF	Alternate CAN I/O
FCANIO = ON	Default CAN I/O

USB USID Selection:

FUSBIDIO = OFF	Controlled by Port Function
FUSBIDIO = ON	Controlled by the USB Module

USB VBUS ON Selection:

FVBUSONIO = OFF	Controlled by Port Function
FVBUSONIO = ON	Controlled by USB Module

PLL Input Divider:

FPLLIDIV = DIV_1	1x Divider
FPLLIDIV = DIV_2	2x Divider
FPLLIDIV = DIV_3	3x Divider
FPLLIDIV = DIV_4	4x Divider
FPLLIDIV = DIV_5	5x Divider
FPLLIDIV = DIV_6	6x Divider
FPLLIDIV = DIV_10	10x Divider
FPLLIDIV = DIV_12	12x Divider

PLL Multiplier:

FPLLMUL = MUL_15	15x Multiplier
FPLLMUL = MUL_16	16x Multiplier
FPLLMUL = MUL_17	17x Multiplier
FPLLMUL = MUL_18	18x Multiplier
FPLLMUL = MUL_19	19x Multiplier
FPLLMUL = MUL_20	20x Multiplier
FPLLMUL = MUL_21	21x Multiplier
FPLLMUL = MUL_24	24x Multiplier

USB PLL Input Divider:

UPLLIDIV = DIV_1	1x Divider
UPLLIDIV = DIV_2	2x Divider
UPLLIDIV = DIV_3	3x Divider
UPLLIDIV = DIV_4	4x Divider

Configuration Settings

UPLLDIV = DIV_5	5x Divider
UPLLDIV = DIV_6	6x Divider
UPLLDIV = DIV_10	10x Divider
UPLLDIV = DIV_12	12x Divider

USB PLL Enable:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled and Bypassed

System PLL Output Clock Divider:

FPLLODIV = DIV_1	PLL Divide by 1
FPLLODIV = DIV_2	PLL Divide by 2
FPLLODIV = DIV_4	PLL Divide by 4
FPLLODIV = DIV_8	PLL Divide by 8
FPLLODIV = DIV_16	PLL Divide by 16
FPLLODIV = DIV_32	PLL Divide by 32
FPLLODIV = DIV_64	PLL Divide by 64
FPLLODIV = DIV_256	PLL Divide by 256

Oscillator Selection Bits:

FNOSC = FRC	Fast RC Osc (FRC)
FNOSC = FRCPLL	Fast RC Osc with PLL
FNOSC = PRI	Primary Osc (XT,HS,EC)
FNOSC = PRIPLL	Primary Osc w/PLL (XT+,HS+,EC+PLL)
FNOSC = SOSC	Low Power Secondary Osc (SOSC)
FNOSC = LPRC	Low Power RC Osc (LPRC)
FNOSC = FRCDIV16	Fast RC Osc w/Div-by-16 (FRC/16)
FNOSC = FRCDIV	Fast RC Osc w/Div-by-N (FRCDIV)

Secondary Oscillator Enable:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Primary Oscillator Configuration:

POSCMOD = EC	External clock mode
POSCMOD = XT	XT osc mode
POSCMOD = HS	HS osc mode
POSCMOD = OFF	Primary osc disabled

CLKO Output Signal Active on the OSCO Pin:

OSCIOFNC = ON	Enabled
OSCIOFNC = OFF	Disabled

Peripheral Clock Divisor:

FPBDIV = DIV_1	Pb_Clk is Sys_Clk/1
FPBDIV = DIV_2	Pb_Clk is Sys_Clk/2
FPBDIV = DIV_4	Pb_Clk is Sys_Clk/4

Configuration Settings

FPBDIV = DIV_8	Pb_Clk is Sys_Clk/8
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Clock Switching and Monitor Selection:

FCKSM = CSECME	Clock Switch Enable, FSCM Enabled
FCKSM = CSECMD	Clock Switch Enable, FSCM Disabled
FCKSM = CSDCMD	Clock Switch Disable, FSCM Disabled

Watchdog Timer Postscaler:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512
WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8192
WDTPS = PS16384	1:16384
WDTPS = PS32768	1:32768
WDTPS = PS65536	1:65536
WDTPS = PS131072	1:131072
WDTPS = PS262144	1:262144
WDTPS = PS524288	1:524288
WDTPS = PS1048576	1:1048576

Watchdog Timer Enable:

FWDTEN = OFF	WDT Disabled (SWDTEN Bit Controls)
FWDTEN = ON	WDT Enabled

Background Debugger Enable:

DEBUG = ON	Debugger is enabled
DEBUG = OFF	Debugger is disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE EMUC1/EMUD1 pins shared with PGC1/PGD1
ICESEL = ICS_PGx2	ICE EMUC2/EMUD2 pins shared with PGC2/PGD2

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K

Configuration Settings

PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K
PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K

Configuration Settings

PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K
PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K

PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disable

Boot Flash Write Protect bit:

BWP = ON	Protection Enabled
BWP = OFF	Protection Disabled

Code Protect:

CP = ON	Protection Enabled
CP = OFF	Protection Disabled

PIC32MXGENERIC

PLL Output Divider Value:

FPLLODIV = DIV_1	Divide by 1
FPLLODIV = DIV_2	Divide by 2
FPLLODIV = DIV_4	Divide by 4
FPLLODIV = DIV_8	Divide by 8
FPLLODIV = DIV_16	Divide by 16
FPLLODIV = DIV_32	Divide by 32
FPLLODIV = DIV_64	Divide by 64
FPLLODIV = DIV_256	Divide by 256

USB PLL Enable bit:

UPLLEN = ON	Enabled
UPLLEN = OFF	Disabled

USB PLL Input Divider bits:

UPLLIDIV = DIV_1	Divide by 1
UPLLIDIV = DIV_2	Divide by 2
UPLLIDIV = DIV_3	Divide by 3
UPLLIDIV = DIV_4	Divide by 4
UPLLIDIV = DIV_5	Divide by 5
UPLLIDIV = DIV_6	Divide by 6
UPLLIDIV = DIV_10	Divide by 10
UPLLIDIV = DIV_12	Divide by 12

PLL Multiplier bits:

FPLLMUL = MUL_15	Multiply by 15
FPLLMUL = MUL_16	Multiply by 16
FPLLMUL = MUL_17	Multiply by 17
FPLLMUL = MUL_18	Multiply by 18
FPLLMUL = MUL_19	Multiply by 19
FPLLMUL = MUL_20	Multiply by 20
FPLLMUL = MUL_21	Multiply by 21
FPLLMUL = MUL_24	Multiply by 24

PLL Input Divider bits:

FPLLIDIV = DIV_1	Divide by 1
FPLLIDIV = DIV_2	Divide by 2
FPLLIDIV = DIV_3	Divide by 3
FPLLIDIV = DIV_4	Divide by 4
FPLLIDIV = DIV_5	Divide by 5
FPLLIDIV = DIV_6	Divide by 6
FPLLIDIV = DIV_10	Divide by 10
FPLLIDIV = DIV_12	Divide by 12

Watchdog Timer Enable bit:

FWDTEN = OFF	Disabled
FWDTEN = ON	Enabled

Watchdog Timer Postscale Select bits:

WDTPS = PS1	1:1
WDTPS = PS2	1:2
WDTPS = PS4	1:4
WDTPS = PS8	1:8
WDTPS = PS16	1:16
WDTPS = PS32	1:32
WDTPS = PS64	1:64
WDTPS = PS128	1:128
WDTPS = PS256	1:256
WDTPS = PS512	1:512

WDTPS = PS1024	1:1024
WDTPS = PS2048	1:2048
WDTPS = PS4096	1:4096
WDTPS = PS8192	1:8,192
WDTPS = PS16384	1:16,384
WDTPS = PS32768	1:32,768
WDTPS = PS65536	1:65,536
WDTPS = PS131072	1:131,072
WDTPS = PS262144	1:262,144
WDTPS = PS524288	1:524,288
WDTPS = PS1048576	1:1,048,576

Clock Switching and Monitor Selection bits:

FCKSM = CSECME	Clock Switching Enabled, Clock Monitoring Enabled
FCKSM = CSECMD	Clock Switching Enabled, Clock Monitoring Disabled
FCKSM = CSDCMD	Clock Switching Disabled, Clock Monitoring Disabled

Bootup PBCLK divider:

FPBDIV = DIV_1	Divide by 1
FPBDIV = DIV_2	Divide by 2
FPBDIV = DIV_4	Divide by 4
FPBDIV = DIV_8	Divide by 8

CLKO Enable bit:

OSCIOFNC = OFF	Disabled
OSCIOFNC = ON	Enabled

Primary Oscillator bits:

POSCMOD = EC	EC oscillator
POSCMOD = XT	XT oscillator
POSCMOD = HS	HS oscillator
POSCMOD = OFF	Disabled

Internal External Switch Over bit:

IESO = OFF	Disabled
IESO = ON	Enabled

Secondary oscillator Enable bit:

FSOSCEN = OFF	Disabled
FSOSCEN = ON	Enabled

Oscillator Selection bits:

FNOSC = FRC	Fast RC oscillator
FNOSC = FRCPLL	Fast RC oscillator w/ PLL
FNOSC = PRI	Primary oscillator (XT, HS, EC)
FNOSC = PRIPLL	Primary oscillator (XT, HS, EC) w/ PLL
FNOSC = SOSC	Secondary oscillator
FNOSC = LPRC	Low power RC oscillator
FNOSC = FRCDIV16	Fast RC oscillator with divide by 16
FNOSC = FRCDIV	Fast RC oscillator with divide

Code Protect Enable bit:

CP = ON	Enabled
CP = OFF	Disabled

Boot Flash Write Protect bit:

BWP = ON	Enabled
BWP = OFF	Disabled

Program Flash Write Protect:

PWP = PWP512K	First 512K
PWP = PWP508K	First 508K
PWP = PWP504K	First 504K
PWP = PWP500K	First 500K
PWP = PWP496K	First 496K
PWP = PWP492K	First 492K
PWP = PWP488K	First 488K
PWP = PWP484K	First 484K
PWP = PWP480K	First 480K
PWP = PWP476K	First 476K
PWP = PWP472K	First 472K
PWP = PWP468K	First 468K
PWP = PWP464K	First 464K
PWP = PWP460K	First 460K
PWP = PWP456K	First 456K
PWP = PWP452K	First 452K
PWP = PWP448K	First 448K
PWP = PWP444K	First 444K
PWP = PWP440K	First 440K
PWP = PWP436K	First 436K
PWP = PWP432K	First 432K
PWP = PWP428K	First 428K
PWP = PWP424K	First 424K
PWP = PWP420K	First 420K
PWP = PWP416K	First 416K
PWP = PWP412K	First 412K
PWP = PWP408K	First 408K
PWP = PWP404K	First 404K
PWP = PWP400K	First 400K
PWP = PWP396K	First 396K
PWP = PWP392K	First 392K
PWP = PWP388K	First 388K
PWP = PWP384K	First 384K
PWP = PWP380K	First 380K
PWP = PWP376K	First 376K
PWP = PWP372K	First 372K
PWP = PWP368K	First 368K
PWP = PWP364K	First 364K
PWP = PWP360K	First 360K

Configuration Settings

PWP = PWP356K	First 356K
PWP = PWP352K	First 352K
PWP = PWP348K	First 348K
PWP = PWP344K	First 344K
PWP = PWP340K	First 340K
PWP = PWP336K	First 336K
PWP = PWP332K	First 332K
PWP = PWP328K	First 328K
PWP = PWP324K	First 324K
PWP = PWP320K	First 320K
PWP = PWP316K	First 316K
PWP = PWP312K	First 312K
PWP = PWP308K	First 308K
PWP = PWP304K	First 304K
PWP = PWP300K	First 300K
PWP = PWP296K	First 296K
PWP = PWP292K	First 292K
PWP = PWP288K	First 288K
PWP = PWP284K	First 284K
PWP = PWP280K	First 280K
PWP = PWP276K	First 276K
PWP = PWP272K	First 272K
PWP = PWP268K	First 268K
PWP = PWP264K	First 264K
PWP = PWP260K	First 260K
PWP = PWP256K	First 256K
PWP = PWP252K	First 252K
PWP = PWP248K	First 248K
PWP = PWP244K	First 244K
PWP = PWP240K	First 240K
PWP = PWP236K	First 236K
PWP = PWP232K	First 232K
PWP = PWP228K	First 228K
PWP = PWP224K	First 224K
PWP = PWP220K	First 220K
PWP = PWP216K	First 216K
PWP = PWP212K	First 212K
PWP = PWP208K	First 208K
PWP = PWP204K	First 204K
PWP = PWP200K	First 200K
PWP = PWP196K	First 196K
PWP = PWP192K	First 192K
PWP = PWP188K	First 188K
PWP = PWP184K	First 184K
PWP = PWP180K	First 180K
PWP = PWP176K	First 176K
PWP = PWP172K	First 172K

Configuration Settings

PWP = PWP168K	First 168K
PWP = PWP164K	First 164K
PWP = PWP160K	First 160K
PWP = PWP156K	First 156K
PWP = PWP152K	First 152K
PWP = PWP148K	First 148K
PWP = PWP144K	First 144K
PWP = PWP140K	First 140K
PWP = PWP136K	First 136K
PWP = PWP132K	First 132K
PWP = PWP128K	First 128K
PWP = PWP124K	First 124K
PWP = PWP120K	First 120K
PWP = PWP116K	First 116K
PWP = PWP112K	First 112K
PWP = PWP108K	First 108K
PWP = PWP104K	First 104K
PWP = PWP100K	First 100K
PWP = PWP96K	First 96K
PWP = PWP92K	First 92K
PWP = PWP88K	First 88K
PWP = PWP84K	First 84K
PWP = PWP80K	First 80K
PWP = PWP76K	First 76K
PWP = PWP72K	First 72K
PWP = PWP68K	First 68K
PWP = PWP64K	First 64K
PWP = PWP60K	First 60K
PWP = PWP56K	First 56K
PWP = PWP52K	First 52K
PWP = PWP48K	First 48K
PWP = PWP44K	First 44K
PWP = PWP40K	First 40K
PWP = PWP36K	First 36K
PWP = PWP32K	First 32K
PWP = PWP28K	First 28K
PWP = PWP24K	First 24K
PWP = PWP20K	First 20K
PWP = PWP16K	First 16K
PWP = PWP12K	First 12K
PWP = PWP8K	First 8K
PWP = PWP4K	First 4K
PWP = OFF	Disabled

ICE/ICD Comm Channel Select:

ICESEL = ICS_PGx1	ICE pins are shared with PGC1, PGD1
ICESEL = ICS_PGx2	ICE pins are shared with PGC2, PGD2

Background Debugger Enable bit:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled