# 1. Description

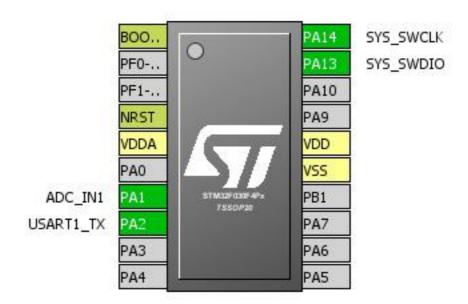
### 1.1. Project

Project Name	06_ADC
Board Name	06_ADC
Generated with:	STM32CubeMX 4.22.0
Date	01/27/2018

#### 1.2. MCU

MCU Series	STM32F0
MCU Line	STM32F0x0 Value Line
MCU name	STM32F030F4Px
MCU Package	TSSOP20
MCU Pin number	20

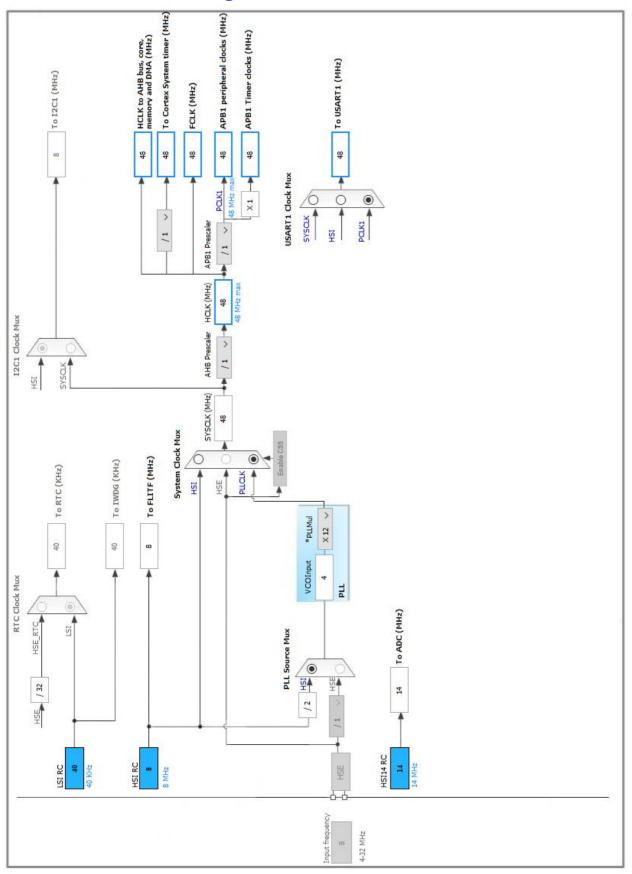
# 2. Pinout Configuration



# 3. Pins Configuration

Pin Number TSSOP20	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	воото	Boot		
4	NRST	Reset		
5	VDDA	Power		
7	PA1	I/O	ADC_IN1	
8	PA2	I/O	USART1_TX	
15	VSS	Power		
16	VDD	Power		
19	PA13	I/O	SYS_SWDIO	
20	PA14	I/O	SYS SWCLK	

## 4. Clock Tree Configuration



### 5. IPs and Middleware Configuration

#### 5.1. ADC

mode: IN1

#### 5.1.1. Parameter Settings:

#### ADC\_Settings:

Clock Prescaler

Resolution

Data Alignment

Scan Conversion Mode

Continuous Conversion Mode

ADC 12-bit resolution

Right alignment

Forward

Enabled \*

Discontinuous Conversion Mode Disabled

DMA Continuous Requests Disabled

End Of Conversion Selection End of single conversion

Overrun behaviour Overrun data preserved

Low Power Auto Wait Disabled

Low Power Auto Power Off Disabled

#### ADC\_Regular\_ConversionMode:

Sampling Time 239.5 Cycles \*

External Trigger Conversion Source Regular Conversion launched by software

External Trigger Conversion Edge None

WatchDog:

Enable Analog WatchDog Mode false

#### 5.2. SYS

mode: Debug Serial Wire Timebase Source: SysTick

#### 5.3. USART1

Mode: Single Wire (Half-Duplex)

#### 5.3.1. Parameter Settings:

#### **Basic Parameters:**

Baud Rate 38400

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

**Advanced Parameters:** 

Data Direction Receive and Transmit

Over Sampling 16 Samples
Single Sample Disable

**Advanced Features:** 

Auto Baudrate Disable TX Pin Active Level Inversion Disable Disable **RX Pin Active Level Inversion** Data Inversion Disable TX and RX Pins Swapping Disable Enable Overrun DMA on RX Error Enable MSB First Disable

#### \* User modified value

# 6. System Configuration

### 6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC	PA1	ADC_IN1	Analog mode	No pull-up and no pull-down	n/a	
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	
	PA14	SYS_SWCLK	n/a	n/a	n/a	
USART1	PA2	USART1_TX	Alternate Function Open Drain	Pull-up	High *	

### 6.2. DMA configuration

nothing configured in DMA service

### 6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
System service call via SWI instruction	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
Flash global interrupt	unused		
RCC global interrupt	unused		
ADC interrupt	unused		
USART1 global interrupt	unused		

<sup>\*</sup> User modified value

# 7. Power Consumption Calculator report

#### 7.1. Microcontroller Selection

Series	STM32F0
Line	STM32F0x0 Value Line
мси	STM32F030F4Px
Datasheet	024849_Rev2

#### 7.2. Parameter Selection

Temperature	25
Vdd	3.6

# 8. Software Project

### 8.1. Project Settings

Name	Value
Project Name	06_ADC
Project Folder	C:\Users\armst\Desktop\STM32\STM32F030F4P6\GCC\06_ADC
Toolchain / IDE	TrueSTUDIO
Firmware Package Name and Version	STM32Cube FW_F0 V1.8.0

#### 8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	