

Table with 4 columns: 8-byte register, Bytes 0-3, Bytes 0-1, Byte 0. Rows include registers like %rax, %rcx, %rdx, %rbx, %rsi, %rdi, %rsp, %rbp, %r8, %r9, %r10, %r11, %r12, %r13, %r14, %r15 and their corresponding bit fields.

Table with 4 columns: cmpq b, a, eins og að reikna a-b, Skilgildis, %rdx, 0xf000, %rcx, 0x0100. Rows include instructions like jmp, je, jne, js, jns, jg, jge, jle, ja, jb and their corresponding bit fields.

Table with 3 columns: Segð, Vistfangsútreikningur, Vistfang. Rows include instructions like movb, movw, movl, movq, movzsd, movssd and their corresponding bit fields.

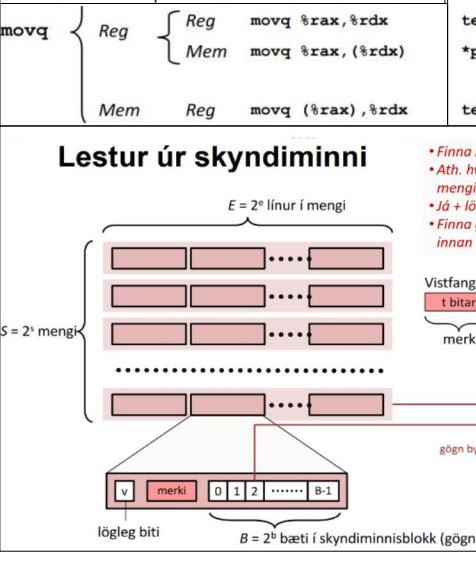


Table with 2 columns: Reg, Mem. Rows include instructions like movq %rax, %rdx, movq %rax, (%rdx), movq (%rax), %rdx. It shows how registers and memory are used in instructions.

Table with 3 columns: Src, Dest, Dest = Dest - Src, Dest = Dest \* Src, Dest = Dest << Src, Dest = Dest >> Src, Dest = Dest >> Src, Dest = Dest ^ Src, Dest = Dest & Src, Dest = Dest | Src. Rows include instructions like subq, imulq, salq, sarq, shrq, xorg, andq, orq.

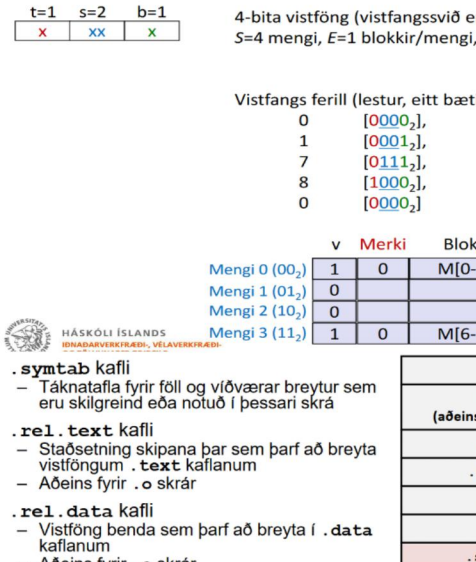


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