

root  
: Root

system  
: System

system\_port

membus  
: SystemXBar

mem\_side\_ports

cpu\_side\_ports

mem\_ctrls  
: MemCtrl

port

l2bus  
: L2XBar

mem\_side\_ports

cpu\_side\_ports

l2  
: L2Cache

mem\_side

cpu\_side

cpu  
: RiscvMinorCPU

dcache\_port

icache\_port

dcache  
: L1DCache

mem\_side

cpu\_side

icache  
: L1ICache

mem\_side

cpu\_side