

Q1

Determine the magnitude of common mode voltage gain of the circuit given below. Assume the bias current sources to be practical with current I_{SS} equal to $28 \mu\text{A}$ and shunt resistance equal to $0.5 \text{ M}\Omega$ each. Bias voltage V_B , resistors R_D and bias current sources I_{SS} are selected in a way to keep all transistors in saturation.

Other parameters are listed below :

Supply voltage (V_{dd}) = 3.3 V

$\mu_n C_{ox} = 200 \mu\text{A}/\text{V}^2$, $\mu_p C_{ox} = 100 \mu\text{A}/\text{V}^2$

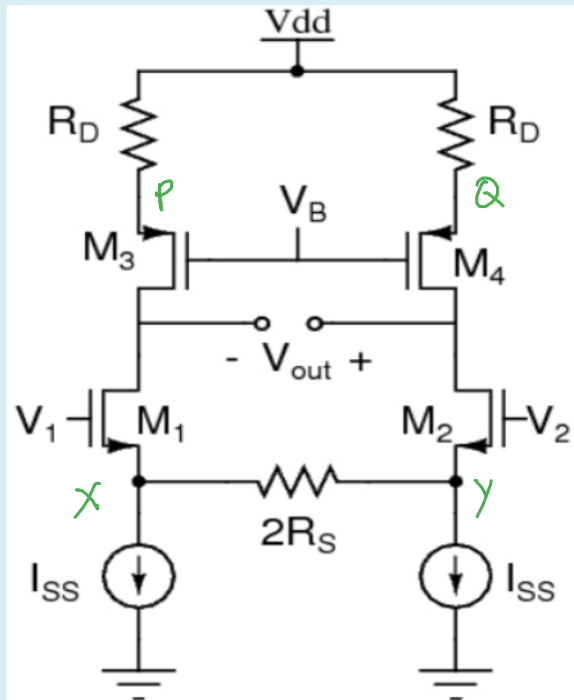
$V_{TN} = |V_{TP}| = 0.7 \text{ V}$

$(W/L)_{1,2} = 1$, $(W/L)_{3,4} = 4$

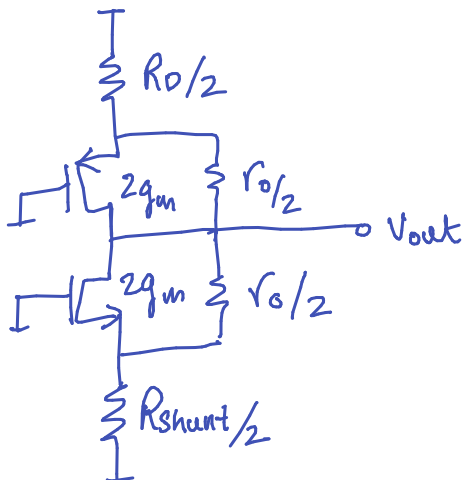
$R_D = 3 \text{ K}\Omega$, $R_S = 10 \text{ K}\Omega$

Channel length modulation parameter (λ) = 0.01 V^{-1}

Give your answer correct upto 3 decimal places.



In the common mode scenario, $V_x = V_y$. $2R_S(x)$



$$\frac{V_{out}}{V_{CM}} = \left[\frac{2g_m}{1 + (2g_m) \frac{R_{shunt}}{2}} \right] [R_{up} \parallel R_{down}]$$

$$\therefore R_{up} = \left[1 + (2g_{m3}) \left(\frac{r_o}{2} \right) \right] \frac{R_D}{2} + \frac{r_o}{2}$$

$$R_{down} = \left[1 + 2g_{m1} \frac{r_o}{2} \right] \frac{R_{sh}}{2} + \frac{r_o}{2}$$

$$g_{mi} = \sqrt{2I_{SS} \mu_n C_{ox} \left(\frac{W}{L} \right)_i}$$

$$r_{oi} = \frac{1}{\lambda I_{SS}}$$

Q2

Calculate the small signal differential voltage gain v_{out}/v_d of the circuit shown below. The bias current source I_{SS} is ideal and equal to $45 \mu\text{A}$.

Take :

Supply voltage (V_{dd}) = 3.3 V

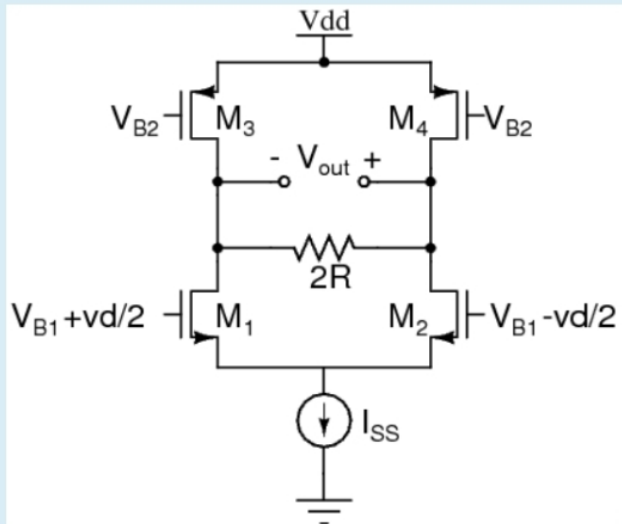
$\mu_n c_{ox} = 200 \mu\text{A}/\text{V}^2$, $\mu_p c_{ox} = 100 \mu\text{A}/\text{V}^2$

$V_{TN} = |V_{TP}| = 0.7 \text{ V}$

$(W/L)_{1,2} = 1$, $(W/L)_{3,4} = 4$

Resistor $R = 117 \text{ K}\Omega$

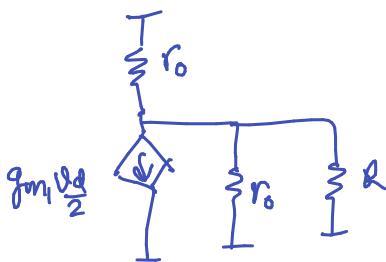
Channel length modulation parameter (λ) = 0.01 V^{-1}



Give your answer correct upto 3 decimal places.



small signal half CRT



$$r_o = r_{on} = r_{op} = \frac{1}{\lambda \frac{I_{SS}}{2}}$$

$$g_{m1} = \sqrt{\frac{2 I_{SS}}{2} \ln(c_{ox} \left(\frac{W}{L}\right)_1}$$

$$\boxed{\frac{v_{out1}}{\frac{v_d}{2}} = -g_{m1} (r_{o/2} \parallel R)}$$

Similarly, $v_{out2} = +g_{m1} \left(\frac{r_o}{2} \parallel R\right) \frac{v_d}{2}$

$$\therefore A_{dm} = \frac{v_{out2} - v_{out1}}{v_d} = g_{m1} \left(\frac{r_o}{2} \parallel R\right)$$

03

Determine the incremental differential voltage gain v_{out}/v_d of the circuit shown below. Assume the bias current source I_{SS} to be ideal and equal to $26 \mu\text{A}$.

Other parameters are listed below:

Supply voltage (V_{dd}) = 3.3 V

Resistor $R = 1 \text{ M}\Omega$

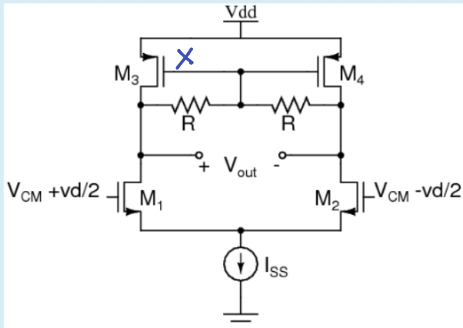
$\mu_n c_{ox} = 100 \mu\text{A}/\text{V}^2$, $\mu_p c_{ox} = 50 \mu\text{A}/\text{V}^2$

$V_{TN} = |V_{TP}| = 0.7 \text{ V}$

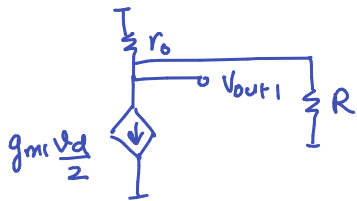
$(W/L)_{1,2} = 1$, $(W/L)_{3,4} = 2$

Channel length modulation parameter (λ) = 0.01 V^{-1}

Give your answer correct upto 3 decimal places.



$x \rightarrow$ signal gnd



$$r_o = r_{o3} = r_{o1} = \frac{1}{\lambda I_{SS}/2}$$

$$g_{m1} = \sqrt{2 \left(\frac{I_{SS}}{2} \right) \mu_n c_{ox} \left(\frac{W}{L} \right)_1}$$

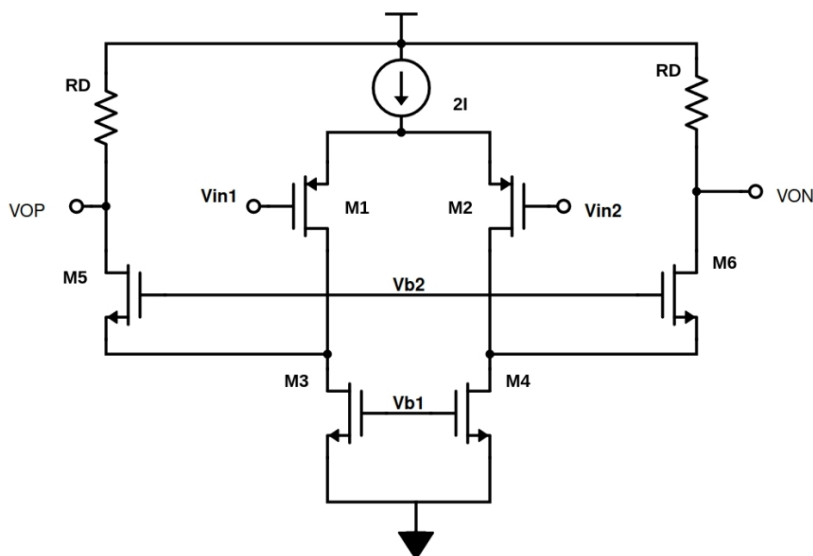
$$v_{out1} = -g_{m1} \left(\frac{r_o}{2} \parallel R \right) \frac{v_d}{2}$$

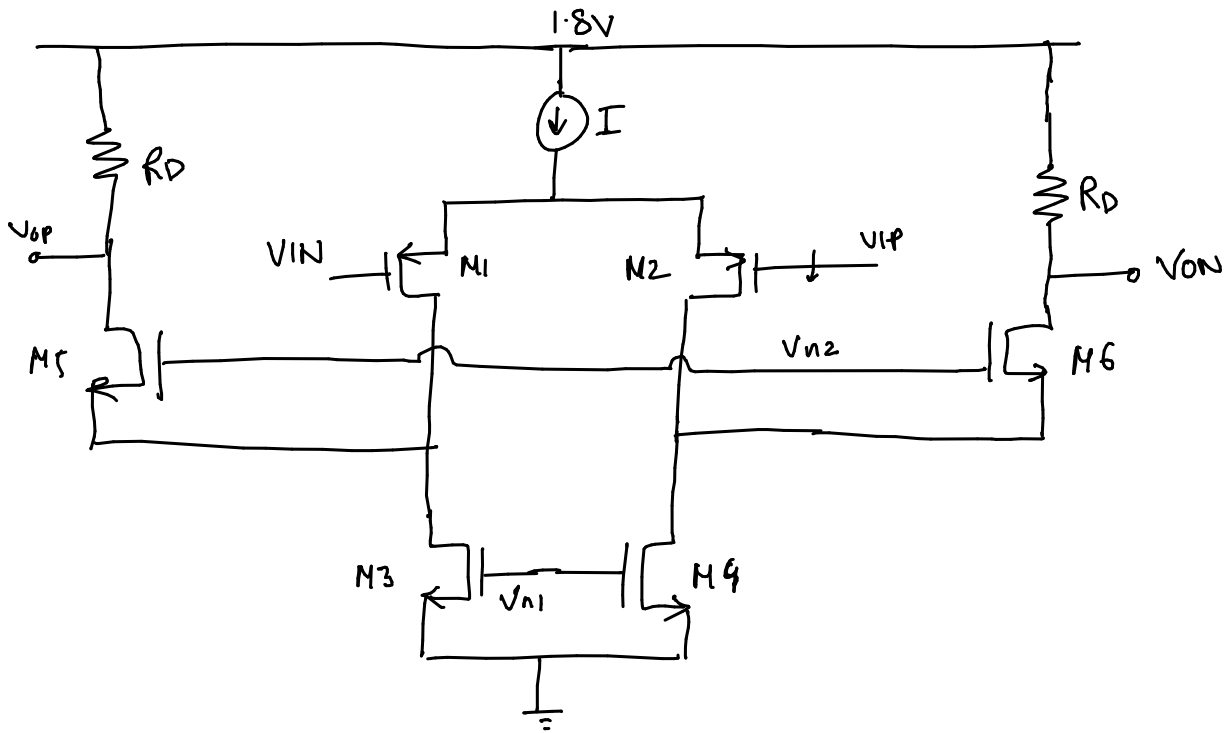
$$\text{Similarly, } v_{out2} = +g_{m1} \left(\frac{r_o}{2} \parallel R \right) \frac{v_d}{2}$$

$$A_d = \frac{v_{out1} - v_{out2}}{v_d} = -g_{m1} \left(\frac{r_o}{2} \parallel R \right)$$

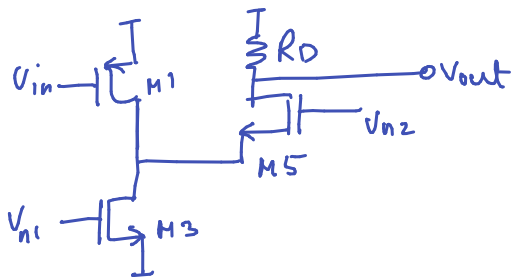
04

For the amplifier shown in the given circuit, find the differential mode gain (with polarity) if small signal output impedance due to short channel effects of each MOSFET is $27 \text{ k}\Omega$, and g_m is 5 mS . The resistance R_D is $227 \text{ k}\Omega$. (At least 1% accuracy is expected.)

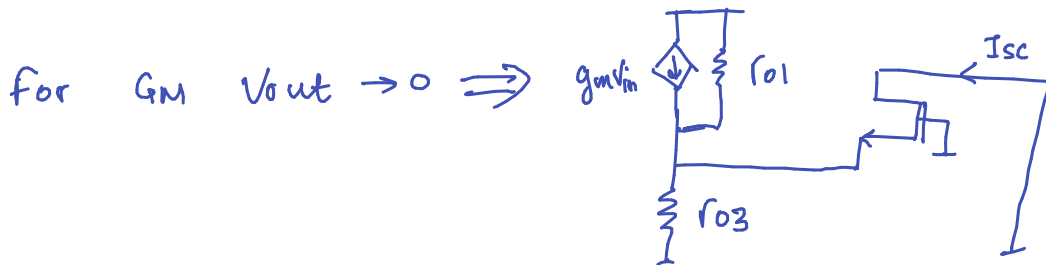




Differential half circuit



$$G_M \approx g_{m1}$$



$$I_{sc} = -g_{m1} V_{in} \left[\frac{r_{o3} + r_{o1}}{r_{o3} + r_{o1} + r_{gs5}} \right] \Rightarrow g_{m1} V_{in} \left[\frac{2g_{m1}r_o}{2g_{m1}r_o + 1} \right]$$

$$\therefore G_M \approx -g_{m1}$$

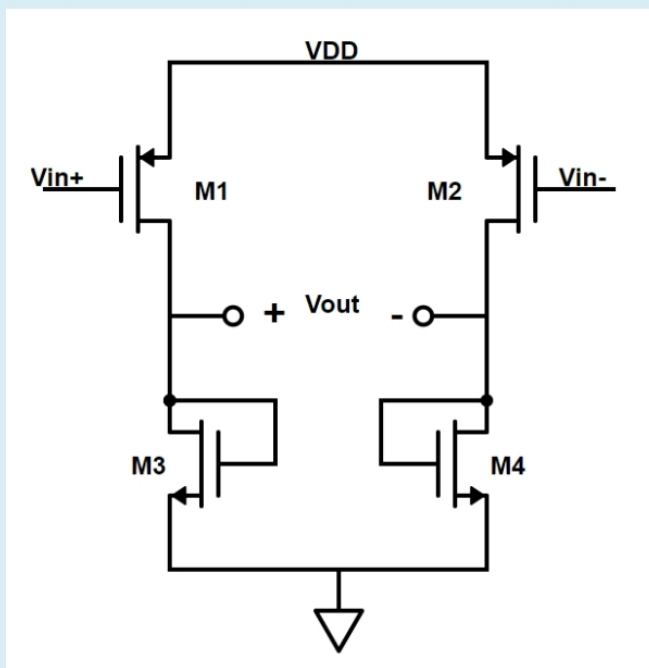
$$R_{out} = R_D \parallel \left[(1 + g_{m5}r_{o5})(r_{o3} \parallel r_{o1}) + r_{o5} \right]$$

$$\approx R_D \parallel \left[g_{m5}r_{o5}(r_{o3} \parallel r_{o1}) \right] \Rightarrow R_D \parallel g_{m1}r_o^2/2$$

$$\therefore A_d = -g_m \left[R_D \parallel \frac{g_{m5}}{g_{ds5}(g_{ds3} + g_{ds1})} \right]$$

05

Calculate the CMRR (in dB) of the below circuit. Assume $g_{m3}=g_{m4}$, $g_{m1}=g_{m2}$, $g_{m1}=2g_{m3}$, $r_{o1}=r_{o2}=r_{o3}=r_{o4}=49 \text{ k}\Omega$.



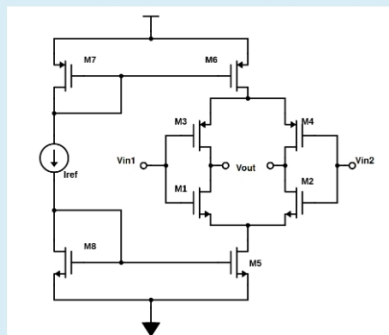
This circuit is a "pseudo-differential pair"

Despite having the same diff. gain, it is incapable of rejecting common mode signals.

$$\text{CMRR} = 20 \log_{10} \left(\frac{A_{cm}}{A_d} \right) = 0 \text{ dB}$$

06

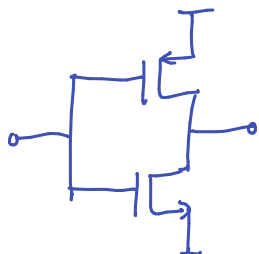
Consider the active load differential amplifier shown in the figure used to enhance the trans-conductance. If I_{ref} is an ideal current source of $247 \mu\text{A}$, find the CMRR ($|A_d/A_{cm}|$) of the arrangement (1% error tolerance) given that $(W/L)_6 = (W/L)_7$; $(W/L)_8 = (W/L)_5$; $g_{m1,2} = g_{m3,4} = 3 \text{ mS}$. Take Channel length modulation into account ($\lambda=0.1$).



$$r_{o6} = \frac{1}{\lambda I_{ref}} = r_o$$

$$r_{o3} = r_{o1} = 2r_{o6} = 2r_o$$

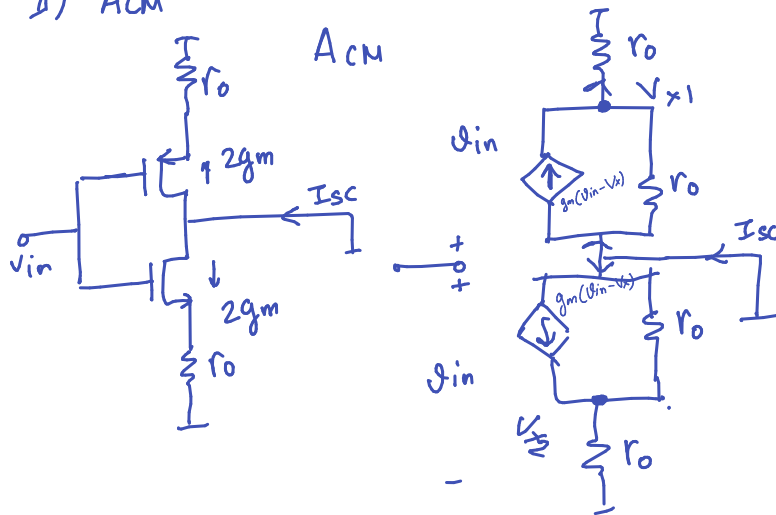
I) A_{dm}



$$\Rightarrow (g_{mn} + g_{mp})(r_{on} \parallel r_{op})$$

$$\Rightarrow \frac{g_{mn} + g_{mp}}{g_{dsn} + g_{dsp}} \approx g_m r_o$$

II) A_{CM}



$$V_{x1} = V_{x2} = V_x \text{ (by symmetry)}$$

$$\frac{I_{sc}}{2} = \frac{V_x}{r_o} \Rightarrow V_x = \frac{I_{sc} r_o}{2}$$

Also,

$$\frac{I_{sc}}{2} = 2g_m(V_{in} - V_x) - \frac{V_x}{r_o}$$

$$\frac{I_{sc}}{2} = 2g_m V_{in} - V_x \left(2g_m + \frac{1}{r_o} \right)$$

$$\frac{I_{sc}}{2} = 2g_m V_{in} - \frac{I_{sc}}{2} (2g_m r_o + 1)$$

$$\frac{I_{sc}}{2} (1 + 2g_m r_o + 1) = 2g_m V_{in}$$

$$I_{sc} (1 + g_m r_o) = 2g_m V_{in}$$

$$G_m = \frac{2g_m}{1 + g_m r_o} \approx \frac{2}{r_o}$$

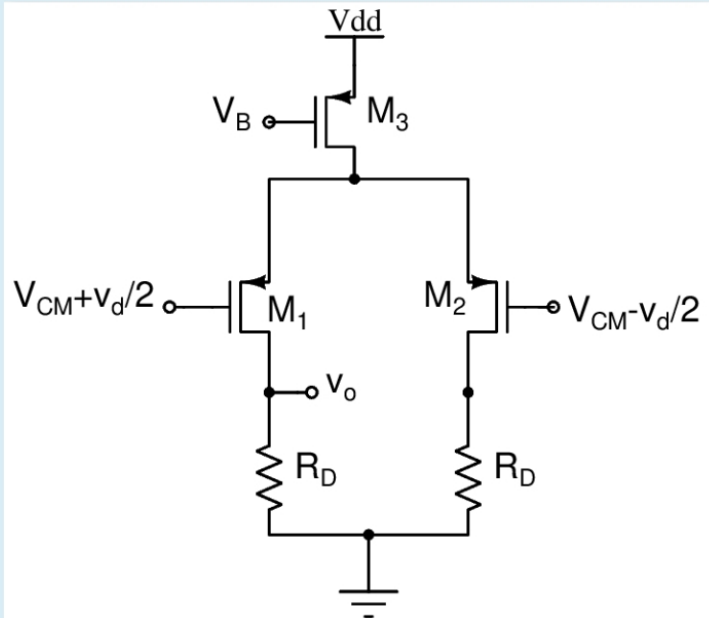
$$R_{out} = \frac{(1 + 2g_m r_o) r_o}{2} \approx g_m r_o^2$$

$$\therefore A_{CM} = 2g_m r_o$$

$$CMRR = \frac{A_{CM}}{A_d} = 2$$

Q7

In the circuit shown below, Find Maximum single ended peak to peak output swing. Given $V_{DD}=8.6$ V, Overdrive voltage of all transistors is 0.2 V.



$$V_{o, \max} = V_{DD} - V_{SD, \text{sat}3} - V_{SD, \text{sat}1}$$

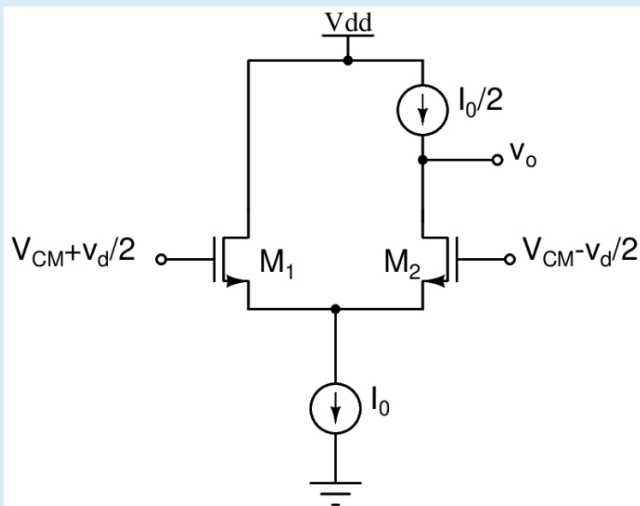
$$= V_{DD} - 2V_{SD, \text{sat}}$$

$$V_{o, \min} = 0V$$

$$\text{single-ended } V_{pp, \text{out}} = V_{DD} - 2V_{SD, \text{sat}}$$

Q8

In the circuit shown below, Calculate single ended gain. $I_0=163 \mu A$, $\lambda=0.01 \text{ v}^{-1}$, Given $V_{DD} = 1.8$ V, aspect ratio of transistors is 2, Mos parameters are $\mu_n C_{ox} = 100 \mu A/V^2$, $V_{TH}=0.5$ V. (Accuracy= ± 0.001)



$$A_v = G_m R_{out}$$



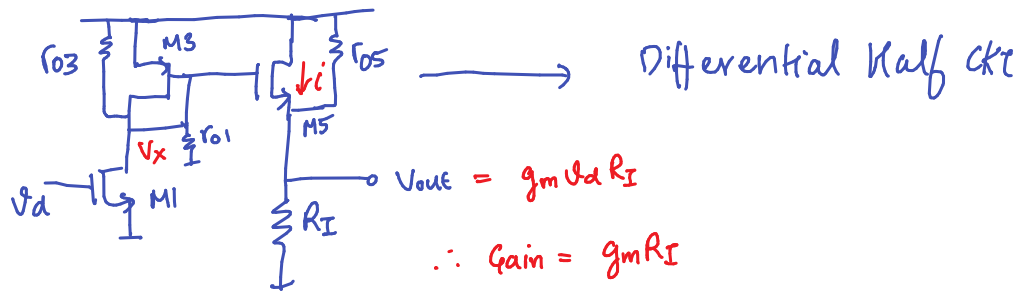
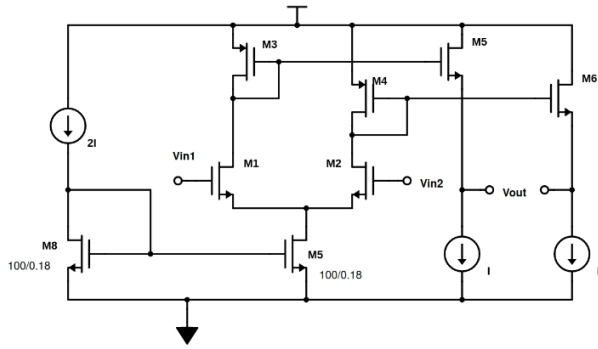
$$\frac{i_{out}}{v_{gs}} = \frac{g_m}{2}$$

$$R_{out} : (1 + g_m r_o) \frac{1}{g_m + r_o} \approx 2r_o$$

$$\therefore \text{gain} = \left(\frac{g_m}{2} \right) (2r_o) = g_m r_o$$

Q9

Find the magnitude of small signal differential gain, $|A_d|$, of the circuit below assuming all transistors are biased in saturation region. The current sources are non-ideal with shunt resistance of 1250Ω . $(W/L)_{5,6} = 1 \times (W/L)_{3,4}$, $g_{m1} = g_{m2} = 1 \text{ mS}$. Small signal output impedance of all transistors is same, 1428.5714285714Ω . [$\pm 1\%$ error tolerated].



$$I) \frac{V_x}{V_d} = \frac{g_{m1}}{g_{m3} + g_{ds1} + g_{ds3}} \quad \left[g_{ds1} = \frac{1}{r_{o1}} \right]$$

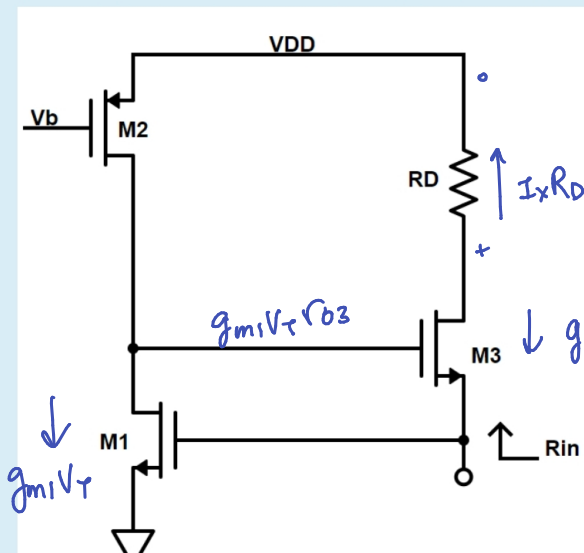
$$\frac{V_{out}}{V_x} = \frac{g_{m5}}{G_I + g_{ds5} + g_{m5}}$$

$$\therefore \text{gain} = \frac{g_{m1} g_{m5}}{(g_{m3} + g_{ds1} + g_{ds3})(G_I + g_{ds5} + g_{m5})}$$

$$= \frac{\sqrt{2} g_m^2}{(g_m + 2g_{ds})(G_I + g_{ds} + \sqrt{2} g_m)}$$

Q10

Calculate the equivalent Thevenin resistance R_{in} in Ω . [Given $R_D = 1.1 \text{ K}\Omega$ $g_{m1} = g_{m2} = g_{m3} = 1.37 \text{ mS}$, $r_{o1} = r_{o2} = 2r_{o3}$, $r_{o3} = 1.1 \text{ M}\Omega$. Accuracy required ± 0.05]



$$g_{m1} V_T r_{o3} \downarrow g_{m3} (g_{m1} r_{o3} - 1) V_x + \frac{I_x R_D - V_x}{r_{o3}} = -I_x$$

$$(g_{m1} g_{m3} r_{o3} - \frac{1}{r_{o3}}) V_x = -I_x (1 + R_D)$$

$$R_{in} = \frac{R_D + r_{o3}}{1 + (g_{m1}(r_{o1} \parallel r_{o2}) + 1) g_{m3} r_{o3}}$$

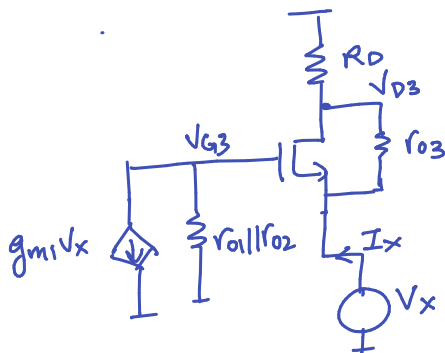
$$= \frac{R_D + r_{o3}}{1 + \left(g_m \frac{r_{o1}}{2} + 1\right) g_m r_{o3}}$$

$$= \frac{R_D + r_{o3}}{1 + (g_m r_{o3} + 1) g_m r_{o3}}$$

$$r_{o3} \gg R_D, \quad g_m r_{o3} \gg 1$$

$$= \frac{r_{o3}}{1 + g_m^2 r_{o3}^2} \Rightarrow \frac{1}{g_m^2 r_{o3}}$$

Alternatively,



$$V_{G3} = g_{m1} V_x (r_{o1} \parallel r_{o2})$$

$$I_x = \frac{V_{D3}}{R_D}$$

$$-I_x = -g_{m3}(V_{G3} - V_x) + \frac{(V_{D3} - V_x)}{r_{o3}}$$

$$-I_x = -g_{m3} \left(g_{m1}(r_{o1} \parallel r_{o2}) \right) V_x + \frac{I_x R_D - V_x}{r_{o3}}$$

$$V_x \left(-g_{m3} g_{m1} \frac{r_{o1}}{2} - \frac{1}{r_{o3}} \right) + I_x \left(\frac{R_D + 1}{r_{o3}} \right) = 0$$

$$R_{in} = \frac{\frac{R_D}{r_{o3}} + 1}{g_{m3} g_{m1} \frac{r_{o1}}{2} - \frac{1}{r_{o3}}}$$

$$\approx \frac{1}{g_m^2 r_{o3}}$$