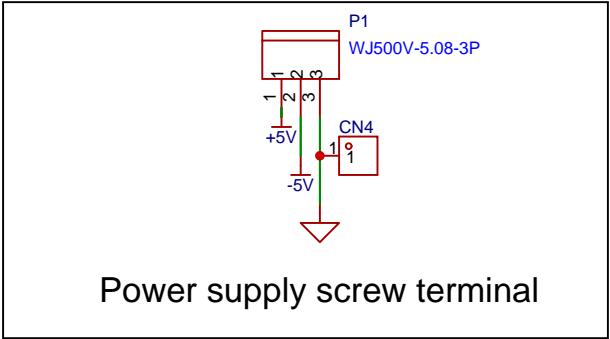
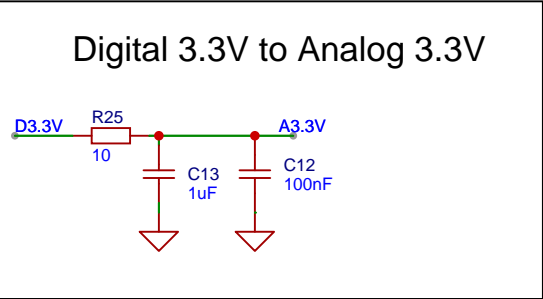
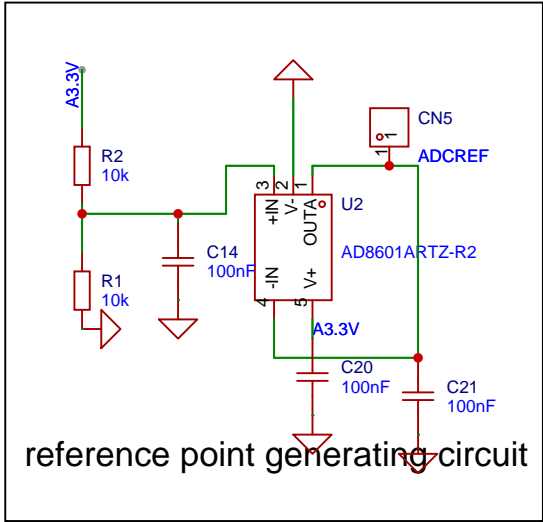


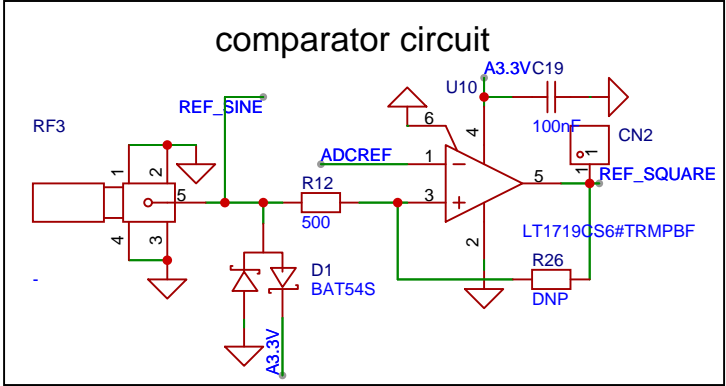
interface/switch for tuning bandwidth and gain



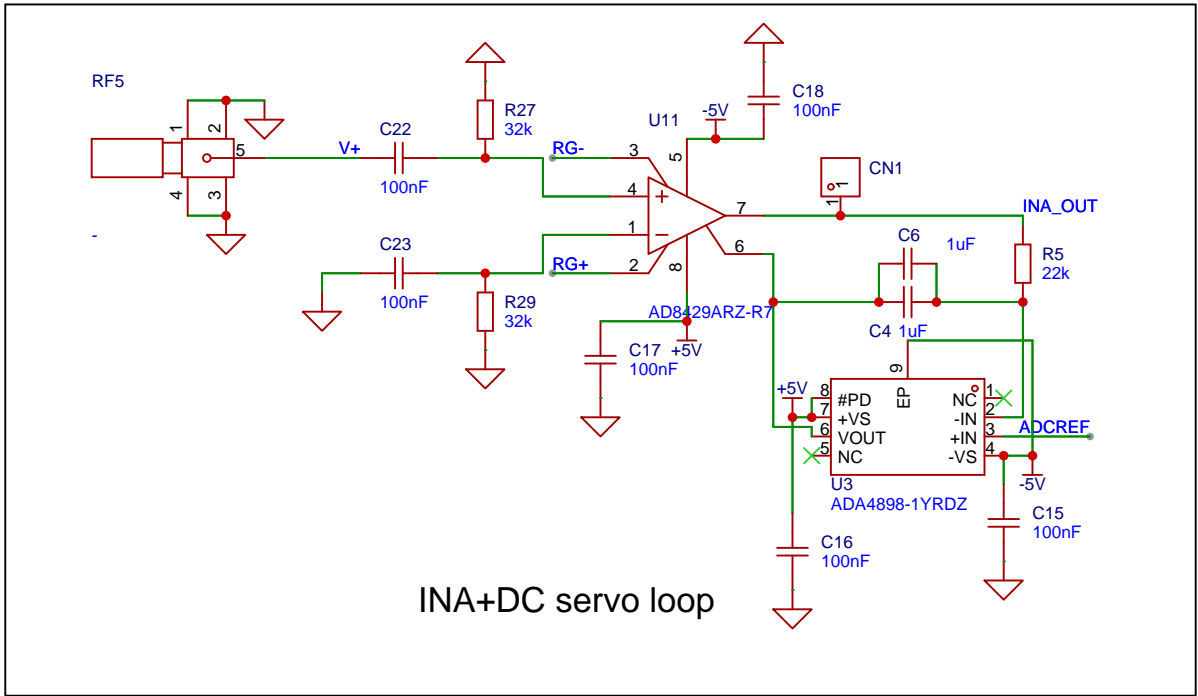
Power supply screw terminal



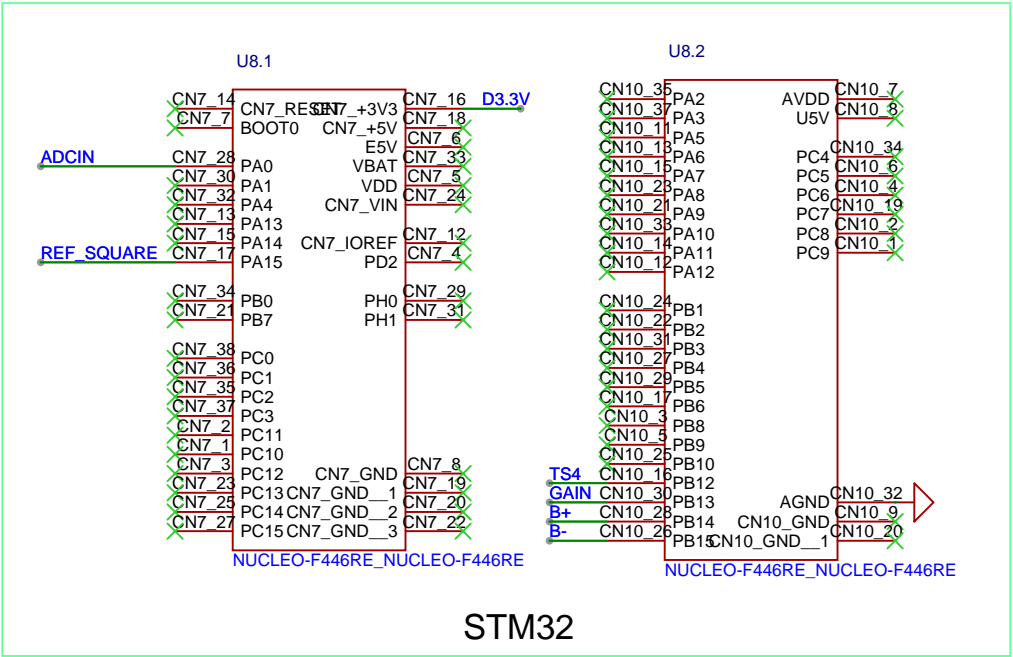
reference point generating circuit



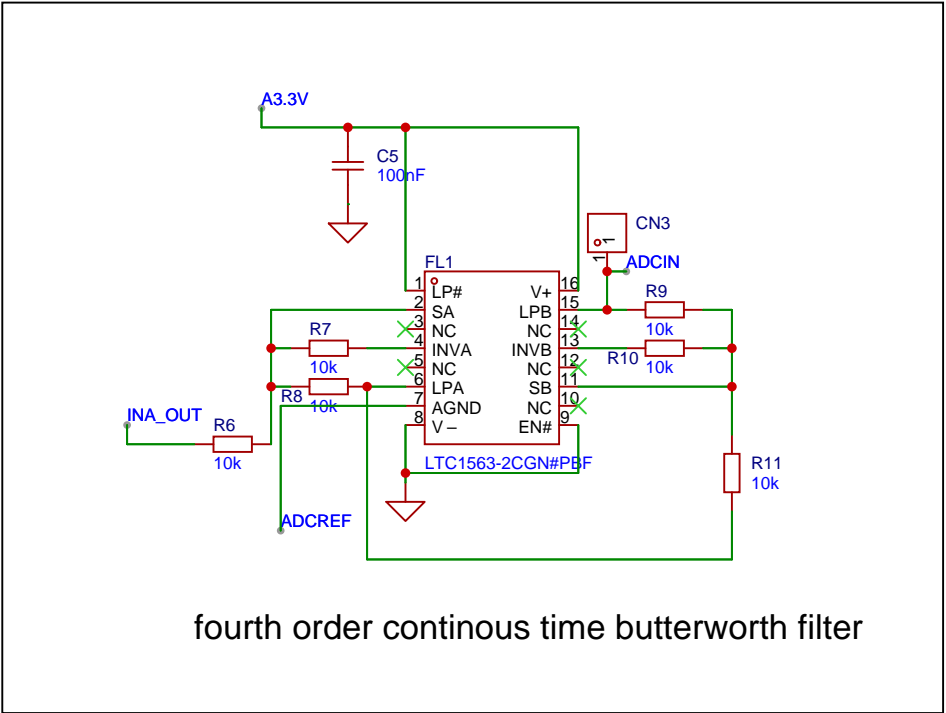
comparator circuit



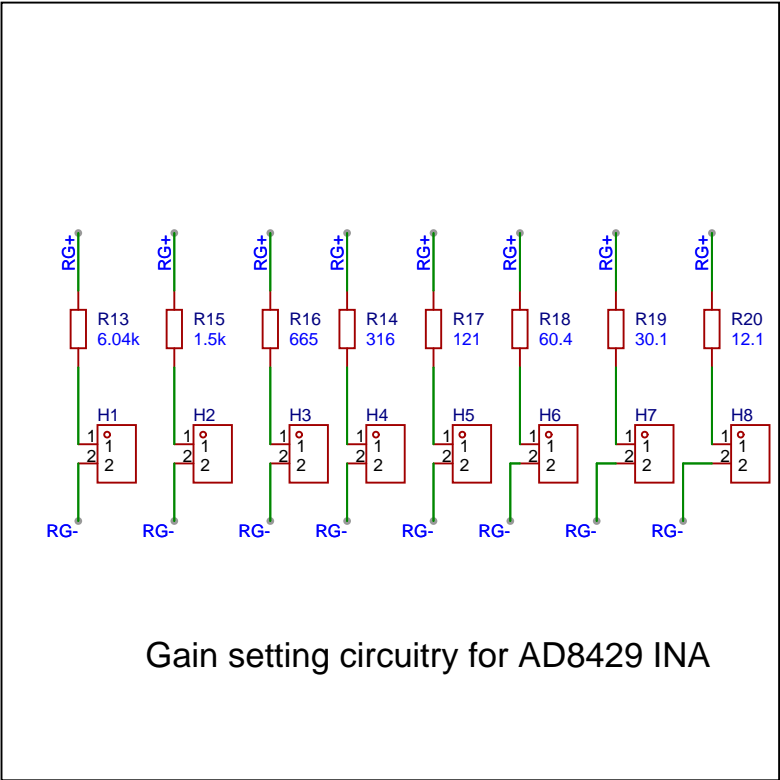
INA+DC servo loop




STM32



fourth order continuous time butterworth filter



Gain setting circuitry for AD8429 INA

原理图	AFE			创建日期	2025-11-14
				更新日期	2025-12-15
板子	AFE			图页	P1
绘制		Lockin Amplifier			
审阅					
		版本	尺寸	页 1 共 1	
		V1.0	A4	嘉立创EDA	