

# STM32F429I-DISC1

MB1075

## Table of contents

- Sheet 1: Project overview (this page)
- Sheet 2: Top
- Sheet 3: STM32F429ZIT6 MCU
- Sheet 4: SDRAM 64Mbits
- Sheet 5: USB\_OTG\_FS
- Sheet 6: LCD 2.4"
- Sheet 7: Peripherals
- Sheet 8: ST-LINK/V2-B (SWD only)

## Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

Notes to generate the board layout.

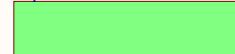
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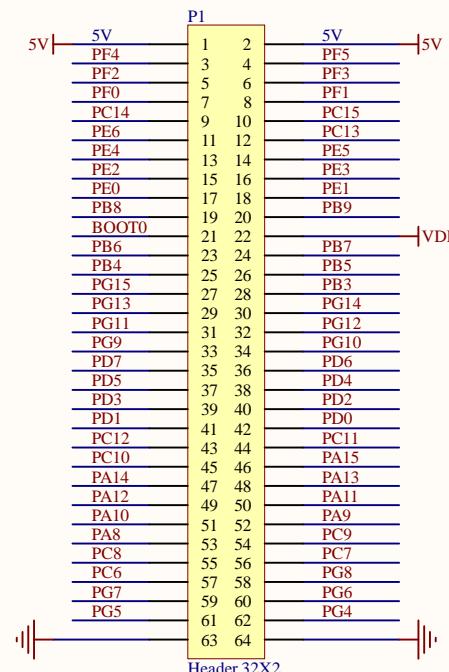
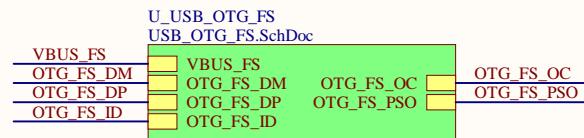
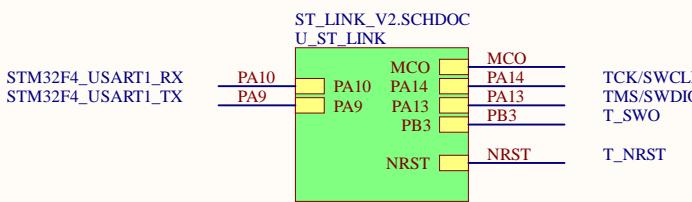
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U\_Top  
Top.SchDoc

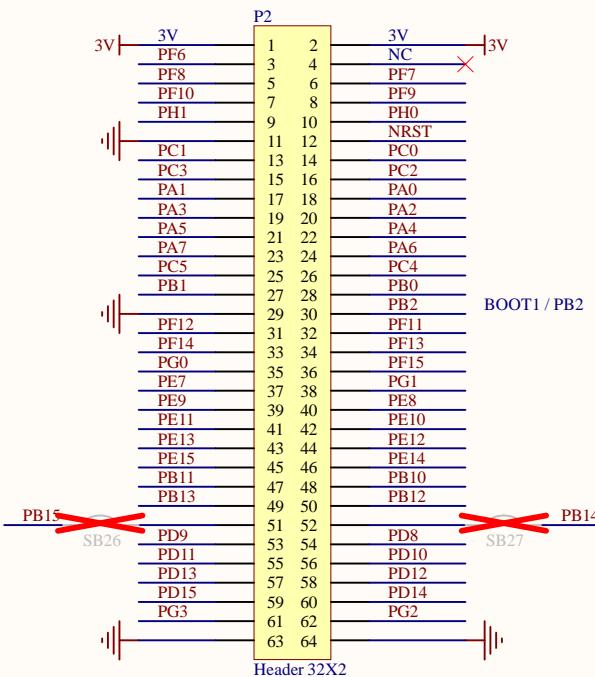
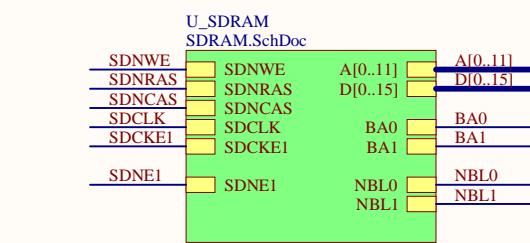
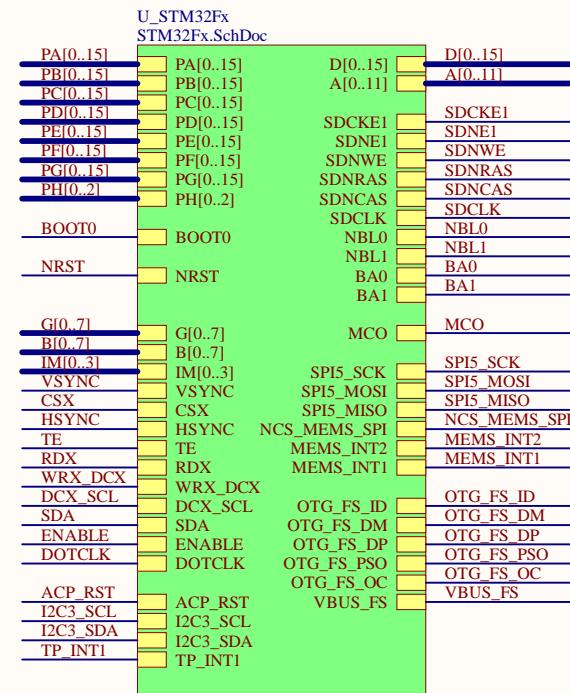
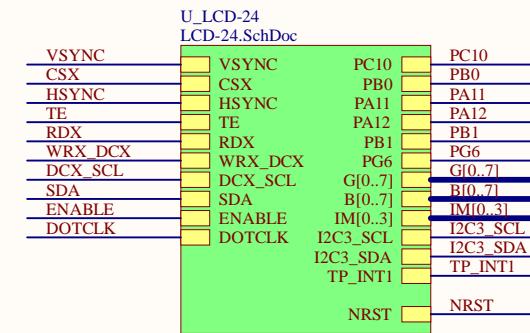


Title: <b>Project overview</b>	Project: <b>STM32F429I-DISC1</b>
Variant: <b>F429I</b>	
Revision: <b>E-01</b>	Reference: <b>MB1075</b>
Size: <b>A4</b>	Date: <b>11-MAY-2020</b>
	Sheet: <b>1 of 8</b>



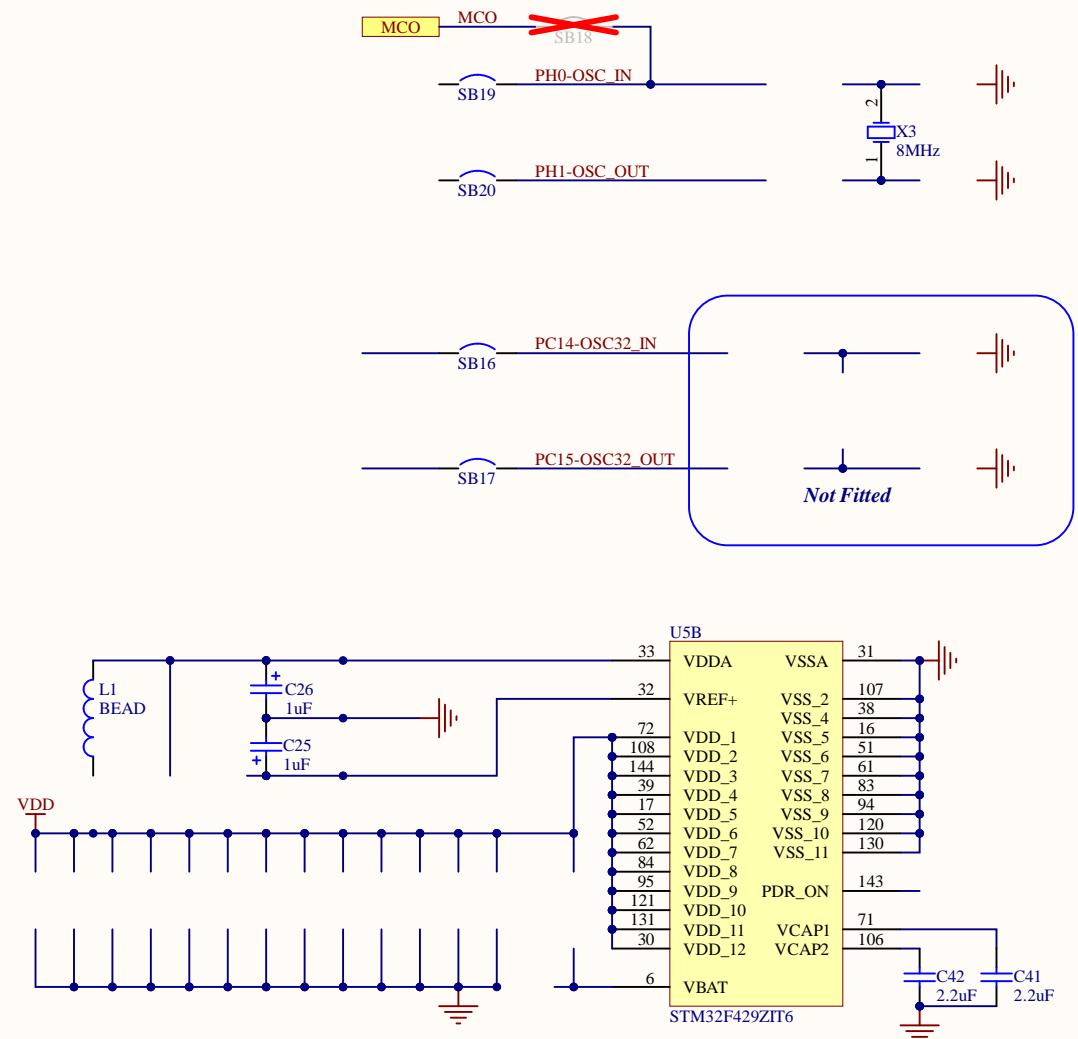
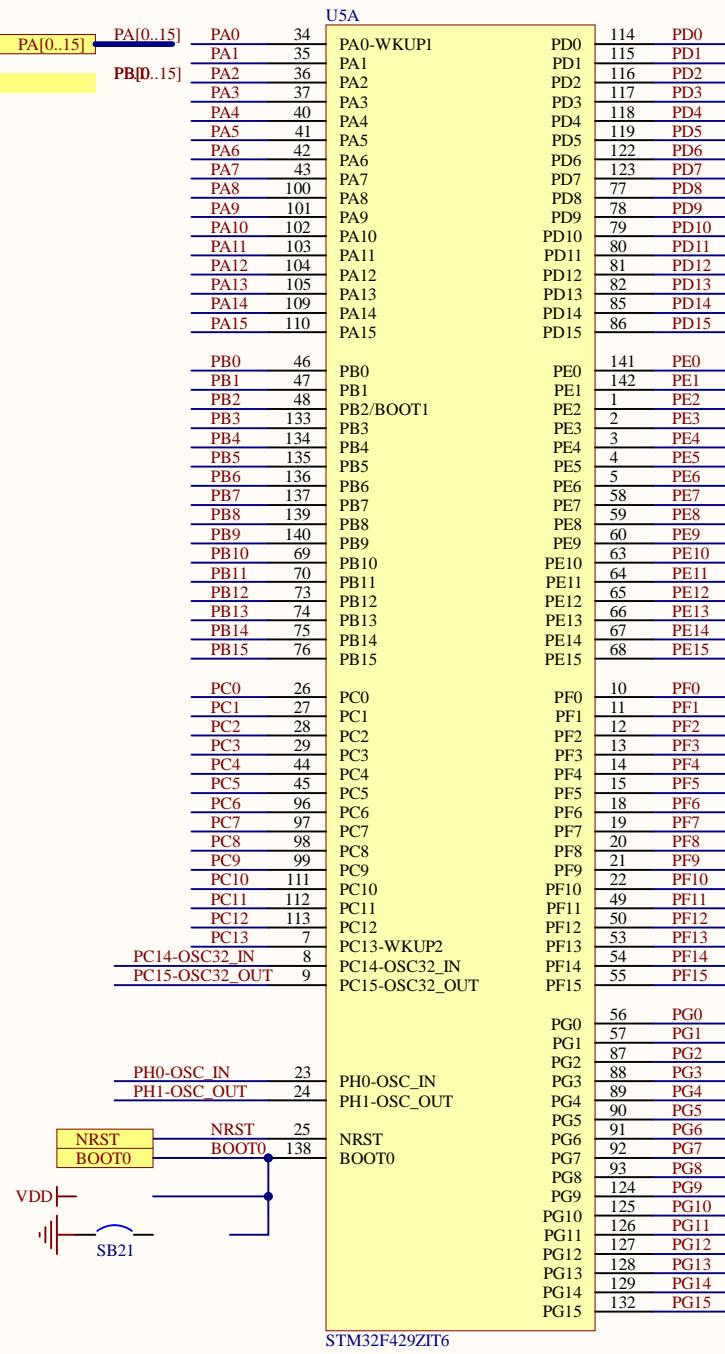


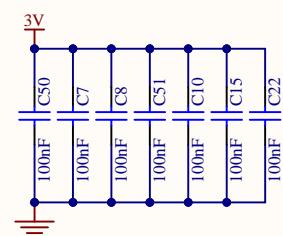
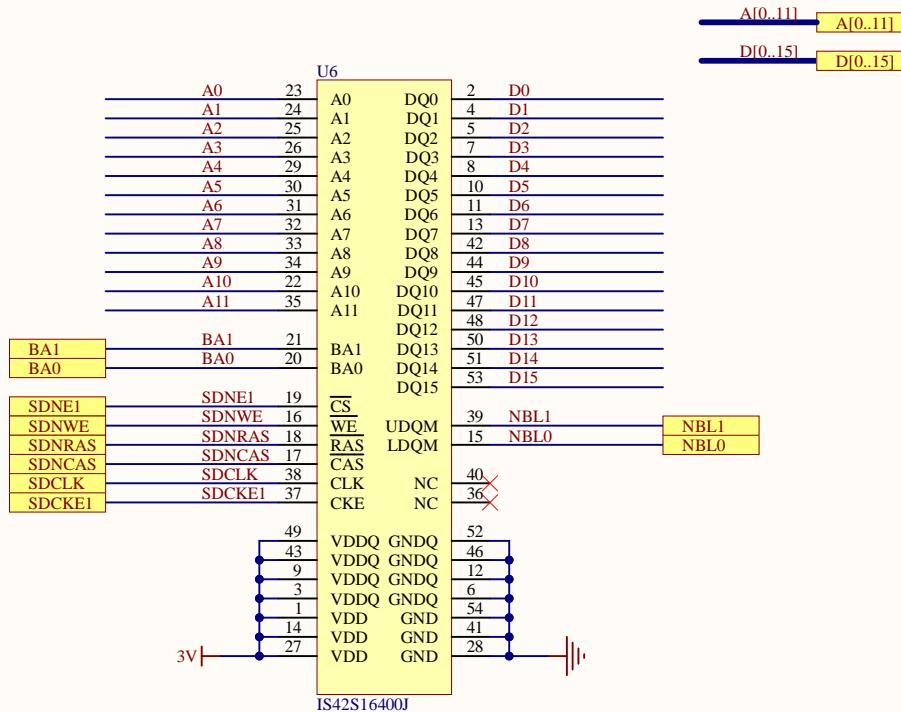
STICKER PRODUCT HW8      STICKER BOARD HW9



Title: Top	
Project: STM32F429I-DISCI	
Variant: F429I	
Revision: E-01	Reference: MB1075
Size: A4	Date: 11-MAY-2020
	Sheet: 2 of 8







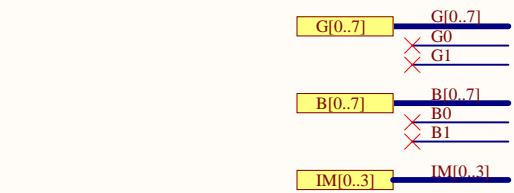
Place close SDRAM

Title: <b>SDRAM 64Mbits</b>	
Project: <b>STM32F429I-DISC1</b>	
Variant: <b>F429I</b>	
Revision: E-01	Reference: <b>MB1075</b>
Size: <b>A4</b>	Date: <b>11-MAY-2020</b>
Sheet: <b>4</b> of 8	

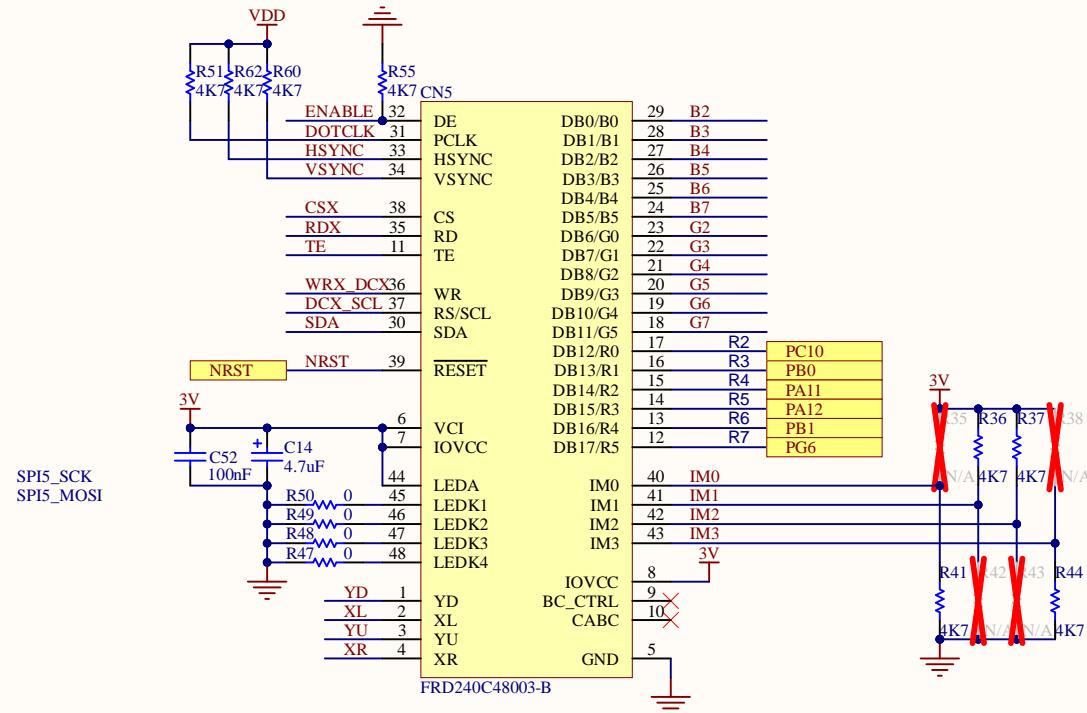


Title: [USB\\_OTG\\_FS](#)

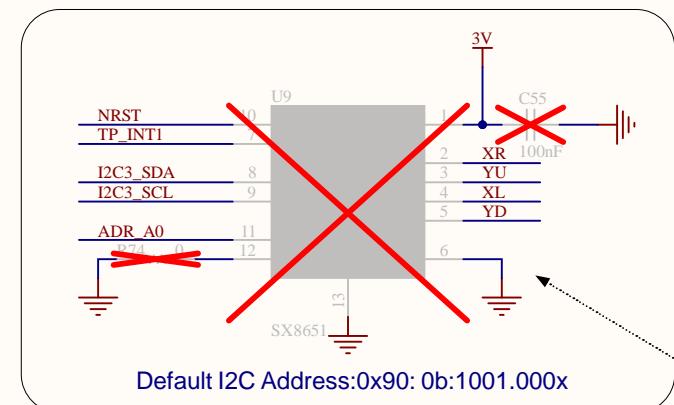
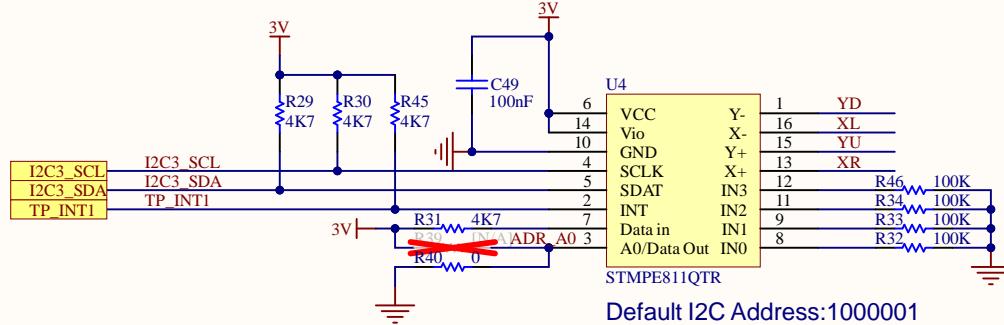
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Size: A4	Sheet: 5 of 8

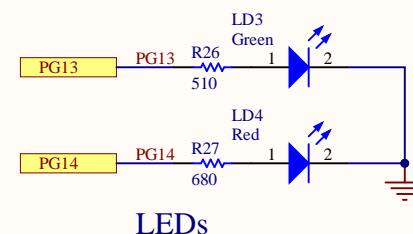
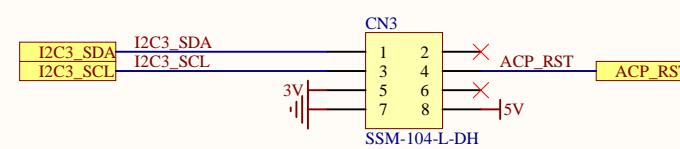
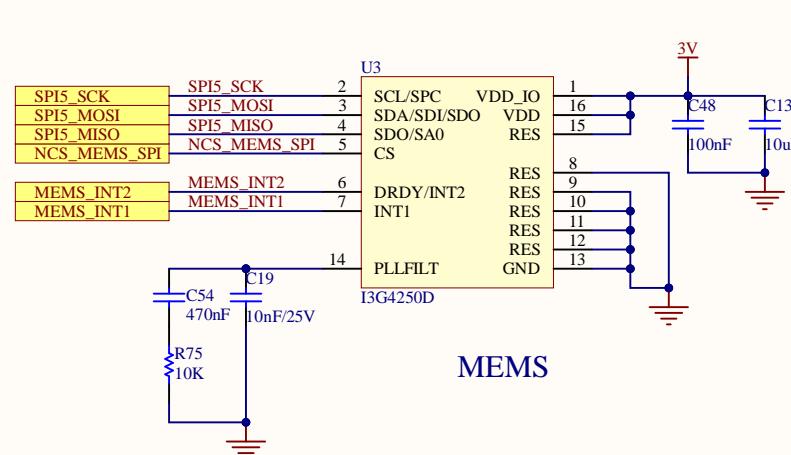
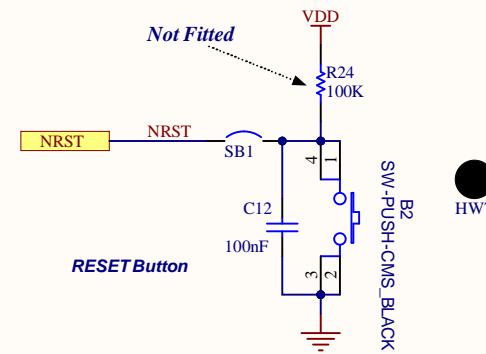
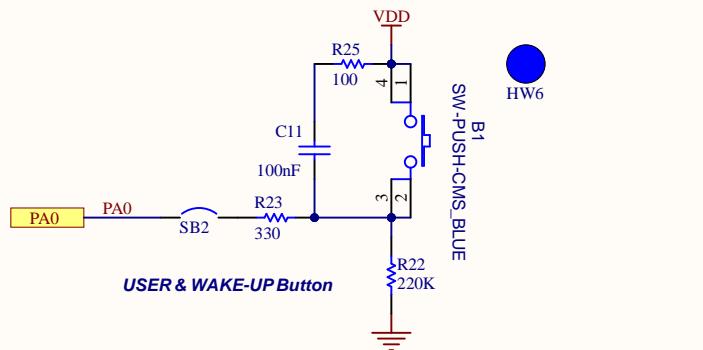


VSYNC	VSYNC
CSX	CSX
H SYNC	H SYNC
TE	TE
RDX	RDX
WRX_DCX	WRX_DCX
DCX_SCL	DCX_SCL
SDA	SDA
ENABLE	ENABLE
DOTCLK	DOTCLK



IM[0..3] = 0110 --> 4-wire 8-bit serial I, SDA:In/Out





Title:	Peripherals
Project:	STM32F429I-DISCI
Variant:	F429I
Revision:	E-01
Size: A4	Date: 11-MAY-2020
	Reference: MB1075
	Sheet: 7 of 8



