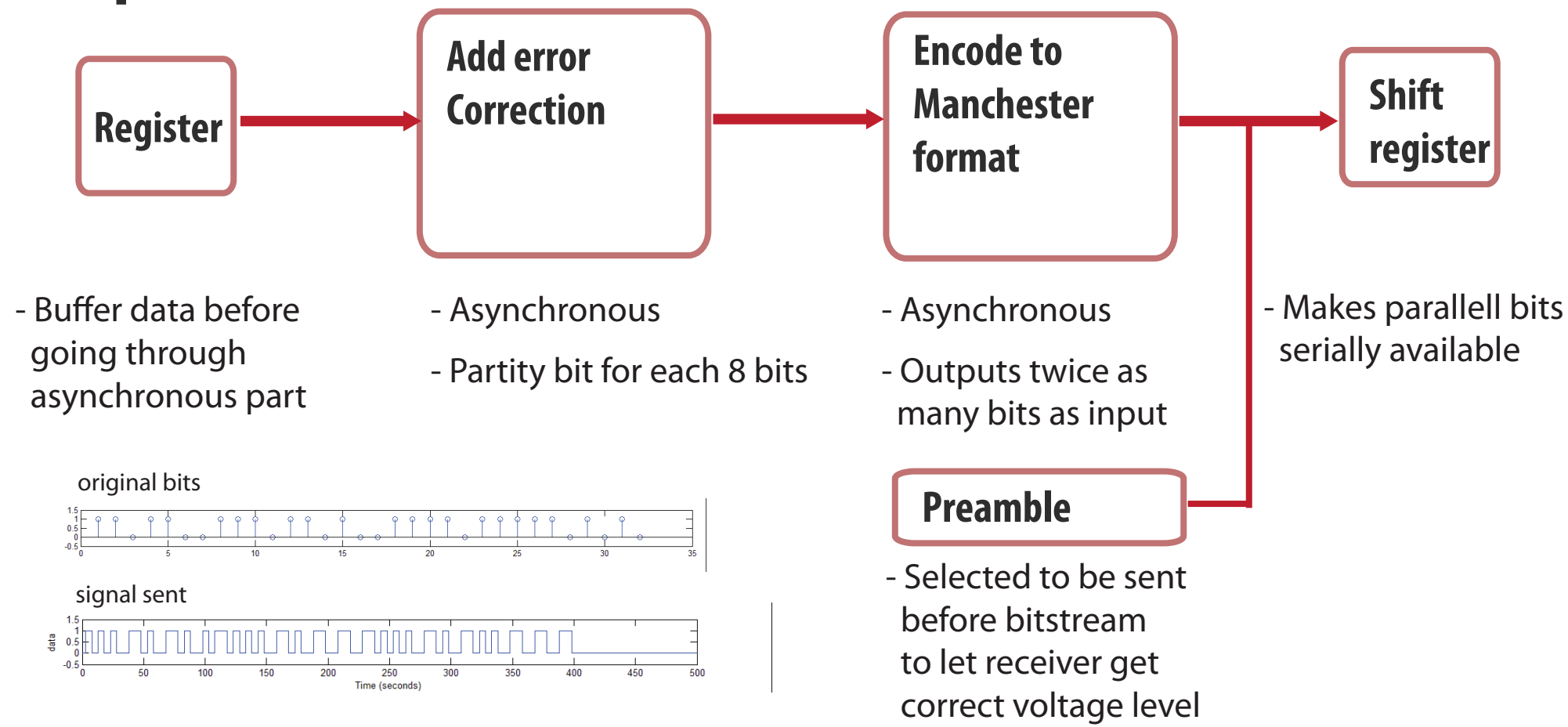
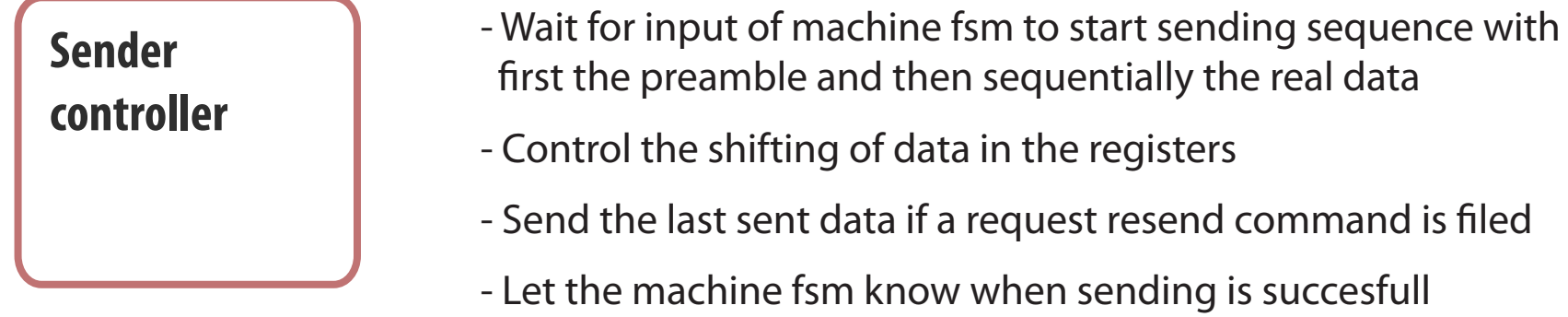


Sender

Datapath

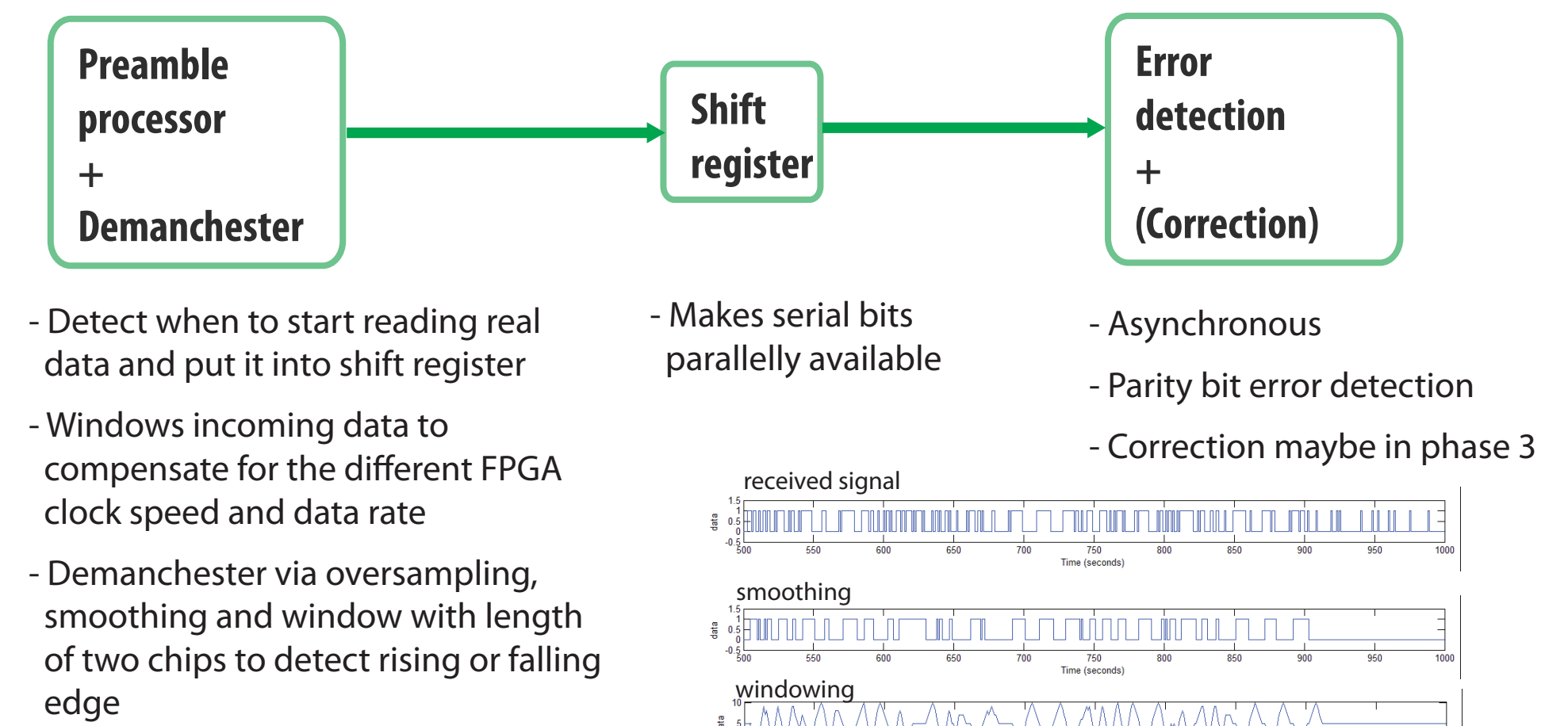


Finite State Machine

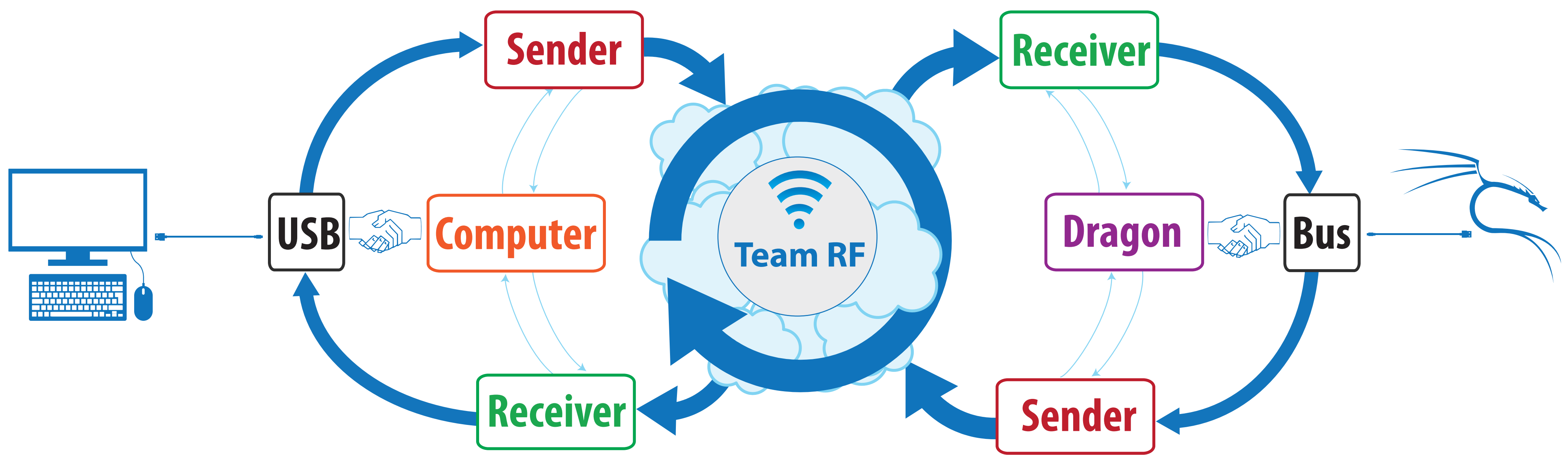
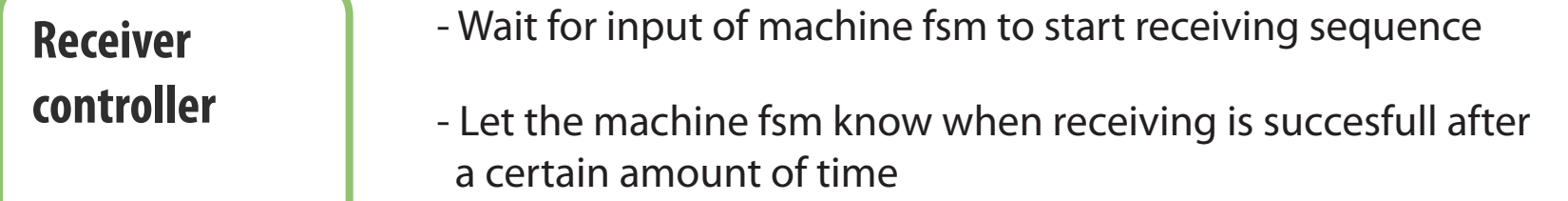


Receiver

Datapath

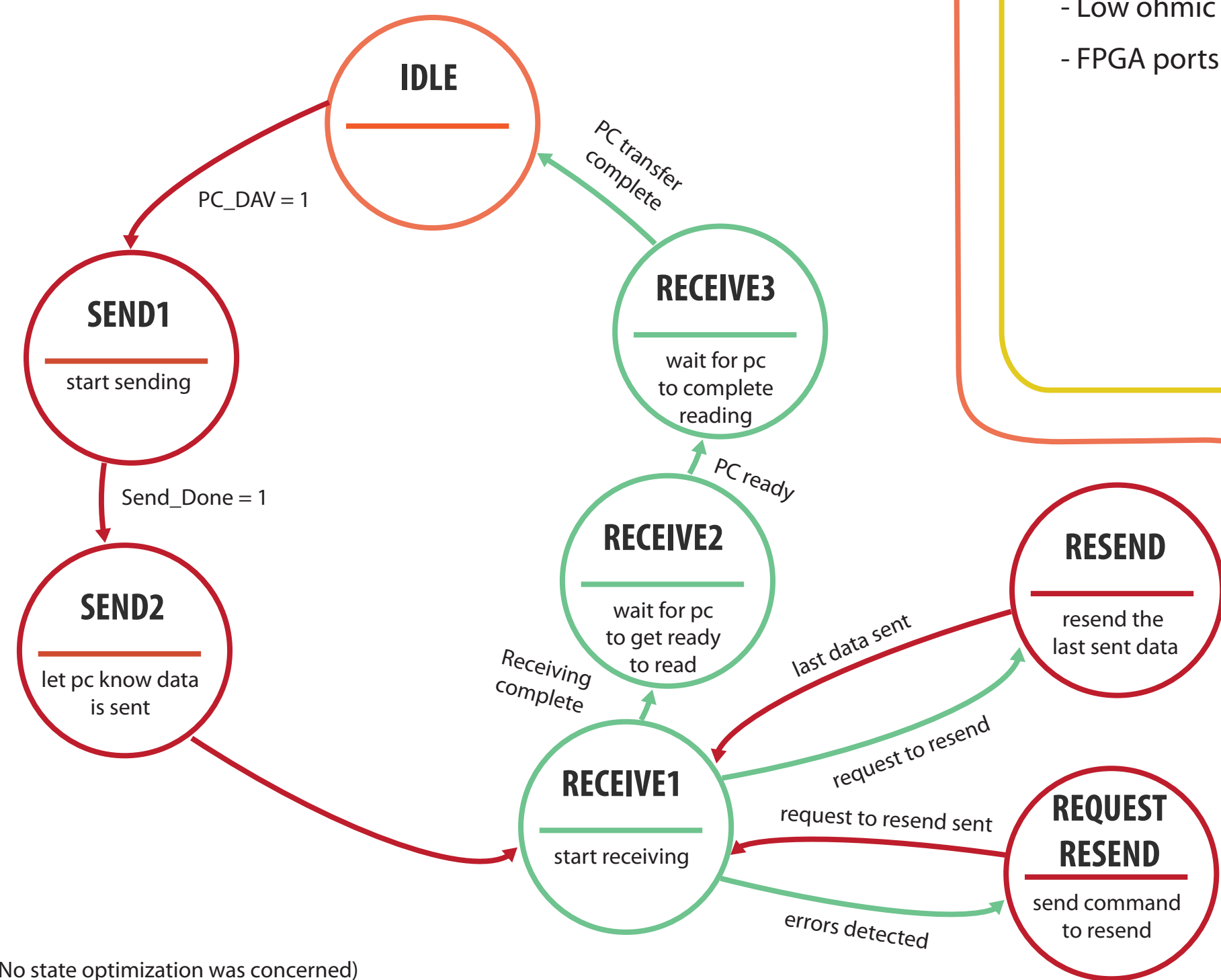


Finite State Machine



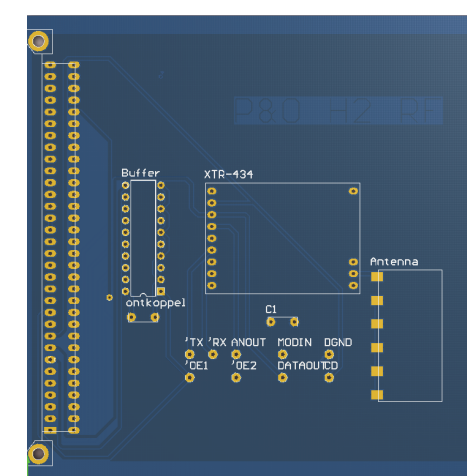
Computer

Finite State Machine



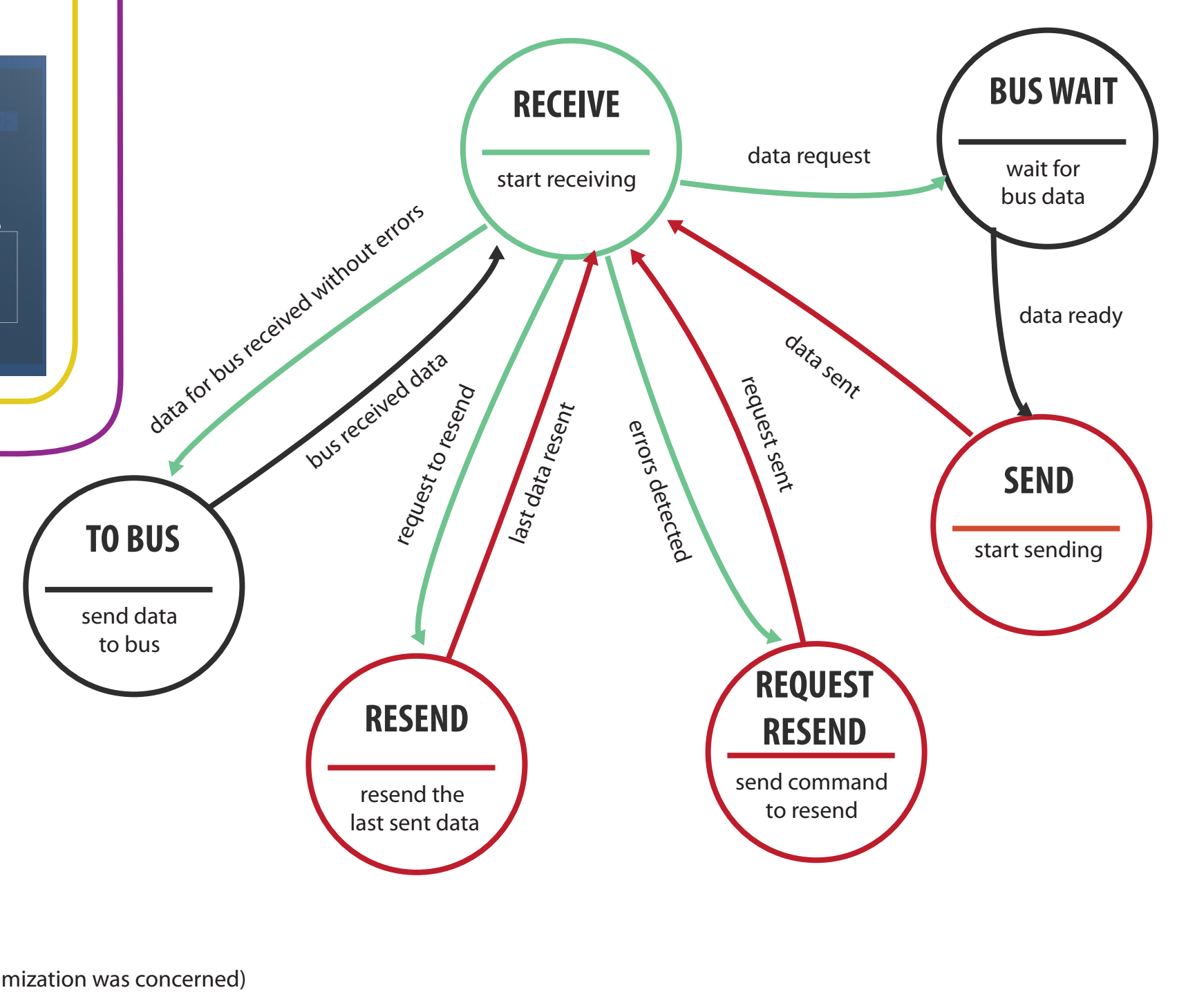
PCB

- Decoupling and shielding for low noise
- Low ohmic line to antenna
- FPGA ports buffered



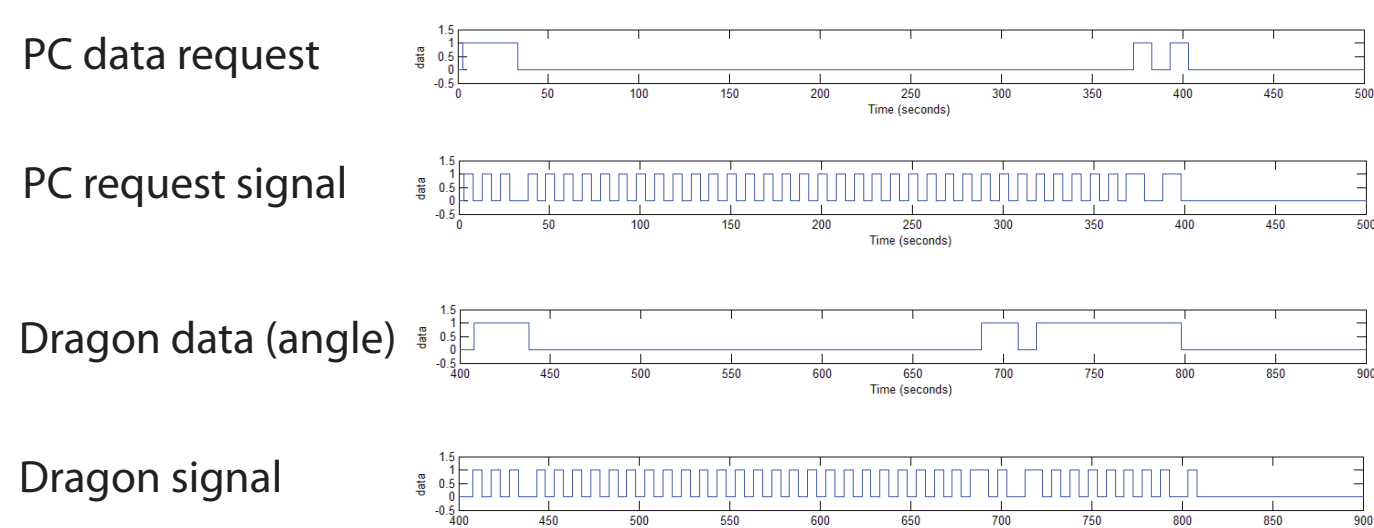
Dragon

Finite State Machine

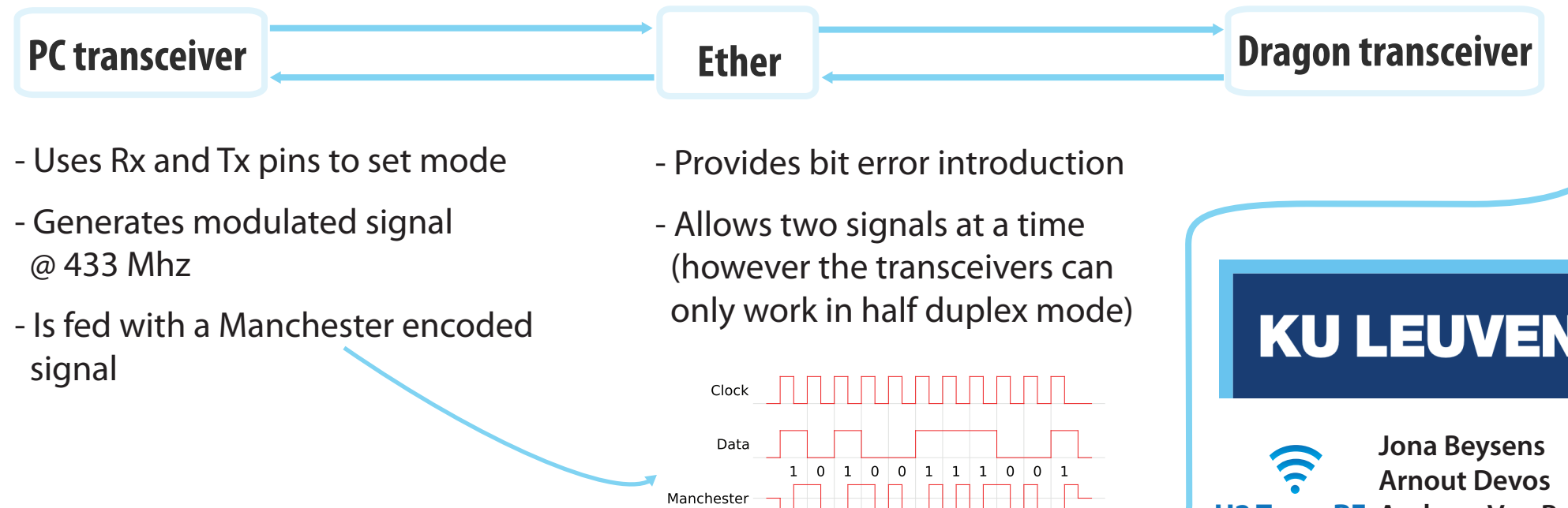


Simulation

- 32 bit word length
- One parity bit per byte
- Manchester encoding
- $(32 + 4) \cdot 2 = 72$ bits
- request angle command = 2
- = 00...010



Channel Model



KU LEUVEN

Jona Beysens
Arnout Devos
H2 Team RF
Andreas Van Barel