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	Revisions							
Rev	Description	Date	Approved					
X1	First draft	3/17/23	T.Q.					
A	Release	4/3/23	T.Q.					
A1	1) R108 x, R109 x, R110 x, R111 x: replaced with 180 ohm resistors 2) C21 x, C23 x: replaced with 10nF capacitors 3) L1, L2, L3, L4: replaced with 0ohm resistors 4) R102 x, R103 x, R104 x, R105 x, R106 x, R107 x: replaced with 0ohm resistors replaced with 0ohm resistors xeplaced with 0ohm resistors xeplaced with 20nF x830 wapacitors	6/7/23	T.Q.					

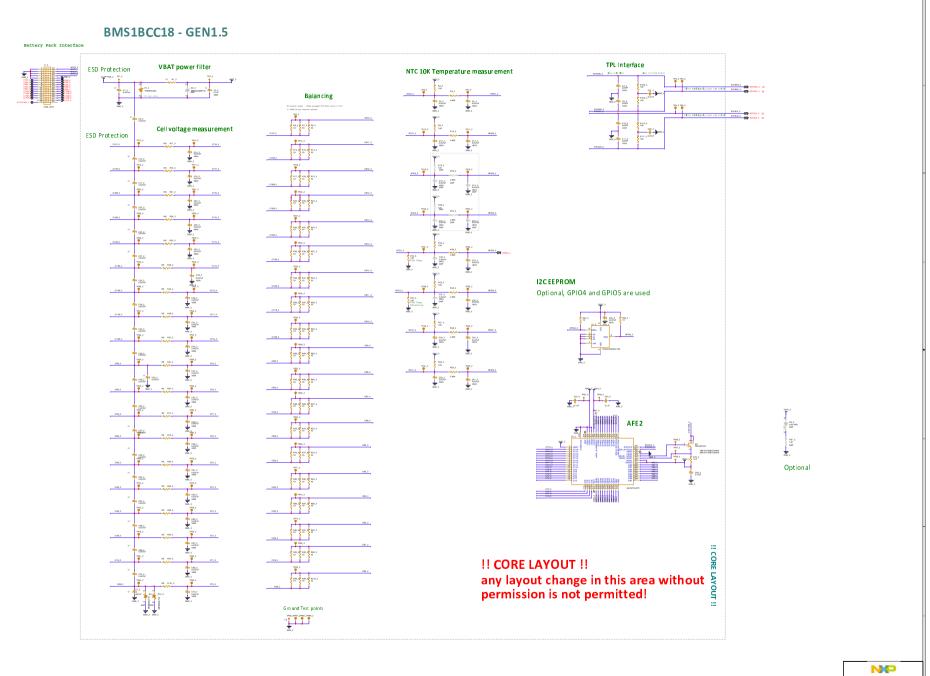
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REF DES	ASSY_OPT	PAGE NAME
C18_1,C19_1,C24_1,C25_1, C32_1,C37_1,D2_1,D5_1,D9_1, D10_1,R15_1,R20_1,R26_1, R27_1,R81_1	DNP	02 - 33774 FIRST
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C18_3,C19_3,C24_3,C25_3, C32_3,C37_3,D2_3,D5_3,D9_3, D10_3,R15_3,R20_3,R26_3, R27_3,R81_3	DNP	04 - 33774 THIRD
C82_1,C82_2,C88_1,C88_2,T1, T2,T3,T4	DNP	05 - TPL - BUS

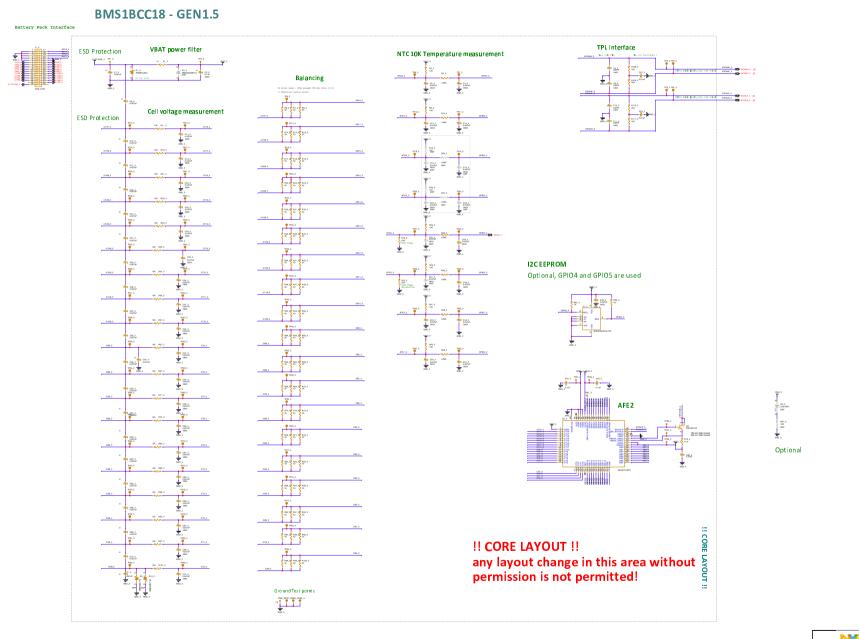
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	Date:	Wednesday, June 07, 2023	Sheet	1 of	5	

BMS1BCC18 - GEN1.5 Battery Pack Interface TPL Interface VBAT pow er filter ESD Protection NTC 10K Temperature measurement Balancing Cell voltage measurement ESD Protection 12C EEPROM Optional, GPIO4 and GPIO5 are used Optional !! CORE LAYOUT !! any layout change in this area without permission is not permitted! Ground Test points THE STAN STAN STAN











TPL Interface & busbar sensing

