

# Rapid-CoreX Fabrication Document

## Top Fabrication (Scale 1:1)

### LAYER STACKUP

Material	Layer	Thickness	Dielectric Material	Type
F.Paste				Paste Mask
F.Silkscreen				Legend
F.Mask			Solder Resist	Solder Mask
Copper	L1 (Sig. PWR)	0.035mm (1oz)		Signal
Prepreg		0.1mm	FR-4	Dielectric
Copper	L2 (GND)	0.035mm (1oz)		Internal Plane
Core		0.535mm	FR-4	Dielectric
Copper	L3 (Sig. 5V)	0.035mm (1oz)		Signal
Prepreg		0.1mm	FR-4	Dielectric
Copper	L4 (GND)	0.035mm (1oz)		Signal
Core		0.535mm	FR-4	Dielectric
Copper	L5 (Sig. 3.3V)	0.035mm (1oz)		Internal Plane
Prepreg		0.1mm	FR-4	Dielectric
Copper	L6 (GND)	0.035mm (1oz)		Signal
B.Mask			Solder Resist	Solder Mask
B.Silkscreen				Legend
B.Paste				Paste Mask

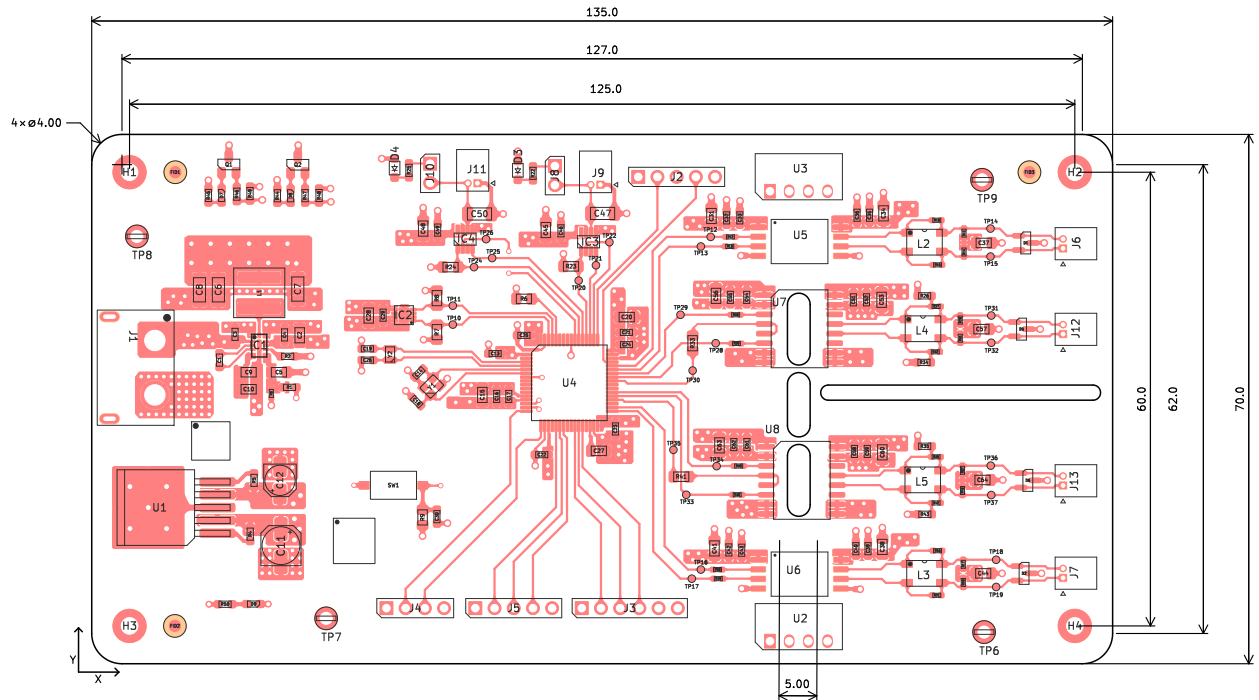
Total thickness: 1.60mm  
Note: external layer thicknesses are specified after plating.

### FABRICATION NOTES (UNLESS OTHERWISE SPECIFIED)

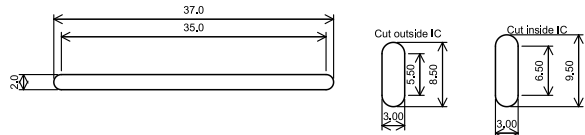
- 1) OUTLINE DEFINED IN SEPARATE GERBER FILE WITH "Edge\_Cuts.GBR" SUFFIX.  
DIMENSIONS OF CIRCUMSIZED RECTANGLE SHOWN ON THIS DWG FOR REF ONLY.
- 2) SEE SEPARATE DRILL FILES WITH ".DRL" SUFFIX FOR HOLE LOCATIONS.  
SELECTED HOLE LOCATIONS SHOWN ON THIS DWG FOR REF ONLY.
- 3) CONFIRM TRACE WIDTHS AND SPACINGS.

### BOARD CHARACTERISTICS

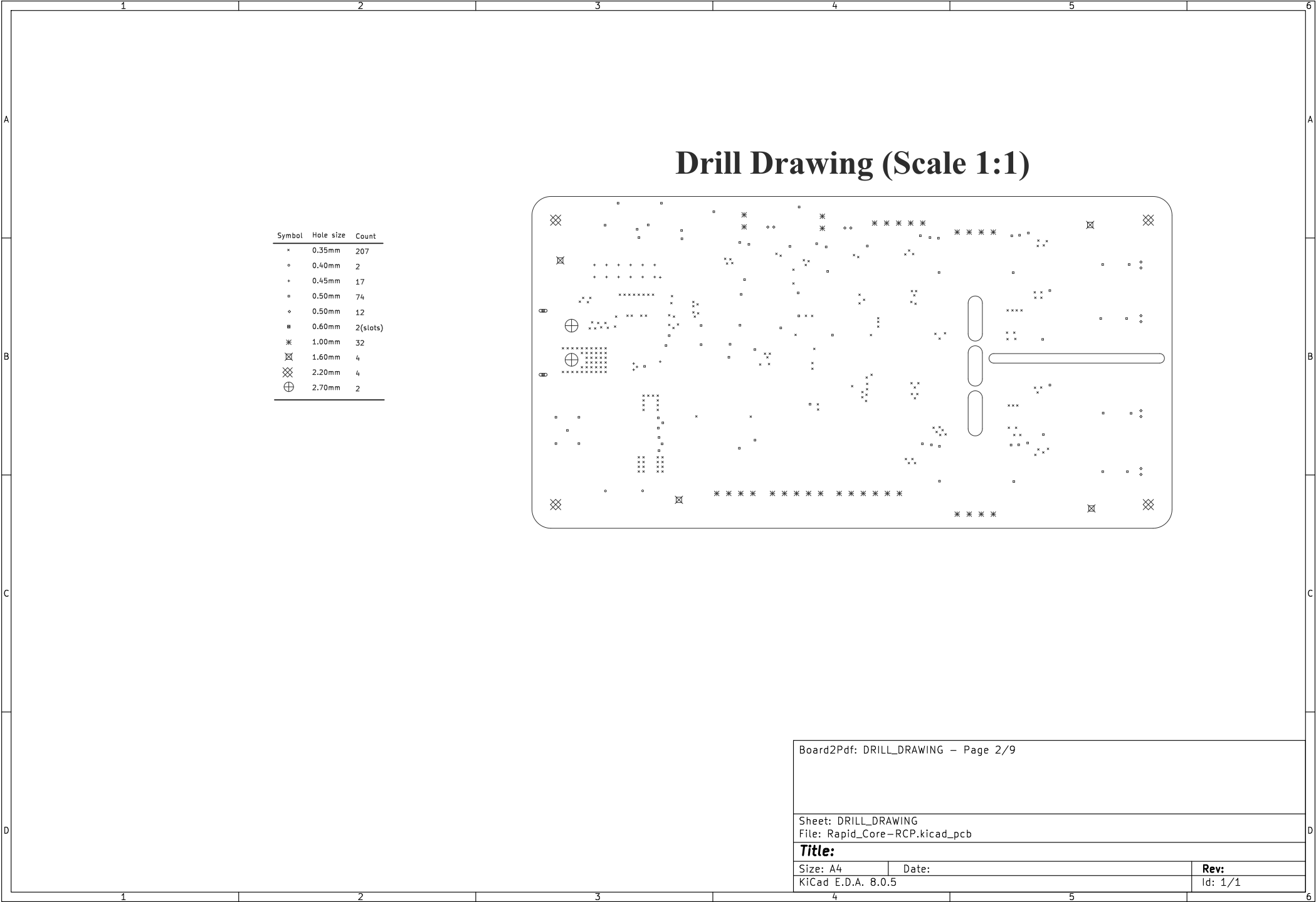
Copper Layer Count:	6	Board Thickness:	1.60 mm
Board overall dimensions:	135.00 mm x 70.00 mm		
Min track/spacing:	0.22 mm / 0.20 mm	Min hole diameter:	0.30 mm
Copper Finish:	HAL lead-free	Impedance Control:	No
Castellated pads:	No	Plated Board Edge:	No
Edge card connectors:	No		



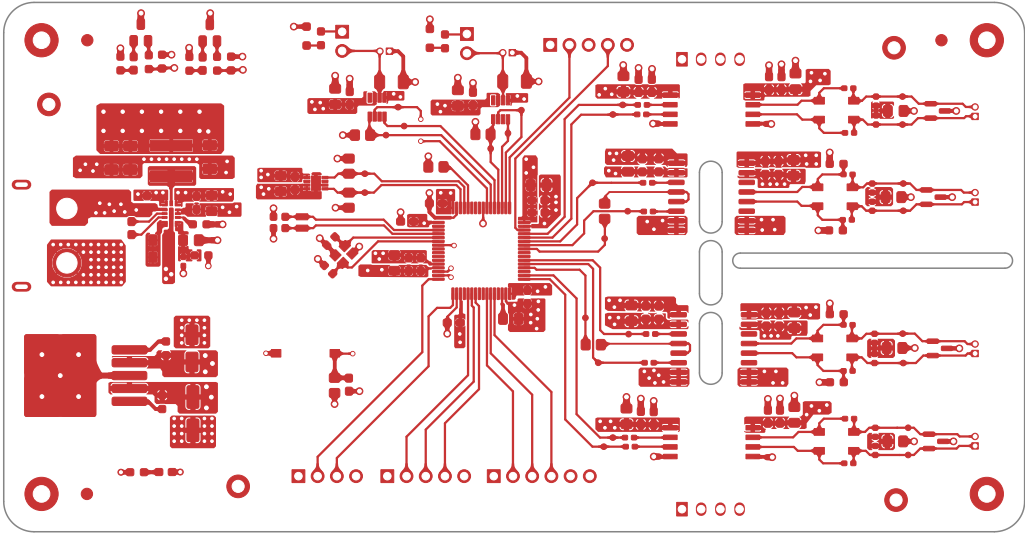
All dimensions are in millimeters unless otherwise specified.



Board2Pdf: DRILL_DRAWING - Page 2/9	
Sheet: Fabrication_Top File: Rapid_Core-RCP.kicad_pcb	
<b>Title:</b>	
Size: A4	Date:
KiCad E.D.A. 8.0.5	Rev: Id: 1/1

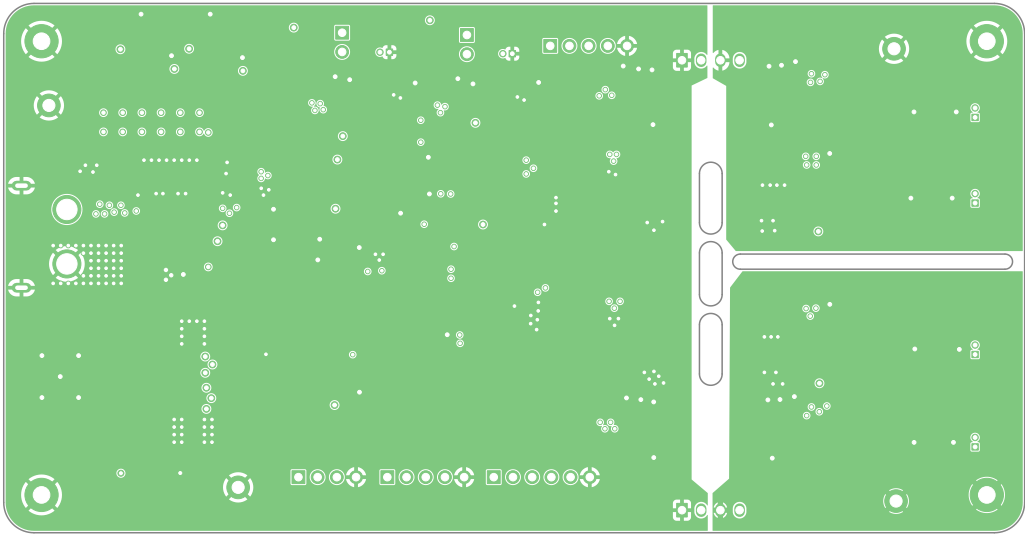


F.Cu ( Signal )



Board2Pdf: DRILL_DRAWING – Page 2/9		
Sheet: L1		
File: Rapid_Core–RCP.kicad_pcb		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.5		Id: 1/1

# Layer 2 ( GND )



Board2Pdf: DRILL\_DRAWING - Page 2/9

Sheet: L2  
File: Rapid\_Core-RCP.kicad\_pcb

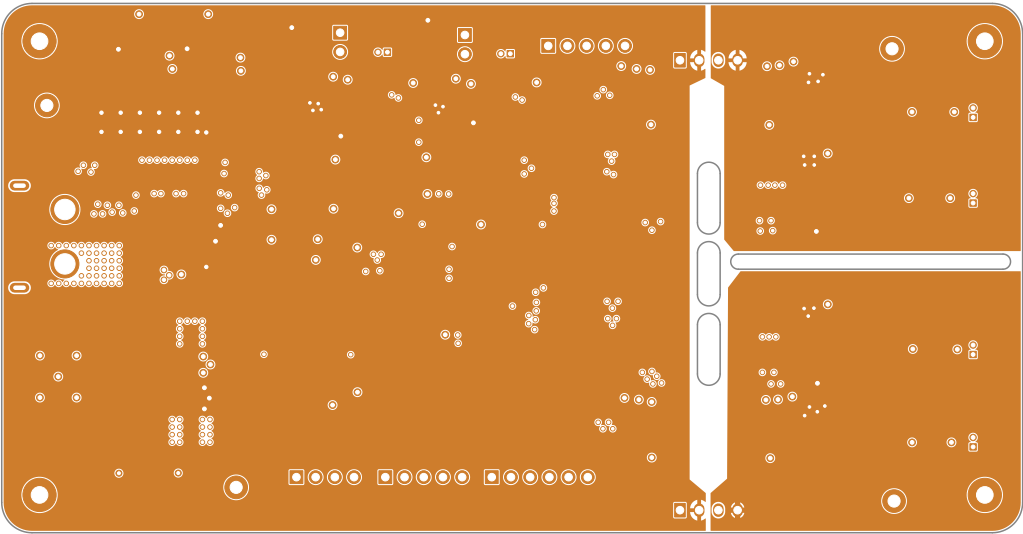
**Title:**

Size: A4  
KiCad E.D.A. 8.0.5

Date:

**Rev:**  
Id: 1/1

Layer 3 ( PWR +5v)



Board2Pdf: DRILL\_DRAWING - Page 2/9

Sheet: L3  
File: Rapid\_Core-RCP.kicad\_pcb

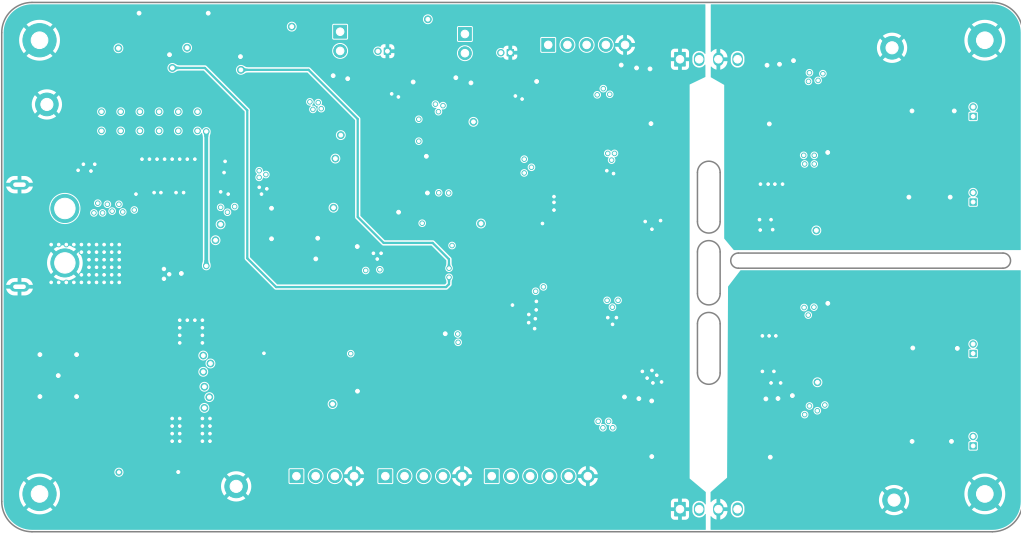
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KiCad E.D.A. 8.0.5

Date:

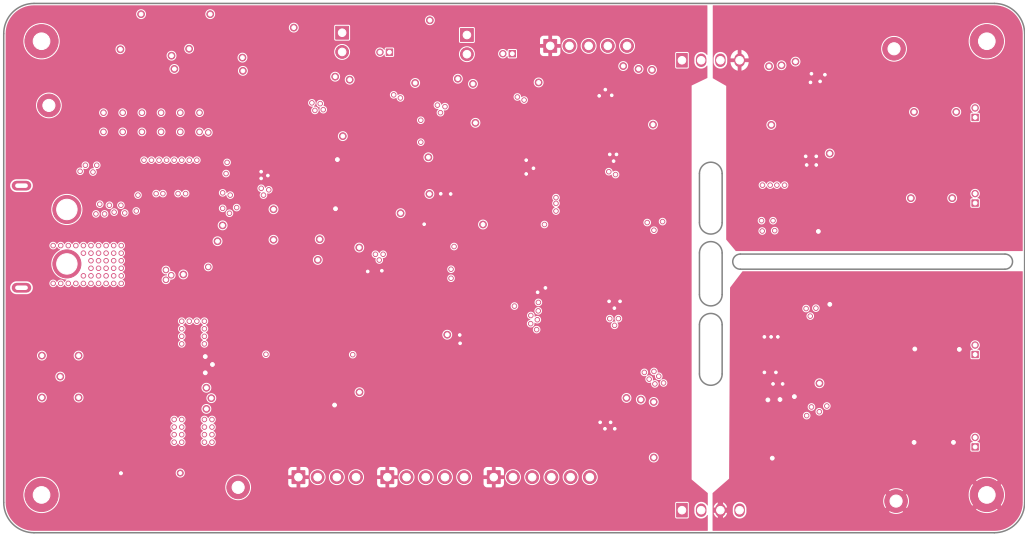
**Rev:**  
Id: 1/1

Layer 4 ( GND )



Board2Pdf: DRILL_DRAWING – Page 2/9		
Sheet: L4		
File: Rapid_Core–RCP.kicad_pcb		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.5		Id: 1/1

Layer 5 ( PWR +3.3V)

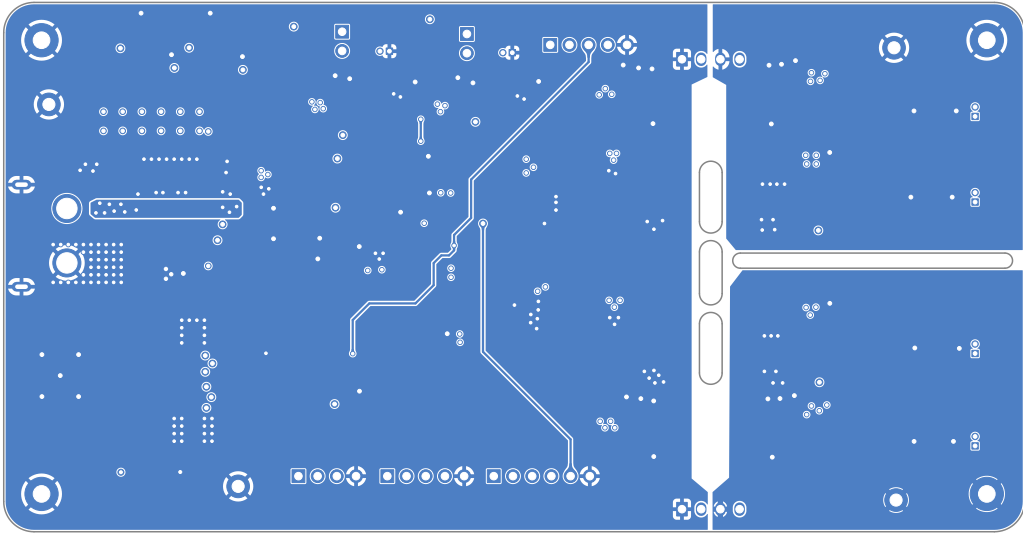


Board2Pdf: DRILL\_DRAWING - Page 2/9

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File: Rapid\_Core-RCP.kicad\_pcb

<b>Title:</b>			
Size: A4	Date:	Rev:	
KiCad E.D.A. 8.0.5		Id: 1/1	

Layer 6 ( GND )



Board2Pdf: DRILL_DRAWING – Page 2/9		
Sheet: L6		
File: Rapid_Core–RCP.kicad_pcb		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.5		Id: 1/1