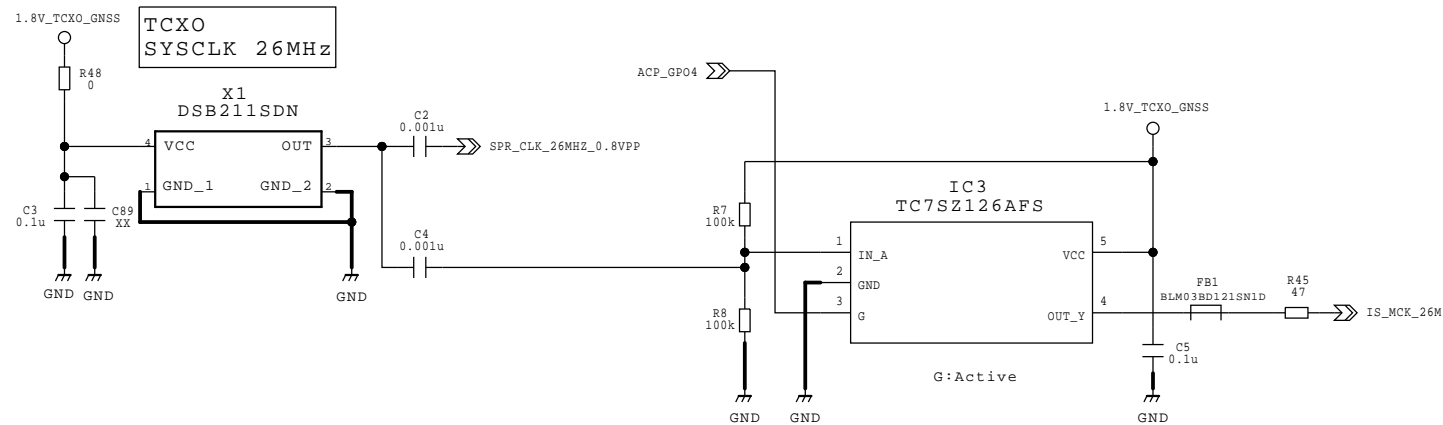
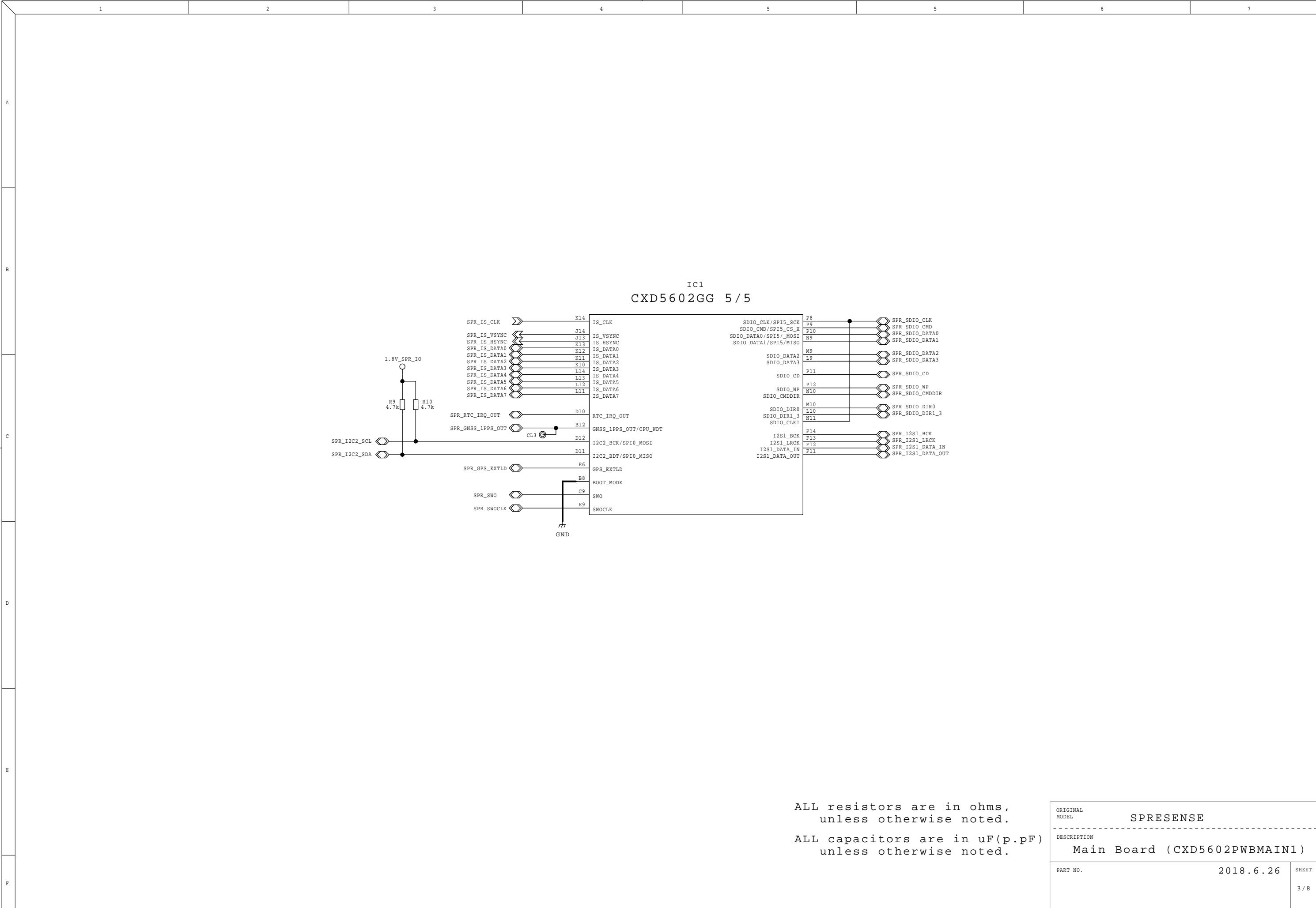


Note) "XX" stands for NM



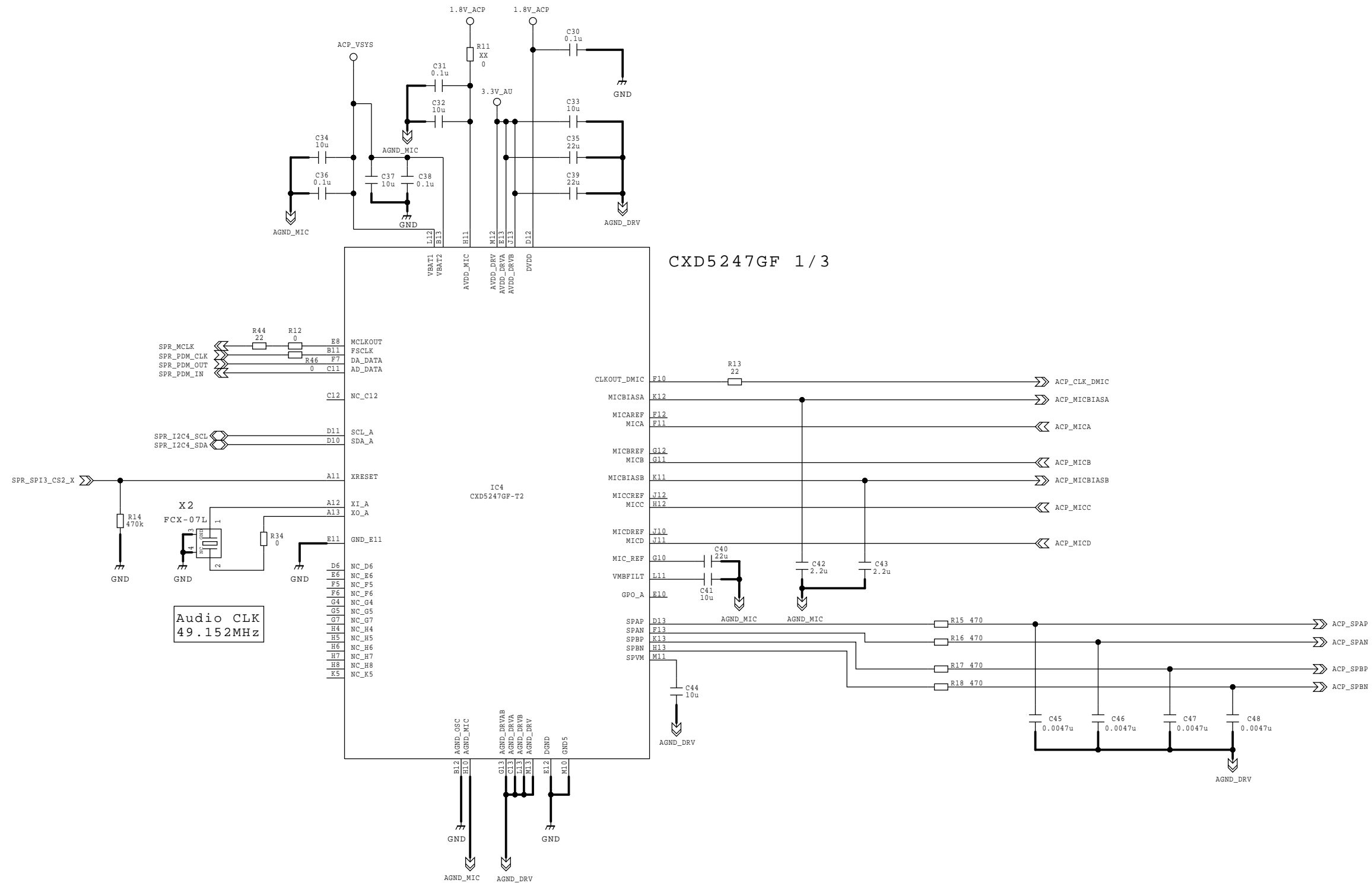
<div>△ 3 X</div>				<div>△ 2 X</div>				<div>△ 1 X</div>				GENERAL TOLERANCE				DATE OF ISSUE			
																DOCUMENT TYPE			
																CIRCUIT DIAGRAM			
																TOLERANCING			
HISTORY X COUNT				SUFFIX /REPL.				RELEASE NO. DATE				REVISION				SIGN.			
LEGAL OWNER												MATERIAL(COLOR)				FINISH(COLOR)			
																2018.7.10			
RESPONSIBLE DEPARTMENT						SIZE A3		UNIT mm		ORIGINAL MODEL SPRESENSE				DESCRIPTION (E) Main Board (CXD5602PWBMAIN1)					
PLANNED BY				CHECKED BY		APPROVED BY		SCALE :		TENTATIVE MODEL				DESCRIPTION (J)					
DRAWN BY								PRJ.METHOD 		TENTATIVE PART NO.				PART NO.		SHEET/PAGES 1/8			



ALL resistors are in ohms,
unless otherwise noted.

ALL capacitors are in uF(p.pF)
unless otherwise noted.

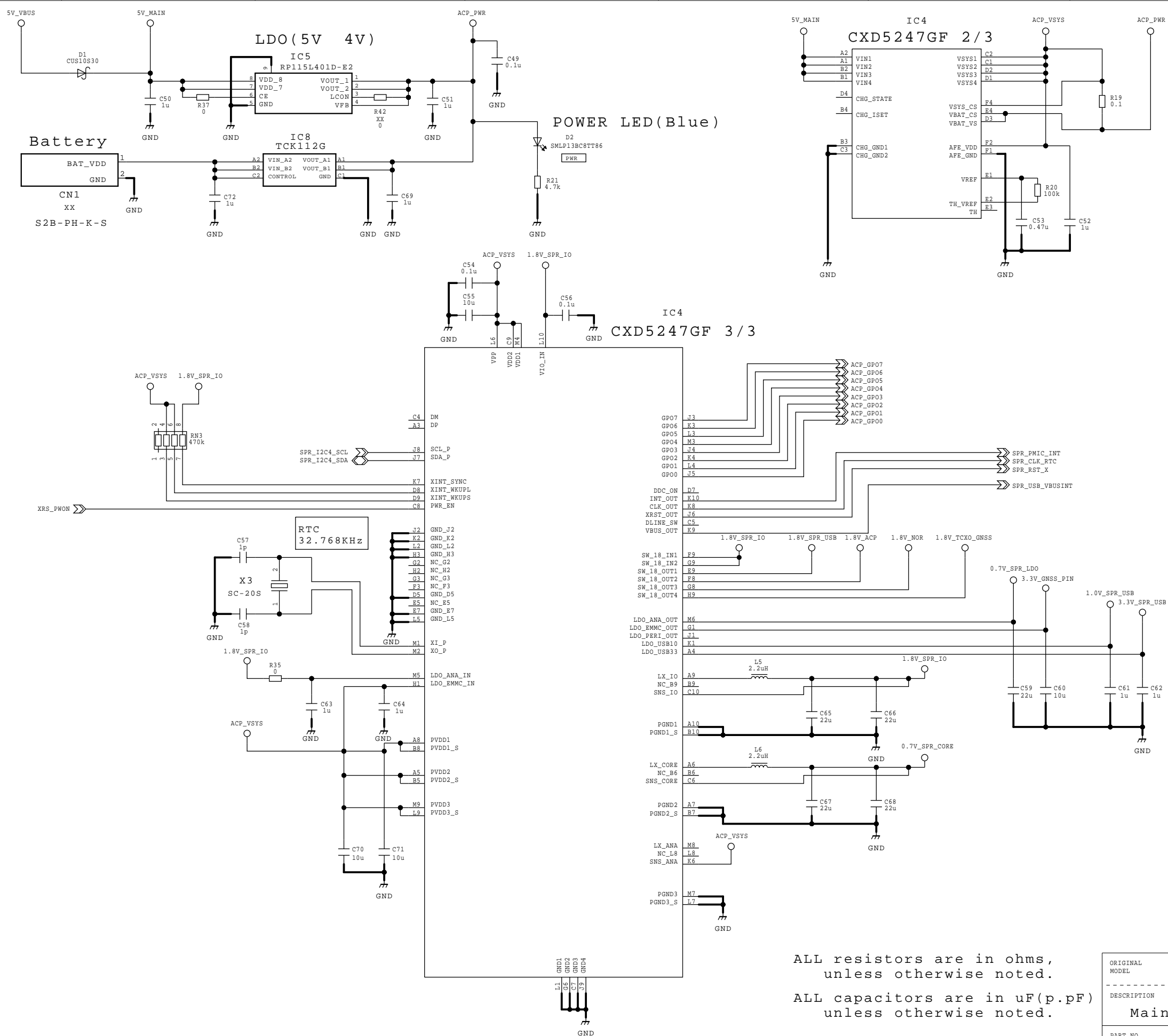
ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	Main Board (CXD5602PWBMAIN1)	
PART NO.	2018.6.26	SHEET 3 / 8



ALL resistors are in ohms,
unless otherwise noted.

ALL capacitors are in uF(p.pF)
unless otherwise noted.

ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	Main Board (CXD5602PWBMAIN1)	
PART NO.	2018.7.10	SHEET 4 / 8

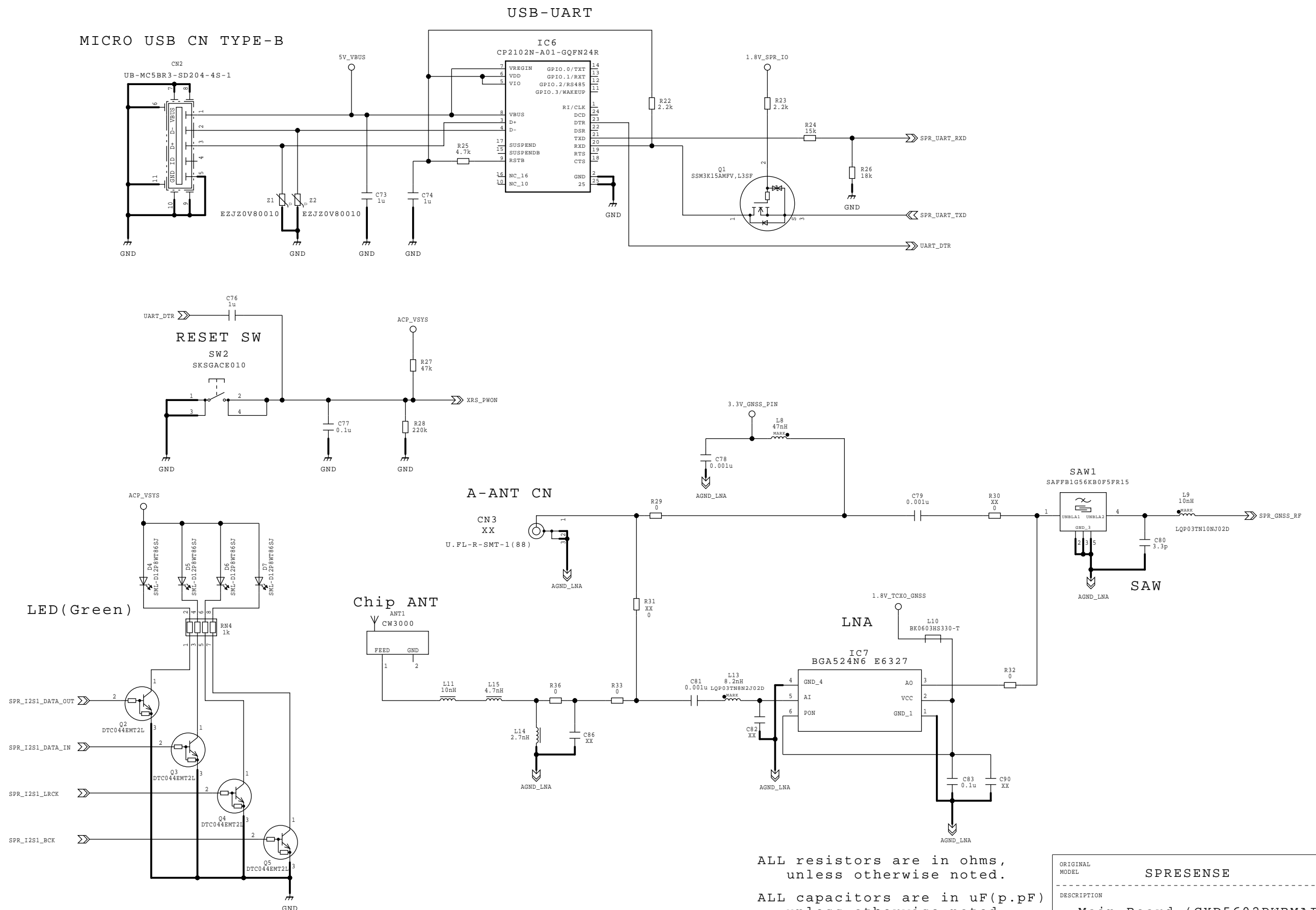


ALL resistors are in ohms,
unless otherwise noted.

ALL capacitors are in uF(p.pF)
unless otherwise noted.

ORIGINAL MODEL		SPRESENSE	

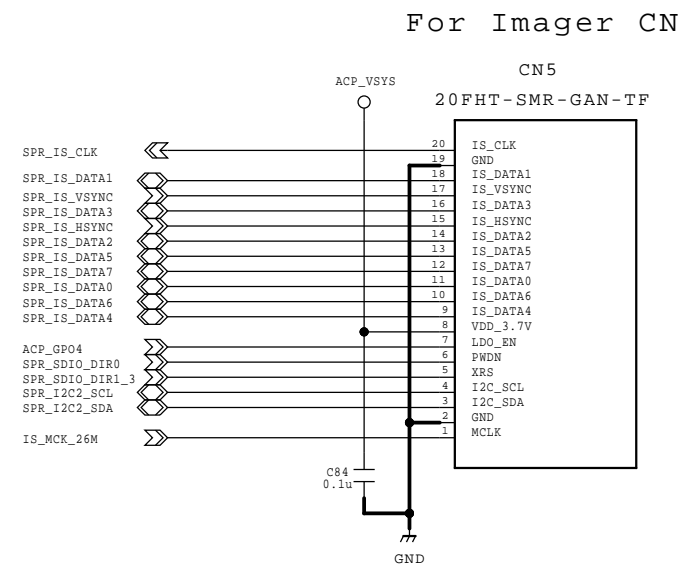
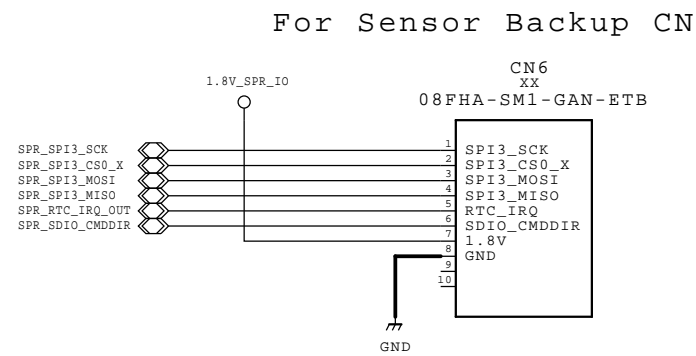
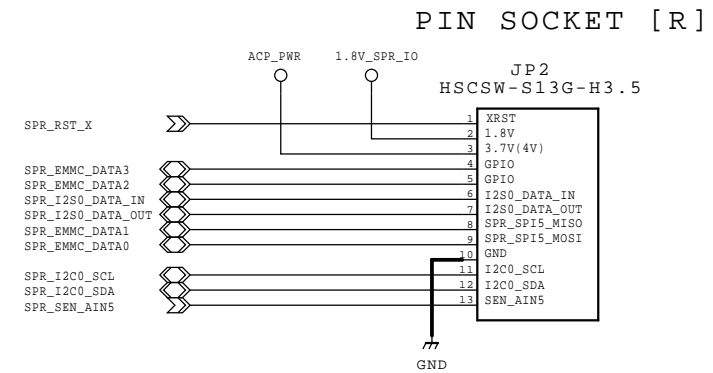
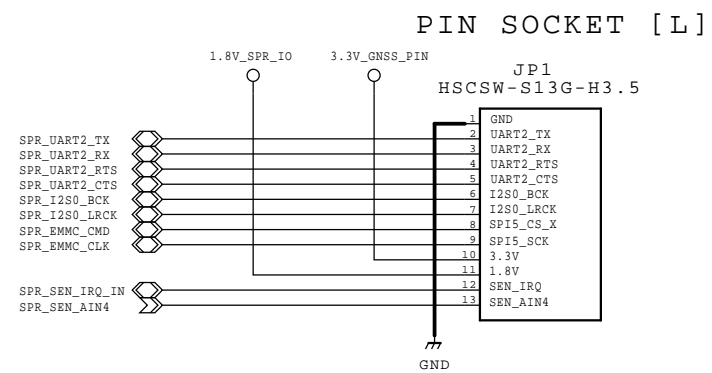
DESCRIPTION			
Main Board (CXD5602PWBMAIN1)			
PART NO.	2018.7.10		SHEET
			5 / 8



ALL resistors are in ohms,
unless otherwise noted.

ALL capacitors are in uF(p.pF)
unless otherwise noted.

ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	Main Board (CXD5602PWBMAIN1)	
PART NO.	2018.7.10	SHEET 6 / 8



ALL resistors are in ohms,
unless otherwise noted.

ALL capacitors are in uF(p.pF)
unless otherwise noted.

ORIGINAL MODEL	SPRESENSE	
DESCRIPTION		
Main Board (CXD5602PWBMMAIN1)		
PART NO.	2018.7.9	SHEET 8 / 8