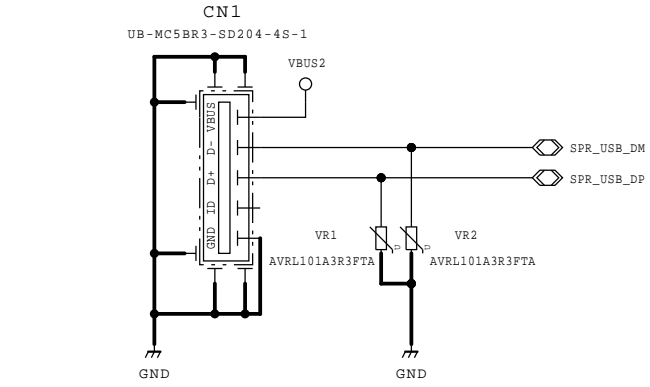
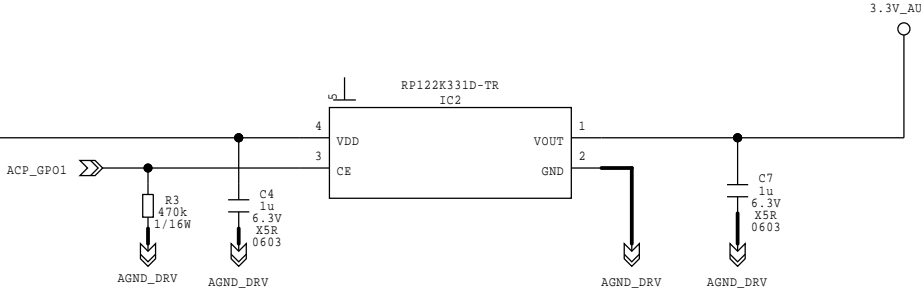
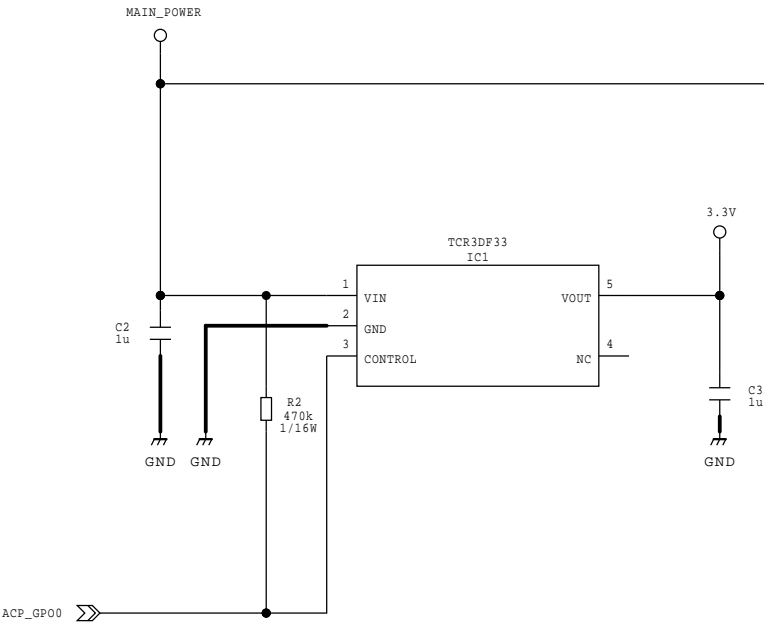
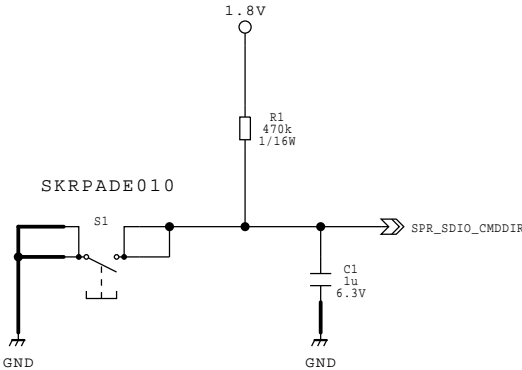
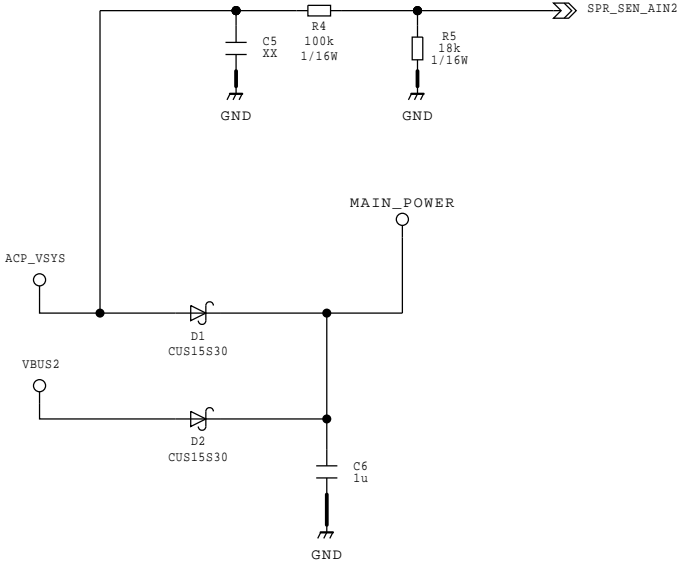
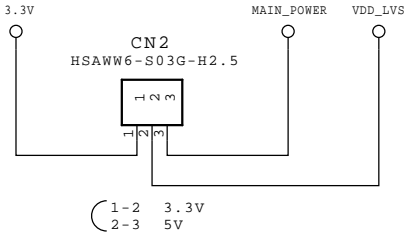


MICRO USB CN TYPE-B



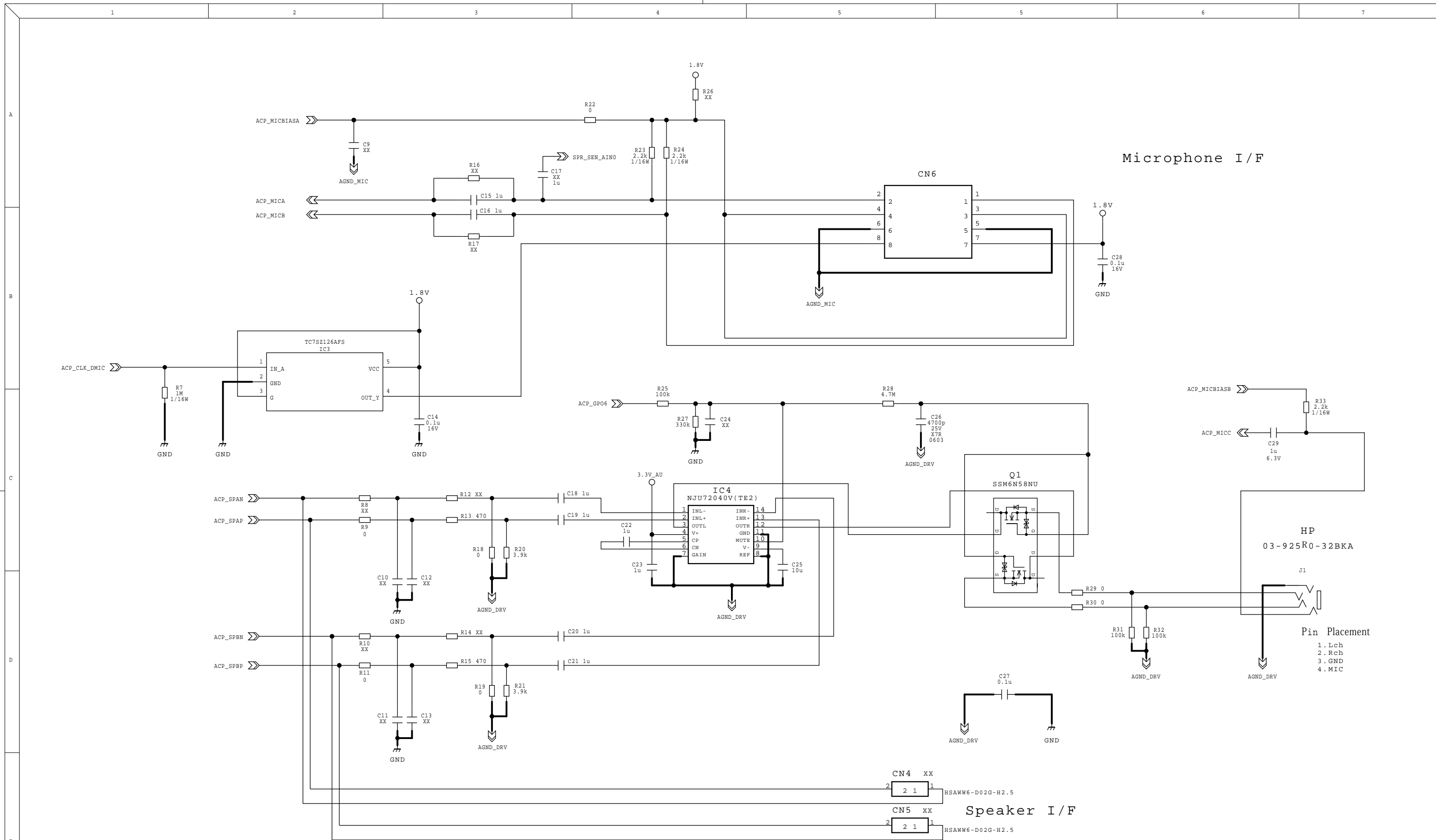
I/O Power SW JP



Note) "XX" stands for NM

<div><div></div><div>X</div></div>						GENERAL TOLERANCE			DATE OF ISSUE		
<div><div></div><div>X</div></div>								DOCUMENT TYPE			
<div><div></div><div>X</div></div>								CIRCUIT DIAGRAM			
								TOLERANCING			
								ISO 8015			
HISTORY X COUNT		SUFFIX /REPL.		RELEASE NO.		REVISION		SIGN.		MATERIAL(COLOR)	FINISH(COLOR)
		DATE									
LEGAL OWNER								2019.11.13			
RESPONSIBLE DEPARTMENT						SIZE	UNIT	ORIGINAL MODEL	DESCRIPTION (E)		
						A3	mm	SPRESENSE	LTE Board (CXD5602PWBLM1)		
PLANNED BY		CHECKED BY		APPROVED BY		SCALE		TENTATIVE MODEL		DESCRIPTION (J)	
						: 					
DRAWN BY						PRJ.METHOD 		TENTATIVE PART NO.		PART NO.	
										SHEET/PAGES	
										1 / 7	

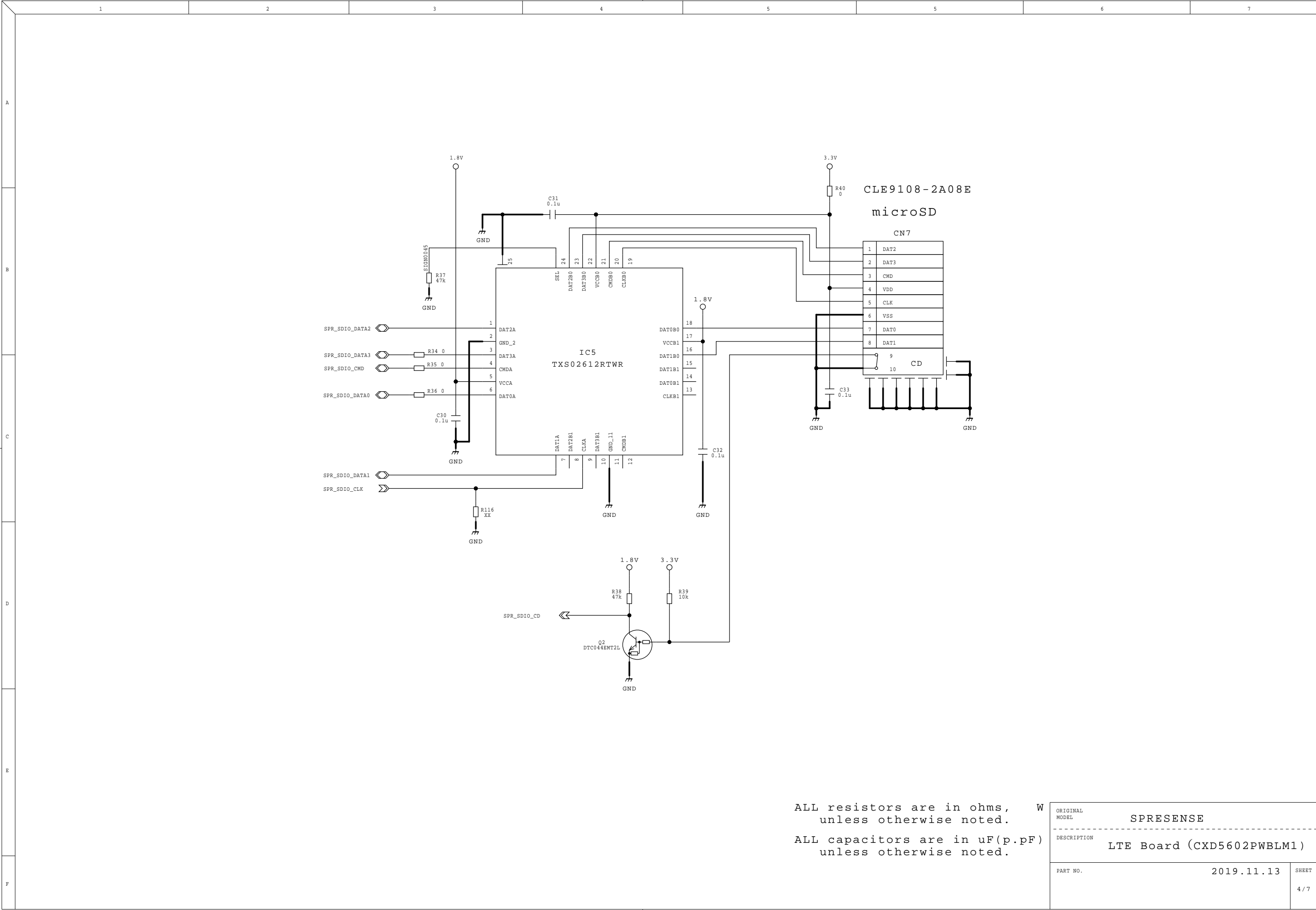


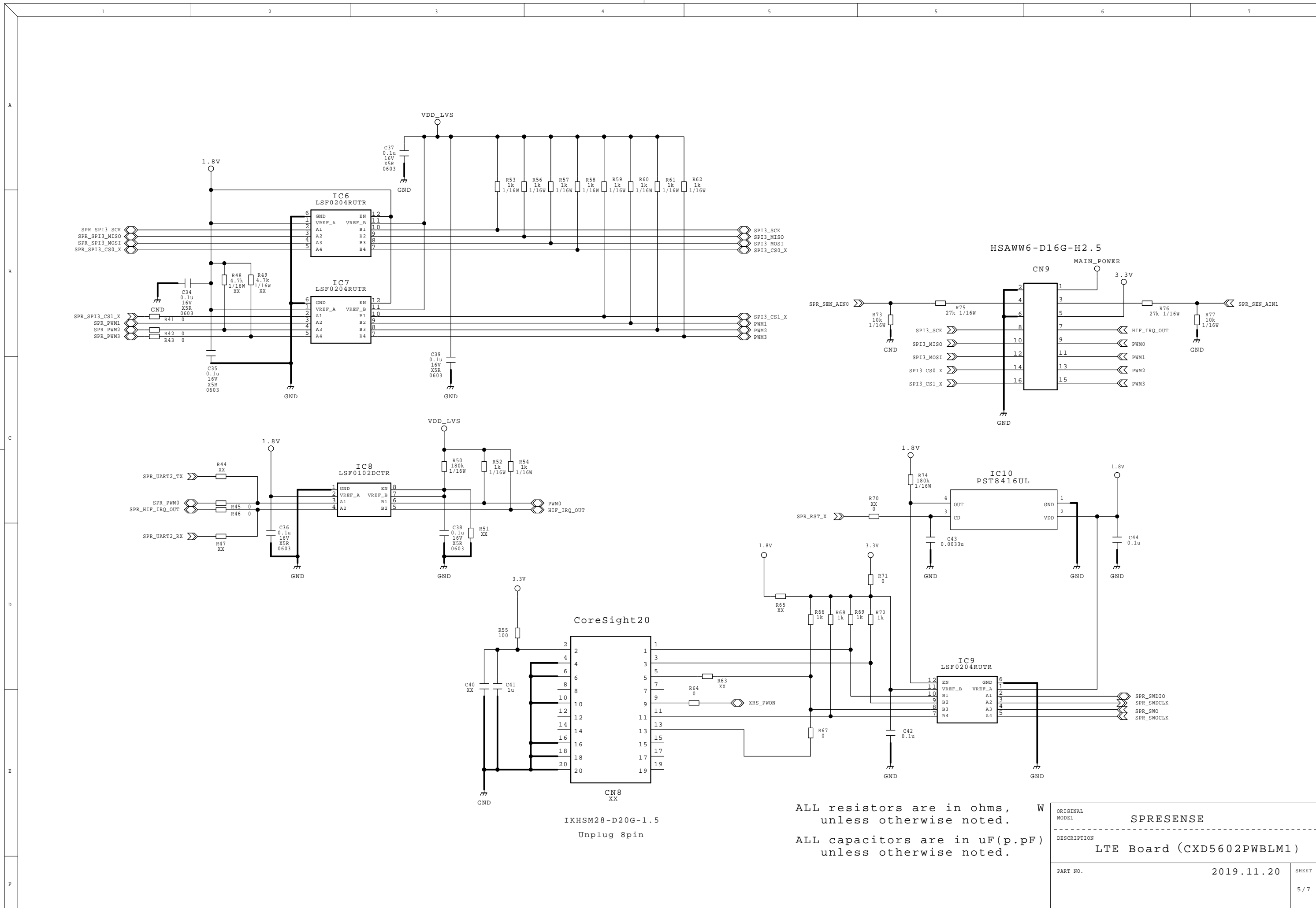


ALL resistors are in ohms, unless otherwise noted.

ALL capacitors are in uF(p.pF) unless otherwise noted.

ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	LTE Board (CXD5602PWBLM1)	
PART NO.	2019.11.20	SHEET 3 / 7

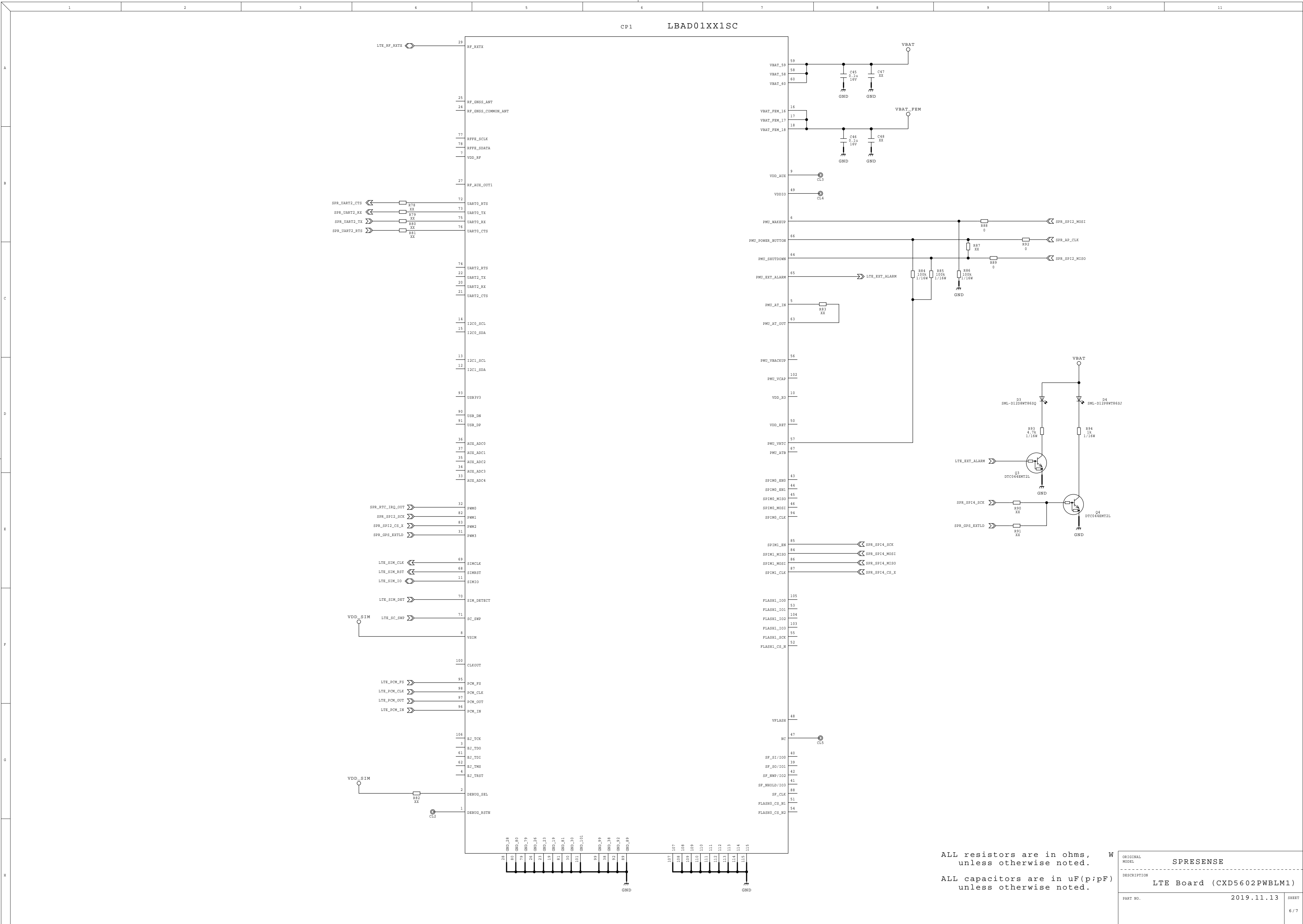


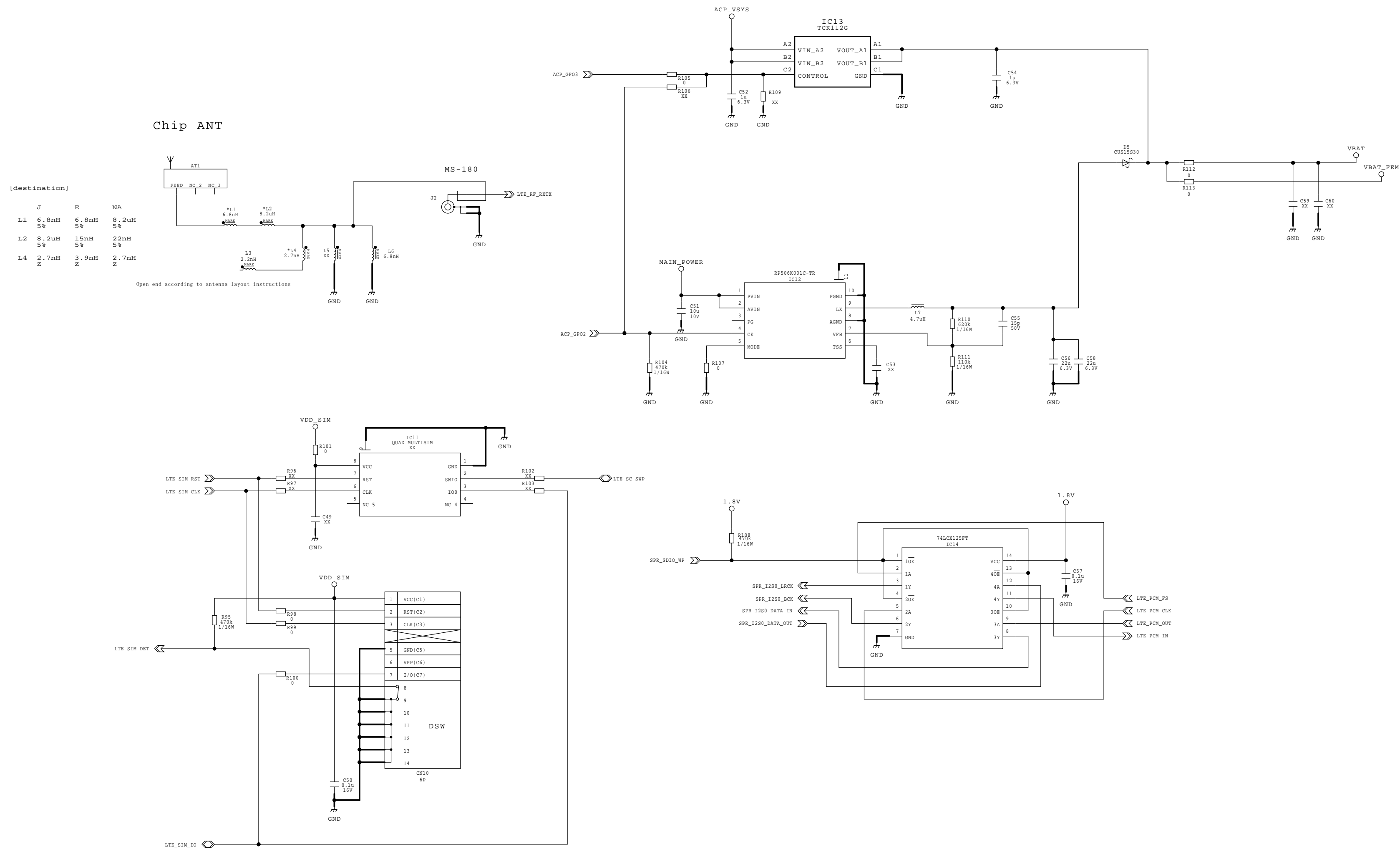


ALL resistors are in ohms,  
unless otherwise noted.

ALL capacitors are in uF(p.pF)  
unless otherwise noted.

ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	LTE Board (CXD5602PWBLM1)	
PART NO.	2019.11.20	SHEET
		5 / 7





ALL resistors are in ohms,  
unless otherwise noted.

ALL capacitors are in uF(p;pF)  
unless otherwise noted.

ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	LTE Board (CXD5602PWBLM1)	
PART NO.	2019.11.20	SHEET 7 / 7