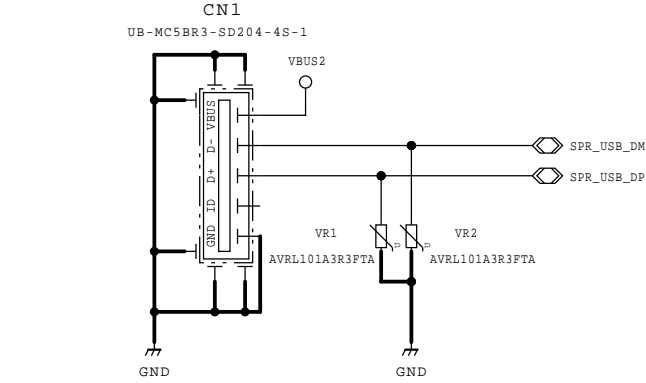
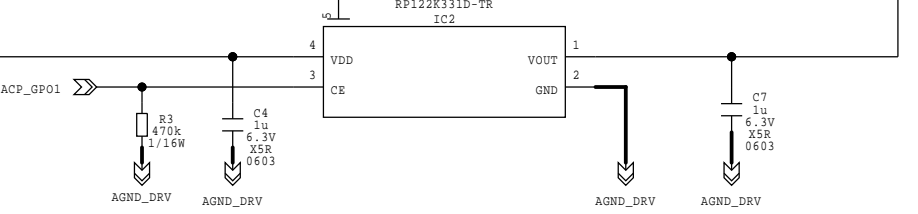
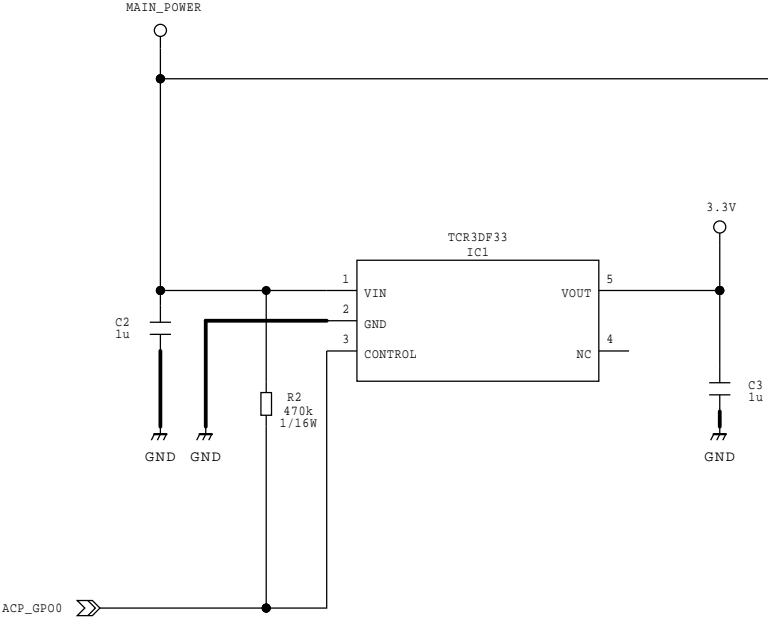
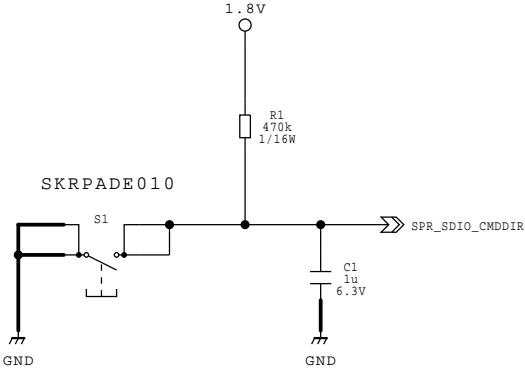
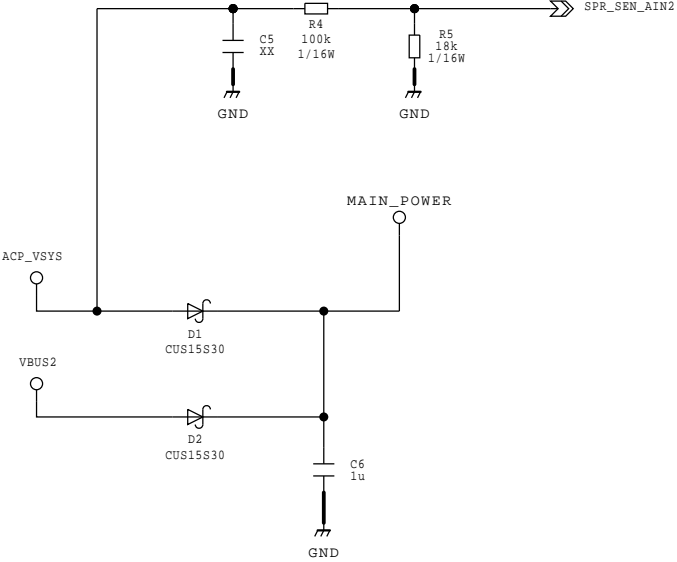
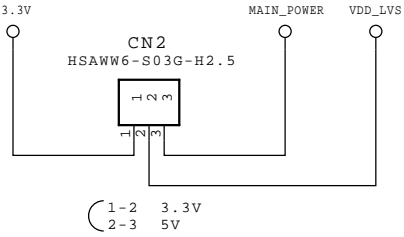


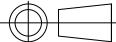
MICRO USB CN TYPE-B

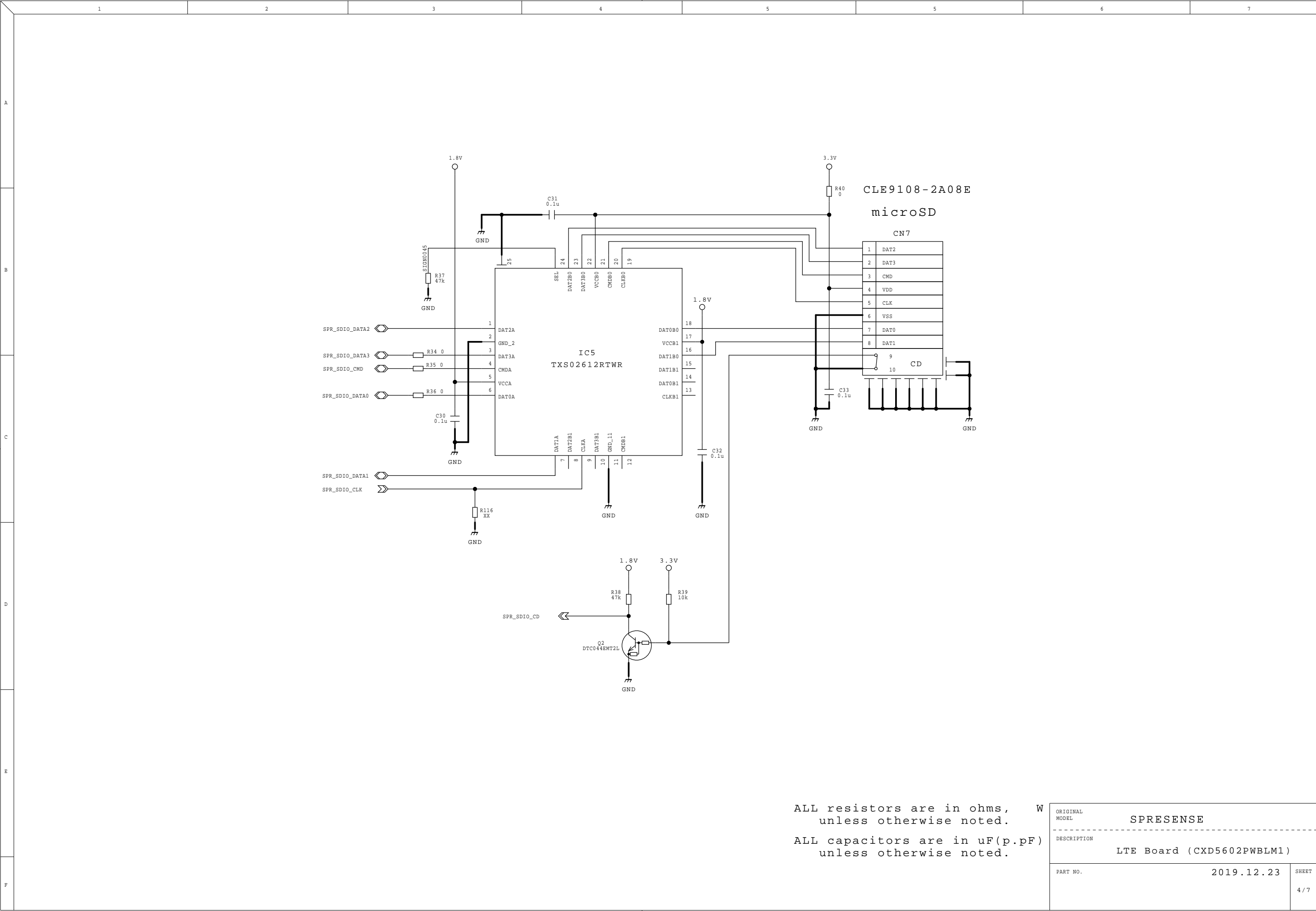


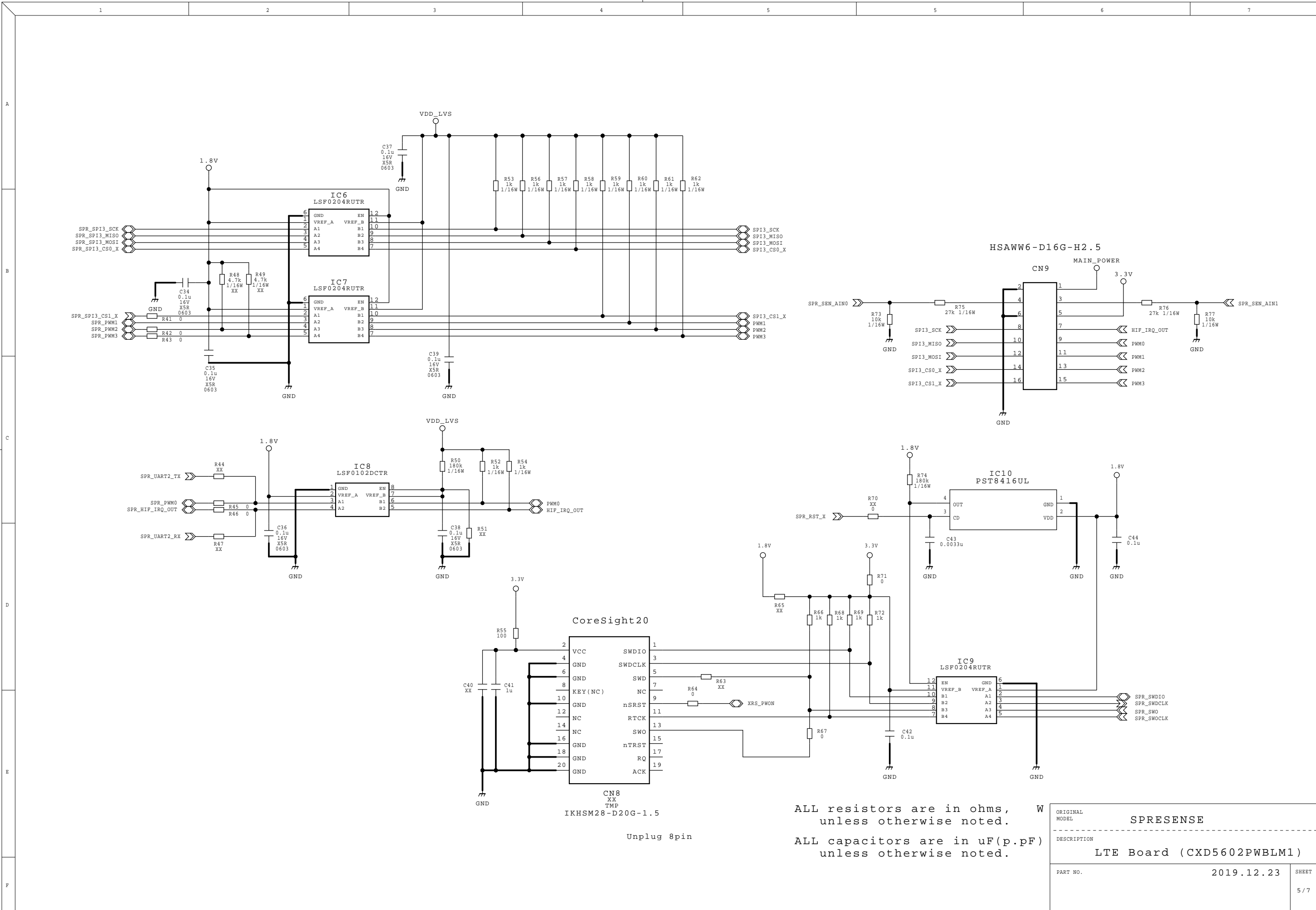
I/O Power SW JP

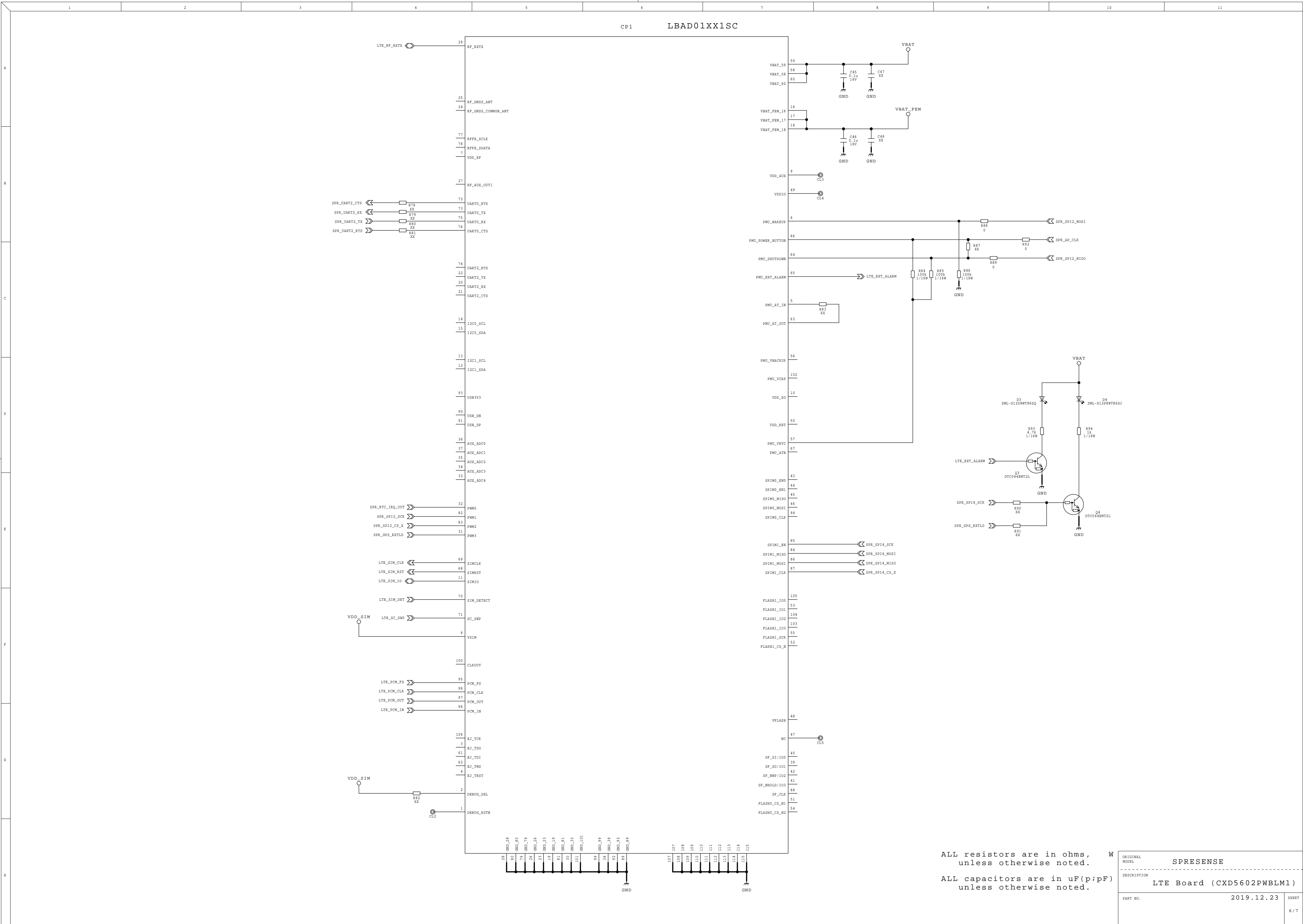


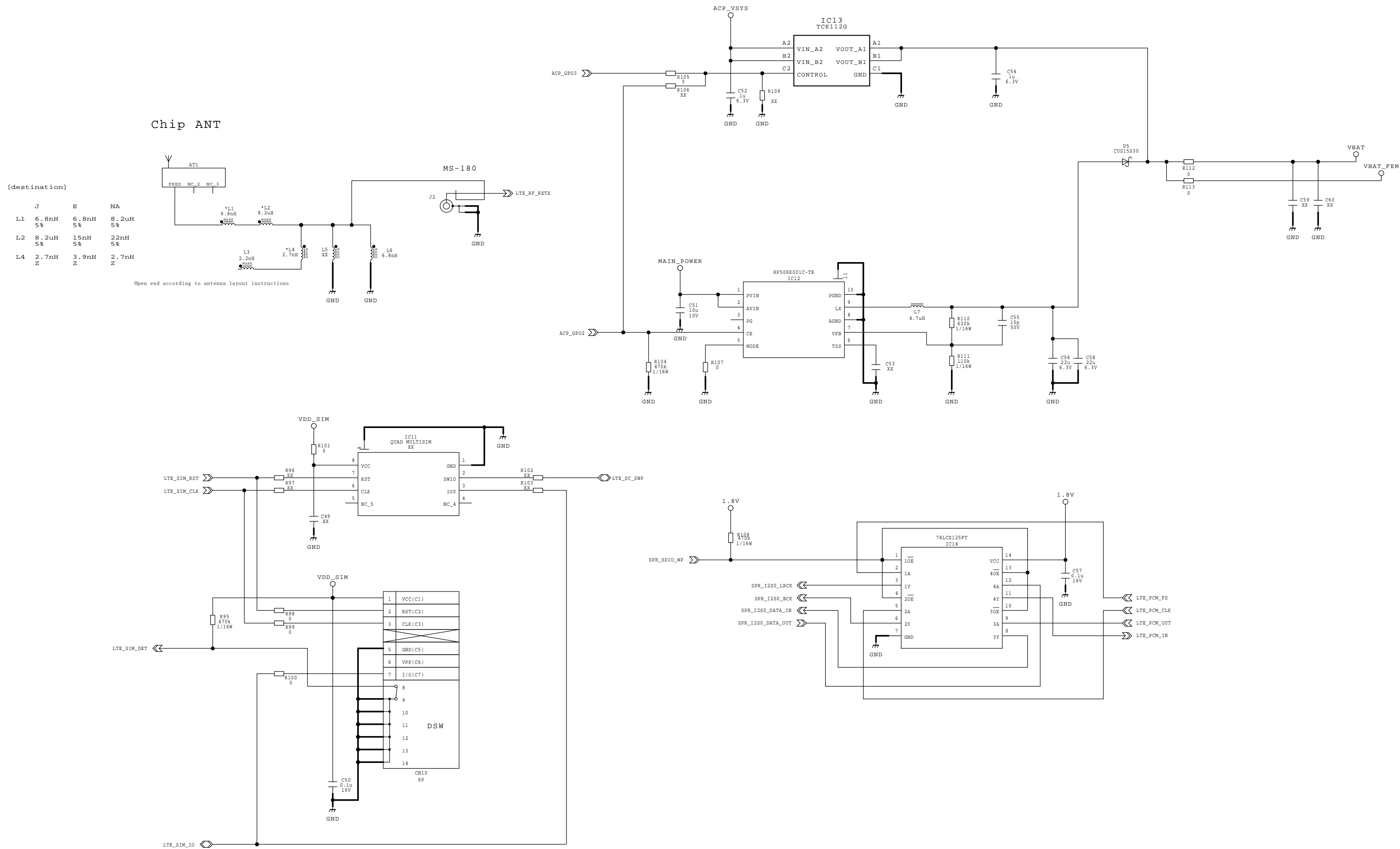
Note) "XX" stands for NM

<div>3</div> X						GENERAL TOLERANCE			DATE OF ISSUE	
<div>2</div> X									DOCUMENT TYPE	
									CIRCUIT DIAGRAM	
<div>1</div> X									TOLERANCING	
								ISO 8015		
HISTORY X COUNT		SUFFIX /REPL.		RELEASE NO. DATE		REVISION		SIGN.		
LEGAL OWNER						MATERIAL (COLOR)		FINISH (COLOR) 2019.12.23		
RESPONSIBLE DEPARTMENT				SIZE A3	UNIT mm	ORIGINAL MODEL SPRESENSE		DESCRIPTION (E) LTE Board (CXD5602PWBLM1)		
PLANNED BY		CHECKED BY		APPROVED BY		SCALE :		TENTATIVE MODEL DESCRIPTION (J)		
DRAWN BY						PRJ.METHOD 		TENTATIVE PART NO. PART NO.		
								SHEET/PAGES 1 / 7		









ALL resistors are in ohms,
unless otherwise noted.

ALL capacitors are in uF(p;pF)
unless otherwise noted.

ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	LTE Board (CXD5602PWBLM1)	
PART NO.	2019.12.23	SHEET
		7 / 7