



DC-DC

ref

12VfD → DC_DC_Vin+

GNDAD → DC_DC_GND

10VforceD → J102 10Vf

10VsenseD → J103 10Vs

7VrefD → J104 7Vref

D101 SMAJ11A

LoD → J105 Lo

File: dcdc.kicad_sch

File: ref.kicad_sch



All capacitors X7R MLCC unless otherwise noted
All resistors 0.1% MF unless otherwise noted

RPG

Sheet: /
File: ADRmu.kicad_sch

Title: Parent schematic

Size: A4 Date: 2022-03-04

KiCad E.D.A. kicad (6.0.0)

Rev: 0.4

Id: 1/3

