



About the authors

Wim Rouwet is a distinguished member of technical staff at NXP Semiconductors. He has an MSc in electrical engineering from the Eindhoven University of Technology in the Netherlands. He had spent more than 15 years in Motorola, Freescale, and NXP in networking and network processing, wireless algorithm development, and system and modem architecture roles. His focus is on 3GPP LTE and 5G as well as 802.11 processing stacks and their implementation. In his job, he has been responsible for 4G and 5G stack development, small cells, and CRAN implementations associated with many wireless infrastructure projects. He has led key next-generation R&D projects including multistandard modem architecture, virtualization, 5G macro and small cell, and client-side products.

Contributing Authors

David Spencer is a product line manager for Timing Solutions at Skyworks Inc., formerly Silicon Labs I&A division. He has a degree in physics and an over 30-year career in technology spanning technical writing, hardware and software design, applications engineering, and product marketing. For the past 15 years, David has focused on synchronization solutions and is considered an expert in both physical layer sync technologies and IEEE 1588 packet-based timing, having presented papers at multiple international conferences such as WSTS and ITSF and represented various companies in the ITU-T standards body. In his current role, David is responsible for driving the definition and implementation of hardware and software enabling Synchronous Ethernet and IEEE 1588 sync in a wide range of systems including 5G RAN networks.

Vishwapathi Rao Tadinada works as a director of test at NXP Semiconductors where he has to ensure the quality of networking applications and customer solution product lines. He has over 20 years of extensive experience leading product testing in 4G, 5G, Security, Networking, Cloud, SDN, NFV, and Embedded Industry. He has several presentations at international conferences and papers on IoT, security, and virtualization. He has BSc from Kanpur University, India.