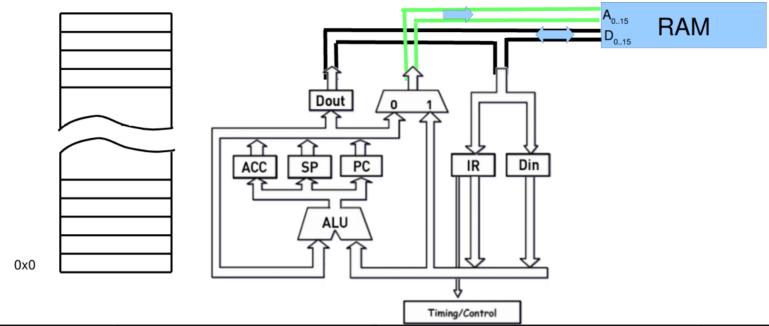
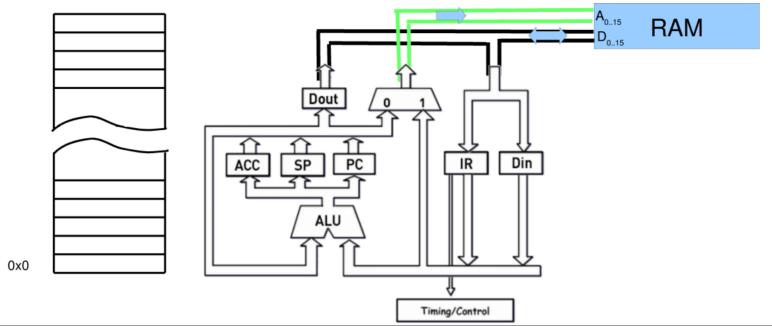


	ln	put	S										Οι	ıtpı	ıts								Description
Instruction	Opcode	Reset	step	ACC2	ACC ₁₅	step	Adress	ACC _{oe}	ACC _{ie}	SP _{oe}	Sp _{ie}	PC_{oe}	PC _{ie}	IR_{oe}	IR_{ie}	DIN	DIN. e	DOUT	DOUT _{ie}	ALU Functi	MEM _{rq}	RnW	



	ln	put	S										Οι	ıtpı	ıts								Description
Instruction	Opcode	Reset	step	ACC	ACC ₁₅	step	Adress	ACC _{oe}	ACC _{ie}	SPoe	Sp _{ie}	PC。	PC _{ie}	IR _{oe}	IR_{ie}	DIN	DIN _{ie}	DOUT	DOUT _{ie}	ALU Funct	MEM _{rq}	RnW	



	ln	put	S										Οι	ıtpı	uts								Description
Instruction	Opcode	Reset	step	ACC _z	ACC ₁₅	step	Adress	ACC。	ACC _{ie}	SPoe	Sp _{ie}	PC。	PC _{ie}	IR _{oe}	$ m IR_{ie}$	DIN。	DIN _{ie}	DOUT	DOUT _{ie}	ALU Functi	MEM_{rq}	RnW	
		1	0	Χ	Χ	1	0	0	0	0	0	1	1	0	1	0	0	0	0	A+1	1	1	IR = [PC]; PC = PC + 1

