


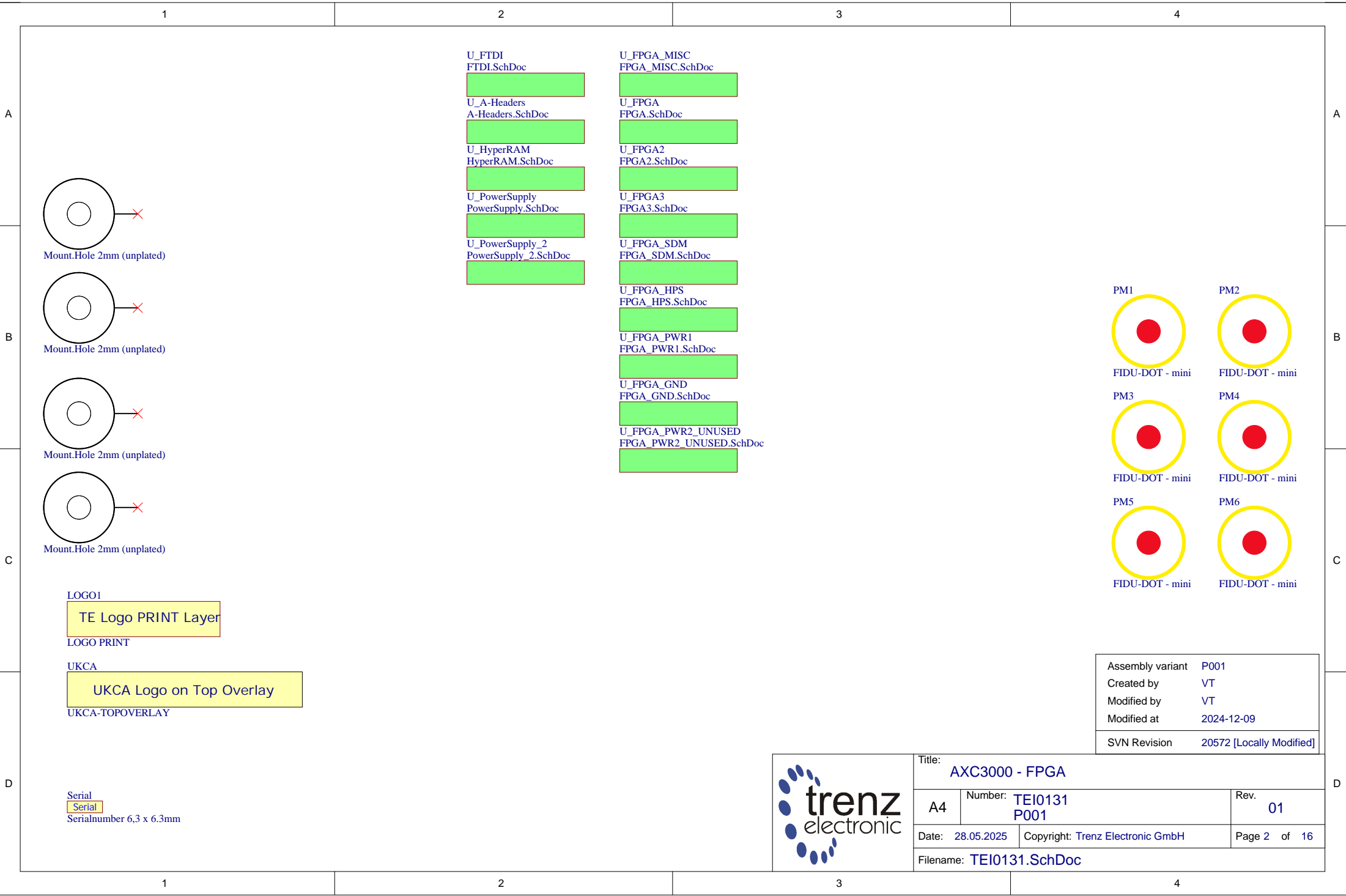


	1	2	3	4													
A	<table><tr><td>REV</td><td>Description</td><td></td></tr><tr><td>-01</td><td>Initial revision</td><td>VT</td></tr><tr><td></td><td></td><td></td></tr></table>				REV	Description		-01	Initial revision	VT							
REV	Description																
-01	Initial revision	VT															
B																	
C																	
D	<table><tr><td rowspan="4"></td><td colspan="3">Title: Revision Changes</td></tr><tr><td>A4</td><td>Number: TEI0131 P001</td><td>Rev. 01</td></tr><tr><td>Date: 2025-05-21</td><td>Copyright: Trenz Electronic GmbH</td><td>Page1 of 16</td></tr><tr><td colspan="3">Filename: Revision_Changes.SchDoc</td></tr></table>					Title: Revision Changes			A4	Number: TEI0131 P001	Rev. 01	Date: 2025-05-21	Copyright: Trenz Electronic GmbH	Page1 of 16	Filename: Revision_Changes.SchDoc		
	Title: Revision Changes																
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U_FTDI
FTDI.SchDoc

U_A-Headers
A-Headers.SchDoc

U_HyperRAM
HyperRAM.SchDoc

U_PowerSupply
PowerSupply.SchDoc

U_PowerSupply_2
PowerSupply_2.SchDoc

U_FPGA_MISC
FPGA_MISC.SchDoc

U_FPGA
FPGA.SchDoc

U_FPGA2
FPGA2.SchDoc

U_FPGA3
FPGA3.SchDoc

U_FPGA_SDM
FPGA_SDM.SchDoc

U_FPGA_HPS
FPGA_HPS.SchDoc

U_FPGA_PWR1
FPGA_PWR1.SchDoc

U_FPGA_GND
FPGA_GND.SchDoc

U_FPGA_PWR2_UNUSED
FPGA_PWR2_UNUSED.SchDoc

PM1
FIDU-DOT - mini

PM2
FIDU-DOT - mini

PM3
FIDU-DOT - mini

PM4
FIDU-DOT - mini


PM5
FIDU-DOT - mini

PM6
FIDU-DOT - mini

LOGO1
TE Logo PRINT Layer
LOGO PRINT

UKCA
UKCA Logo on Top Overlay
UKCA-TOPOVERLAY

Serial
Serial
Serialnumber 6,3 x 6.3mm



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A4	Number: TEI0131 P001	Rev. 01
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1

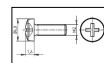
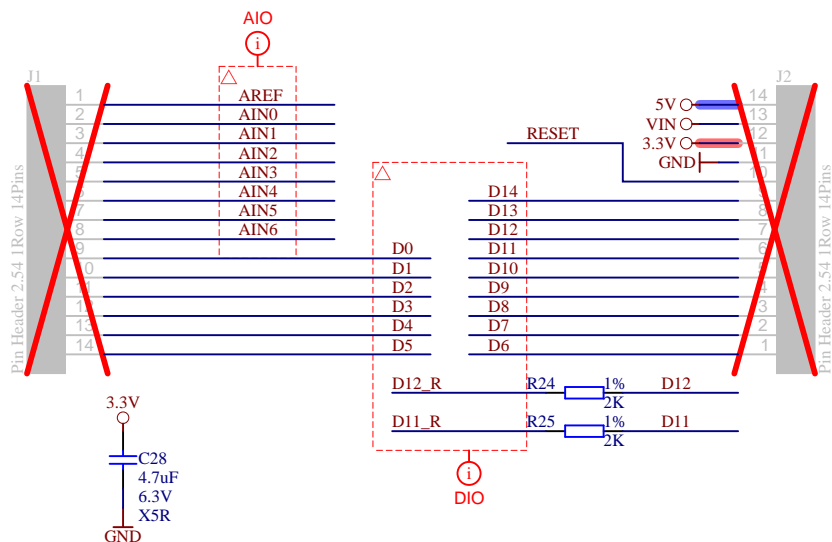
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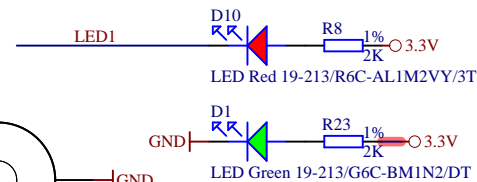
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Screw M2x6



Steel Spacer M2 5mm SMD

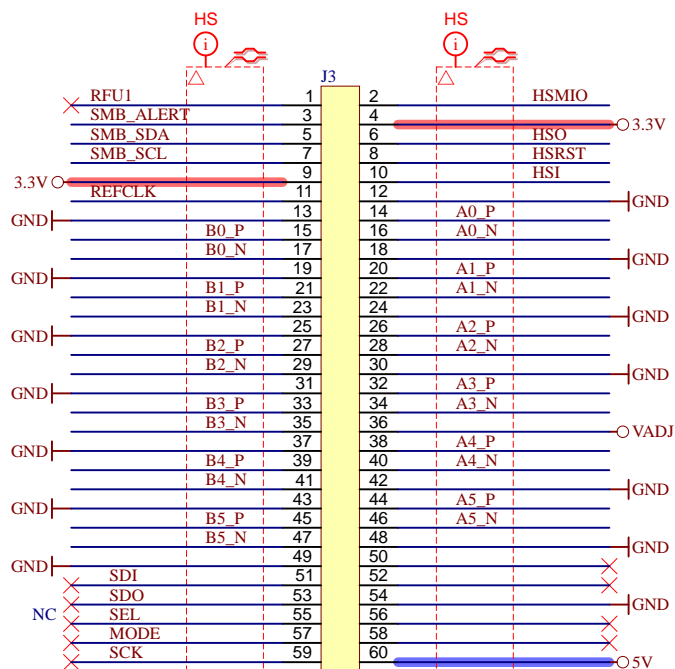


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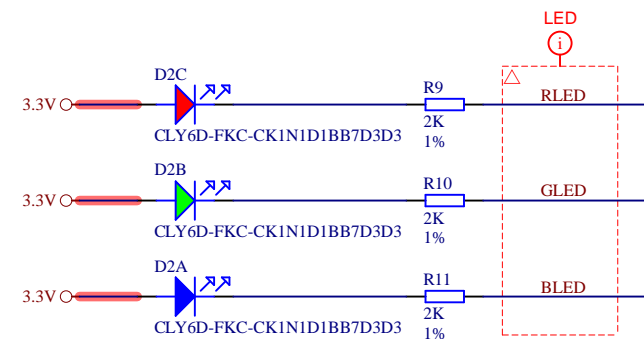
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SS4-30-3.50-L-D-K-TR

SNAP1

SNAP CRUV1 3x



Title:

AXC3000 - Headers

A4

Number: TEI0131
P001

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01

Date: 28.05.2025

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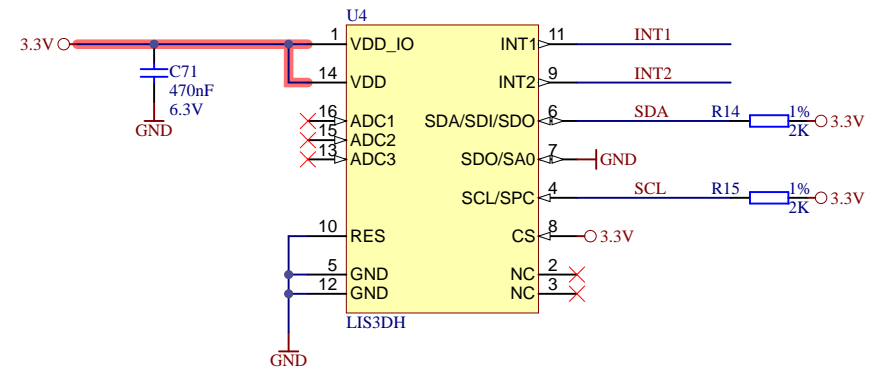
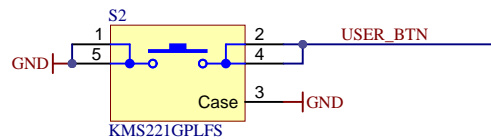
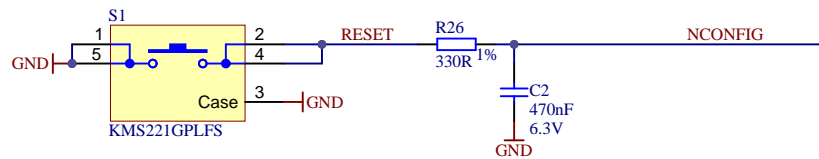
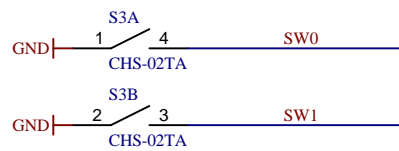
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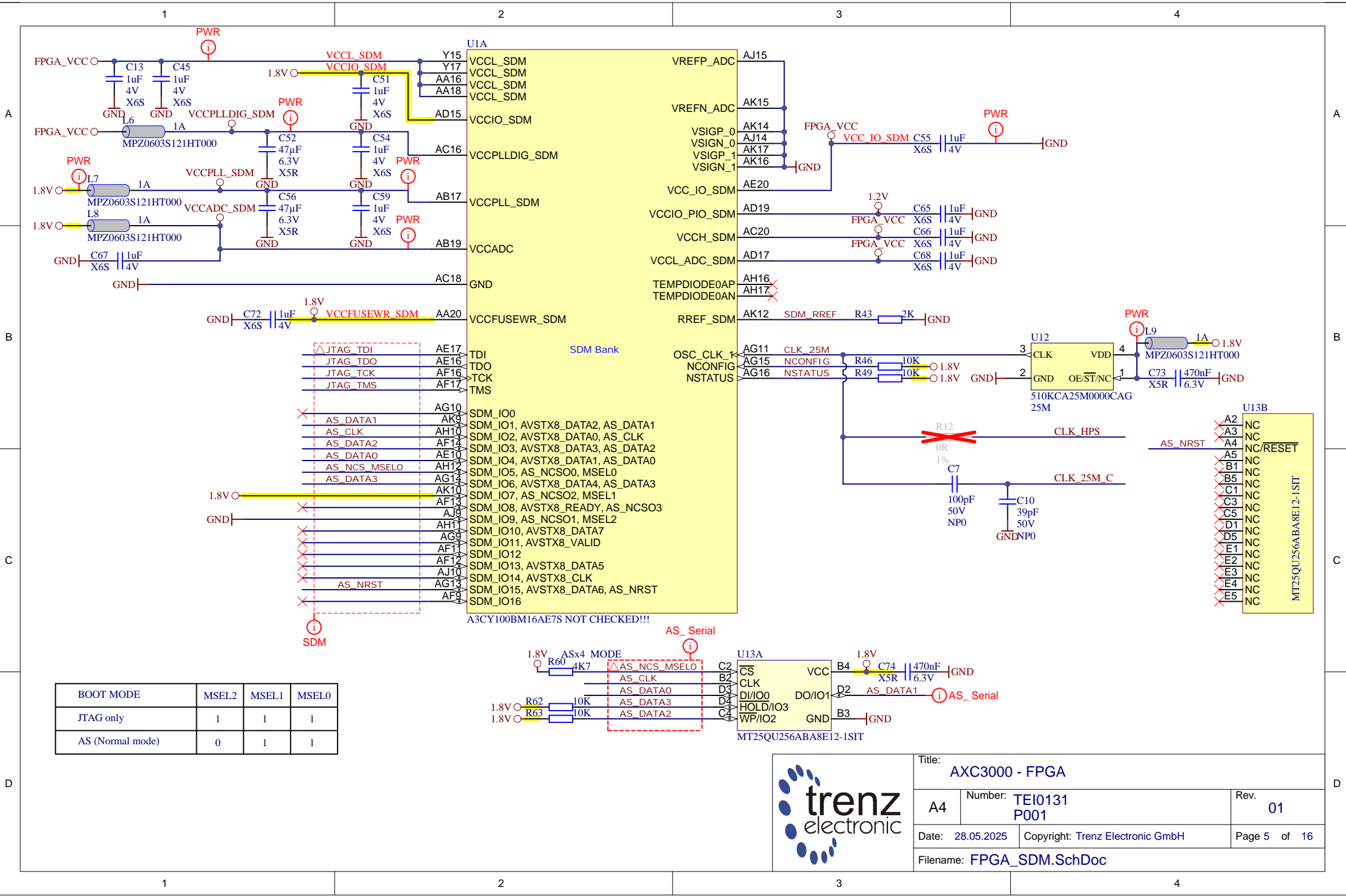
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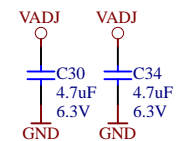
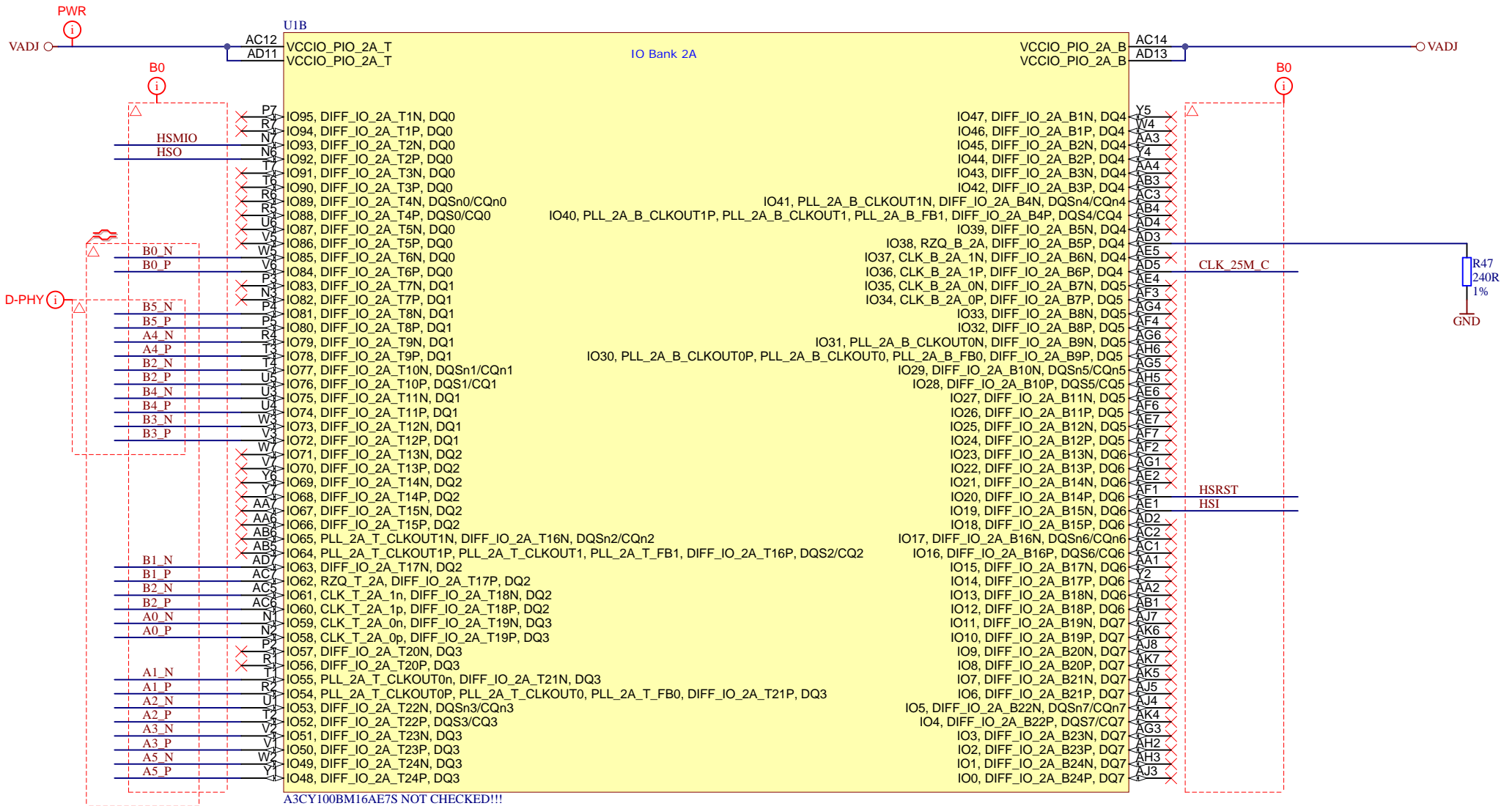
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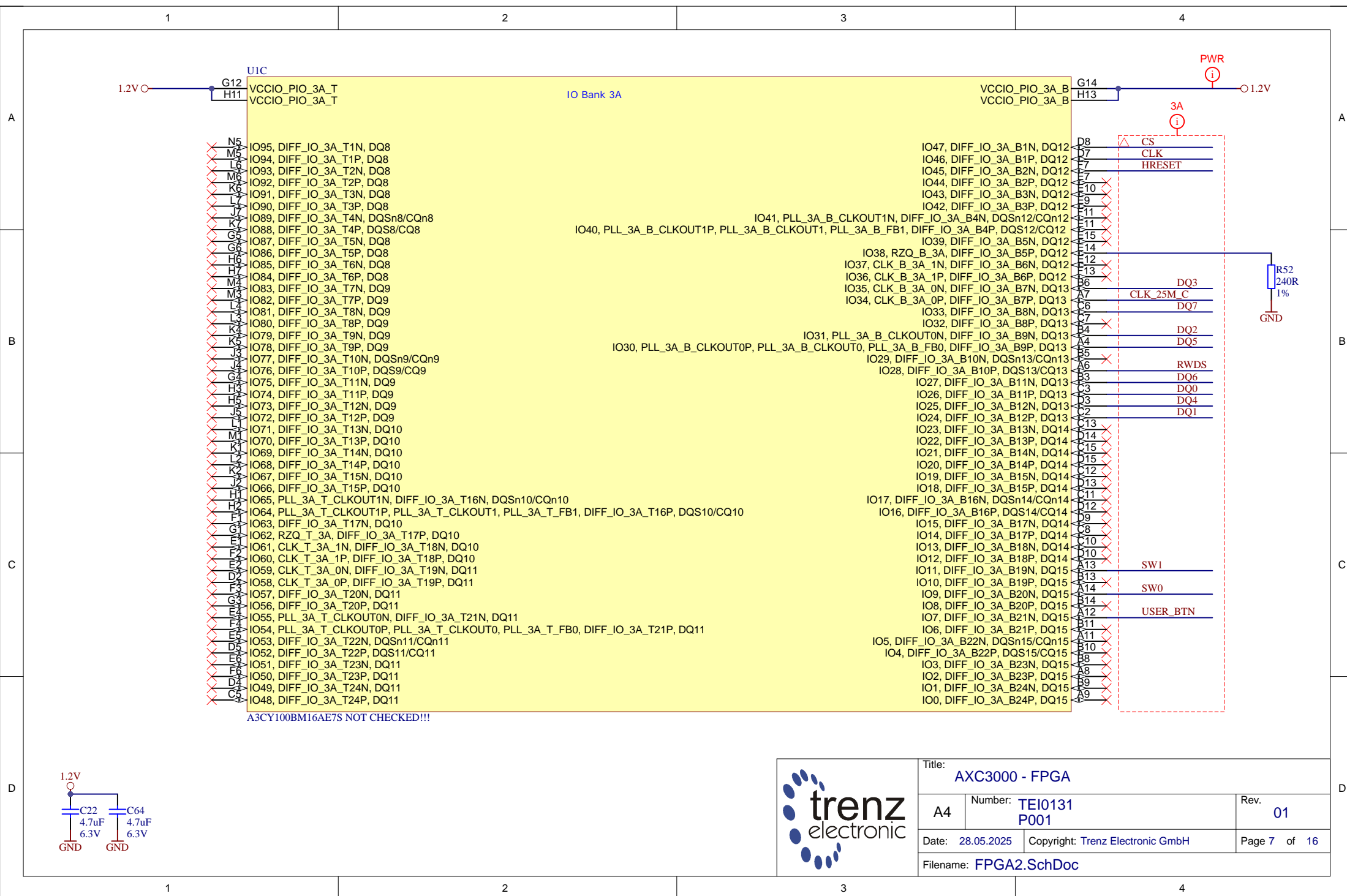


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		A4 Number: TEI0131 P001	Rev. 01
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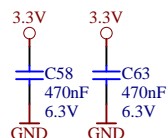
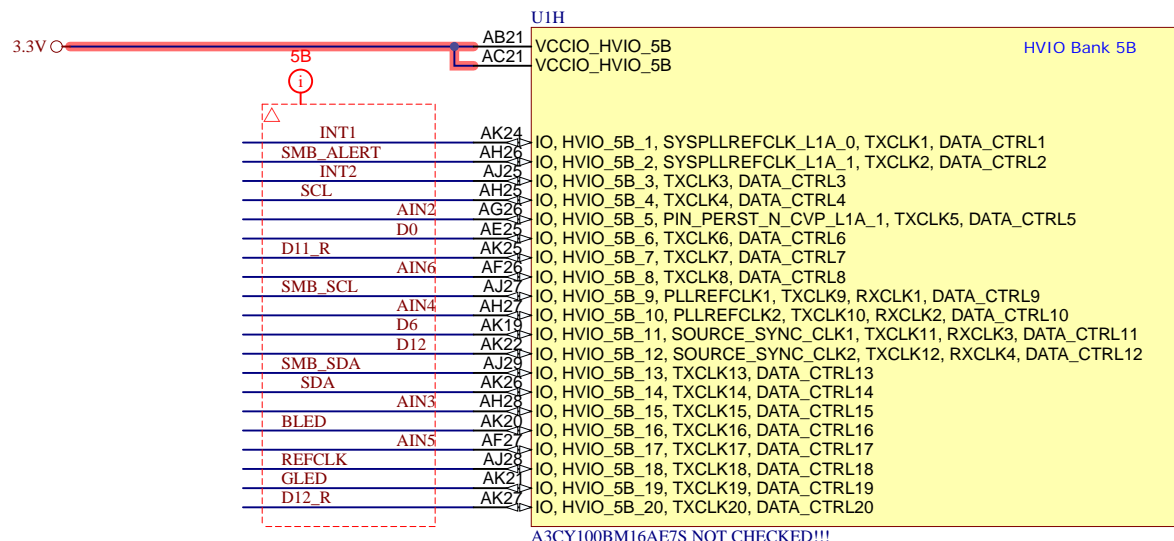
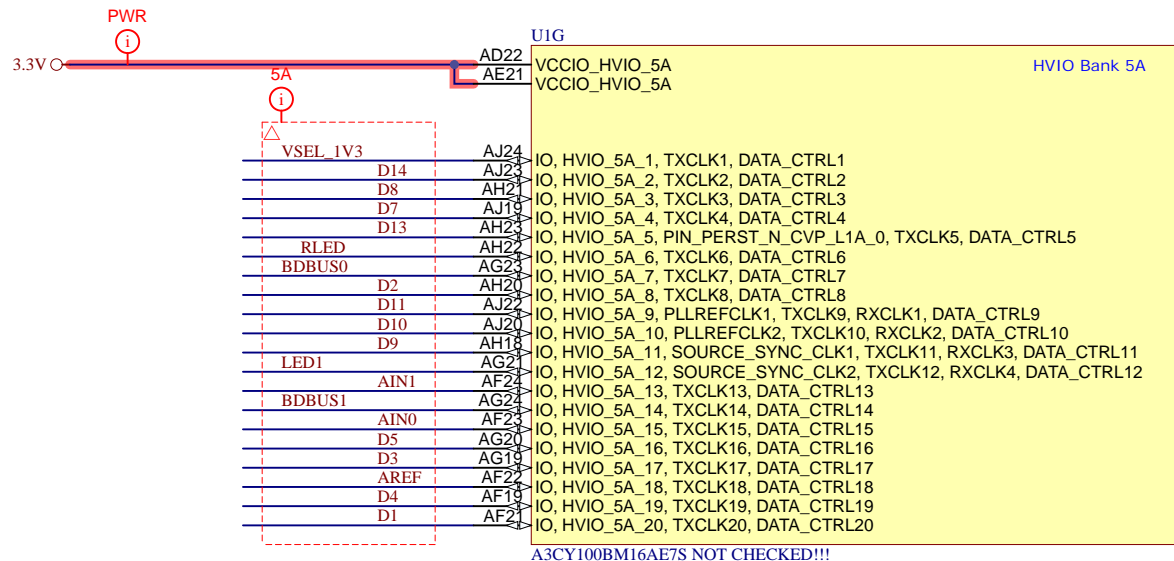
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Title: AXC3000 - FPGA		
A4	Number: TEI0131 P001	Rev. 01
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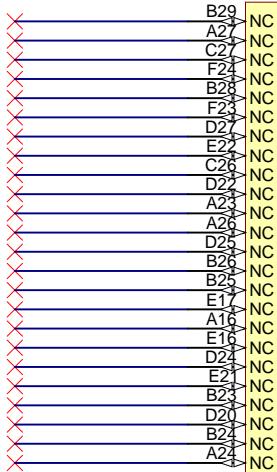
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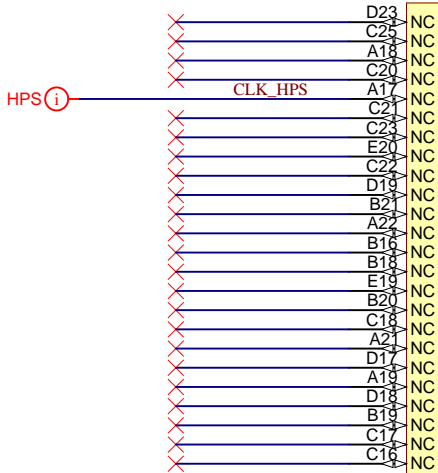
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U1E



A3CY100BM16AE7S NOT CHECKED!!!

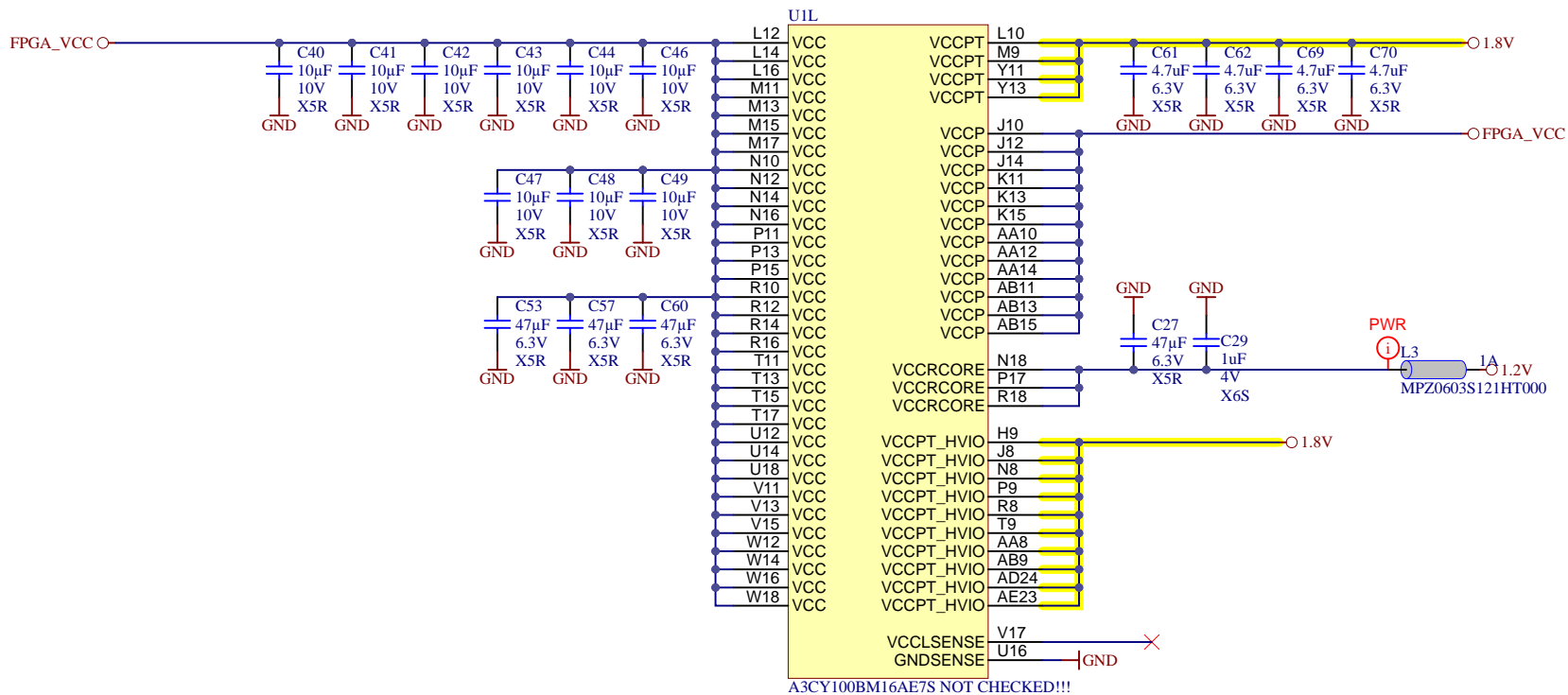
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A3CY100BM16AE7S NOT CHECKED!!!



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A4	Number: TEI0131 P001		Rev. 01
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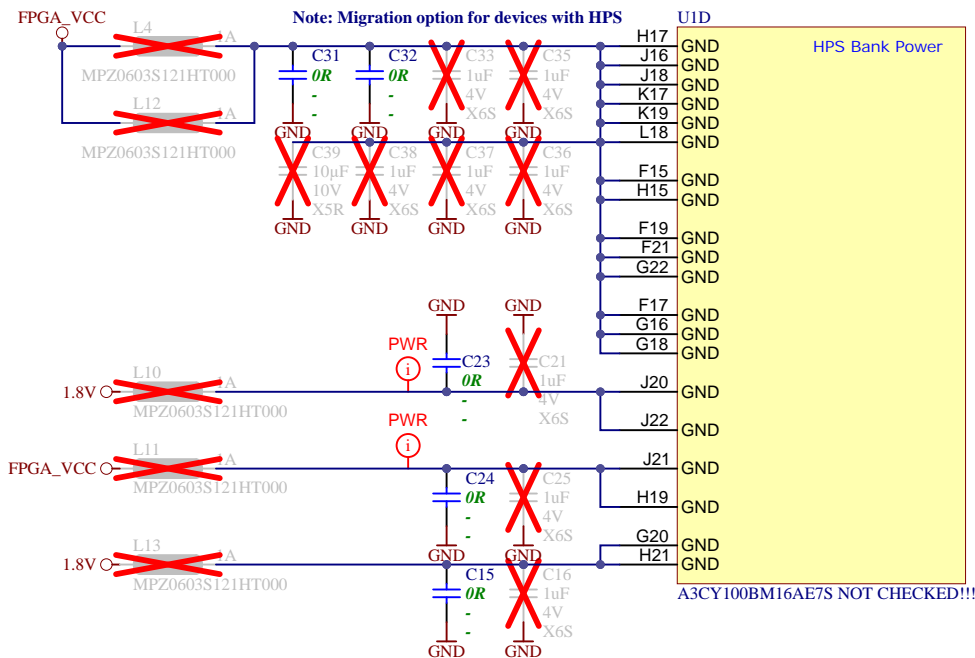
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	Filename: FPGA_PWR1.SchDoc		

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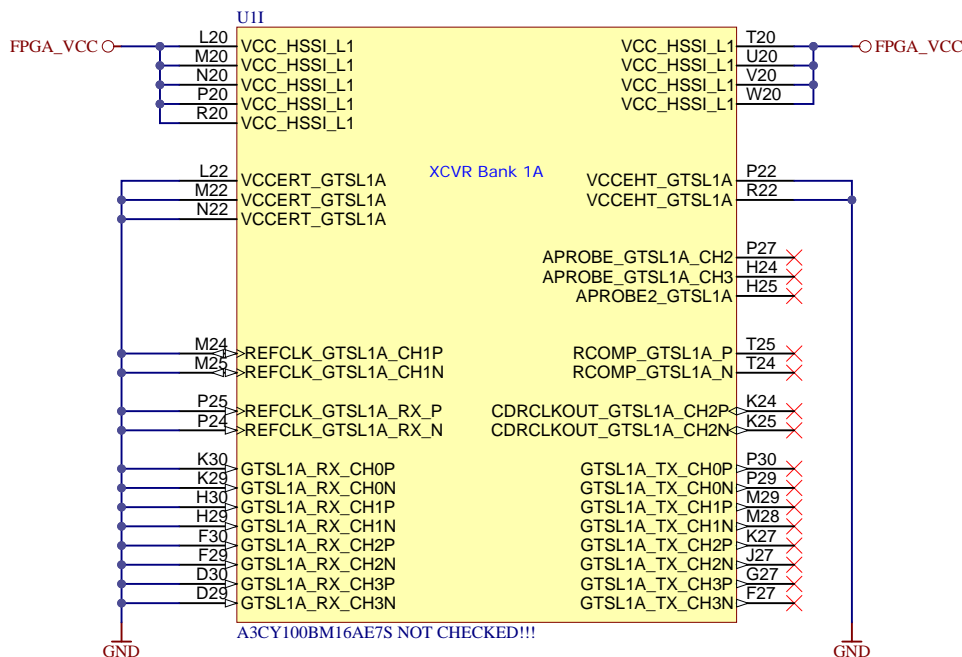


U1J			
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E25	DNU	DNU	AC10
F9	DNU	DNU	AC23
G8	DNU	DNU	AC25
G10	DNU	DNU	AD9
K9	DNU	DNU	AE12
L8	DNU	DNU	AJ12
R27	DNU	DNU	AJ17
U8	DNU	DNU	AJ18
AB24	DNU	DNU	AK11

A3CY100BM16AE7S NOT CHECKED!!!

U1K			
B15	NC	NC	Y30
T22	NC	NC	AA27
T27	NC	NC	AB29
T29	NC	NC	AB30
T30	NC	NC	AC27
U22	NC	NC	AD27
V10	NC	NC	AD29
V22	NC	NC	AD30
V24	NC	NC	AE14
V25	NC	NC	AE18
V28	NC	NC	AF8
V29	NC	NC	AF18
W10	NC	NC	AF29
W22	NC	NC	AF30
Y10	NC	NC	AG8
Y22	NC	NC	AG18
Y24	NC	NC	AH7
Y25	NC	NC	AH8
Y27	NC	NC	AJ2
Y29	NC	NC	

A3CY100BM16AE7S NOT CHECKED!!!



Title: AXC3000 - FPGA			
A4	Number: TEI0131 P001		Rev. 01
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U1M

U1N

U1O

U1P

A2	GND	F16
A3	GND	F18
A5	GND	F20
A10	GND	F22
A15	GND	F25
A20	GND	F26
A25	GND	F28
A28	GND	G2
A29	GND	G7
B1	GND	G9
B2	GND	G11
B7	GND	G13
B12	GND	G15
B17	GND	G17
B22	GND	G19
B27	GND	G21
B30	GND	G23
C1	GND	G24
C4	GND	G25
C9	GND	G26
C14	GND	G28
C19	GND	G29
C24	GND	G30
C28	GND	H4
C29	GND	H8
C30	GND	H10
D1	GND	H12
D6	GND	H14
D11	GND	H16
D16	GND	H18
D21	GND	H20
D26	GND	H22
D28	GND	H23
E3	GND	H26
E8	GND	H27
E13	GND	H28
E18	GND	J1
E23	GND	J6
E26	GND	J9
E27	GND	J11
E28	GND	J13
E29	GND	J15
E30	GND	J17
F5	GND	J19
F8	GND	J23
F10	GND	J24
F12	GND	J25
F14	GND	J26

A3CY100BM16AE7S NOT CHECKED!!!

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J29	GND	N13
J30	GND	N15
K3	GND	N17
K8	GND	N19
K10	GND	N21
K12	GND	N23
K14	GND	N24
K16	GND	N25
K18	GND	N26
K20	GND	N27
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K22	GND	N29
K23	GND	N30
K26	GND	P1
K28	GND	P6
L5	GND	P8
L9	GND	P10
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L15	GND	P16
L17	GND	P18
L19	GND	P19
L21	GND	P21
L23	GND	P23
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L26	GND	R3
L27	GND	R9
L28	GND	R11
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M10	GND	R23
M12	GND	R24
M14	GND	R25
M16	GND	R26
M18	GND	R28
M19	GND	R29
M21	GND	R30
M23	GND	T5
M26	GND	T8
M27	GND	T10
M30	GND	T12
N4	GND	T14
N9	GND	T16

A3CY100BM16AE7S NOT CHECKED!!!

T18	GND	W24
T19	GND	W25
T21	GND	W26
T23	GND	W27
T26	GND	W28
T28	GND	W29
U2	GND	W30
U7	GND	Y3
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U10	GND	Y9
U11	GND	Y12
U13	GND	Y14
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U17	GND	Y18
U19	GND	Y19
U21	GND	Y20
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U25	GND	Y26
U26	GND	Y28
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U28	GND	AA9
U29	GND	AA11
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V23	GND	AA28
V26	GND	AA29
V27	GND	AA30
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W8	GND	AB8
W9	GND	AB10
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W13	GND	AB14
W15	GND	AB16
W17	GND	AB18
W19	GND	AB20
W21	GND	AB22
W23	GND	AB23
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A3CY100BM16AE7S NOT CHECKED!!!

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AB27	GND	AF5
AB28	GND	AF10
AC4	GND	AF15
AC9	GND	AF20
AC11	GND	AF25
AC13	GND	AG2
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AE22	GND	AK8
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A3CY100BM16AE7S NOT CHECKED!!!

GND



Title:

AXC3000 - FPGA

A4

Number: TEI0131
P001

Rev.

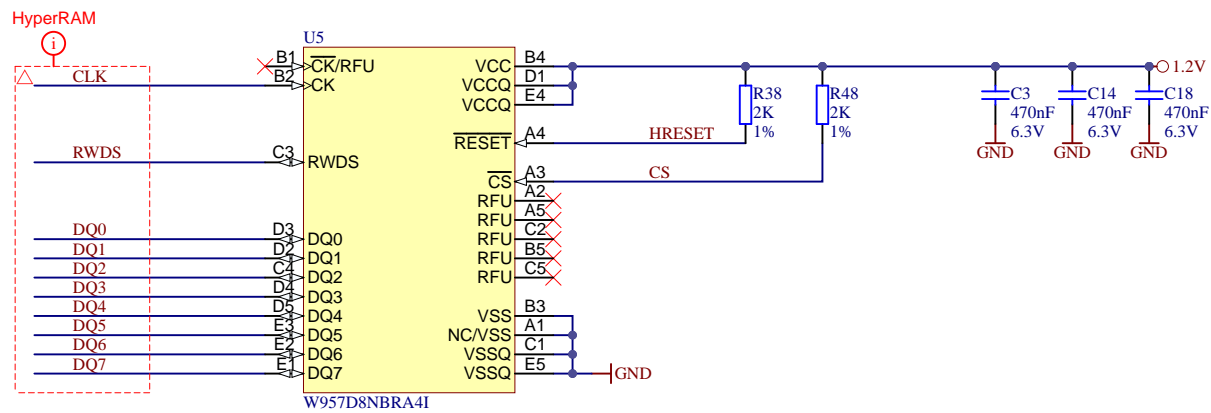
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Date: 28.05.2025

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Rev. 01

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A

A

B

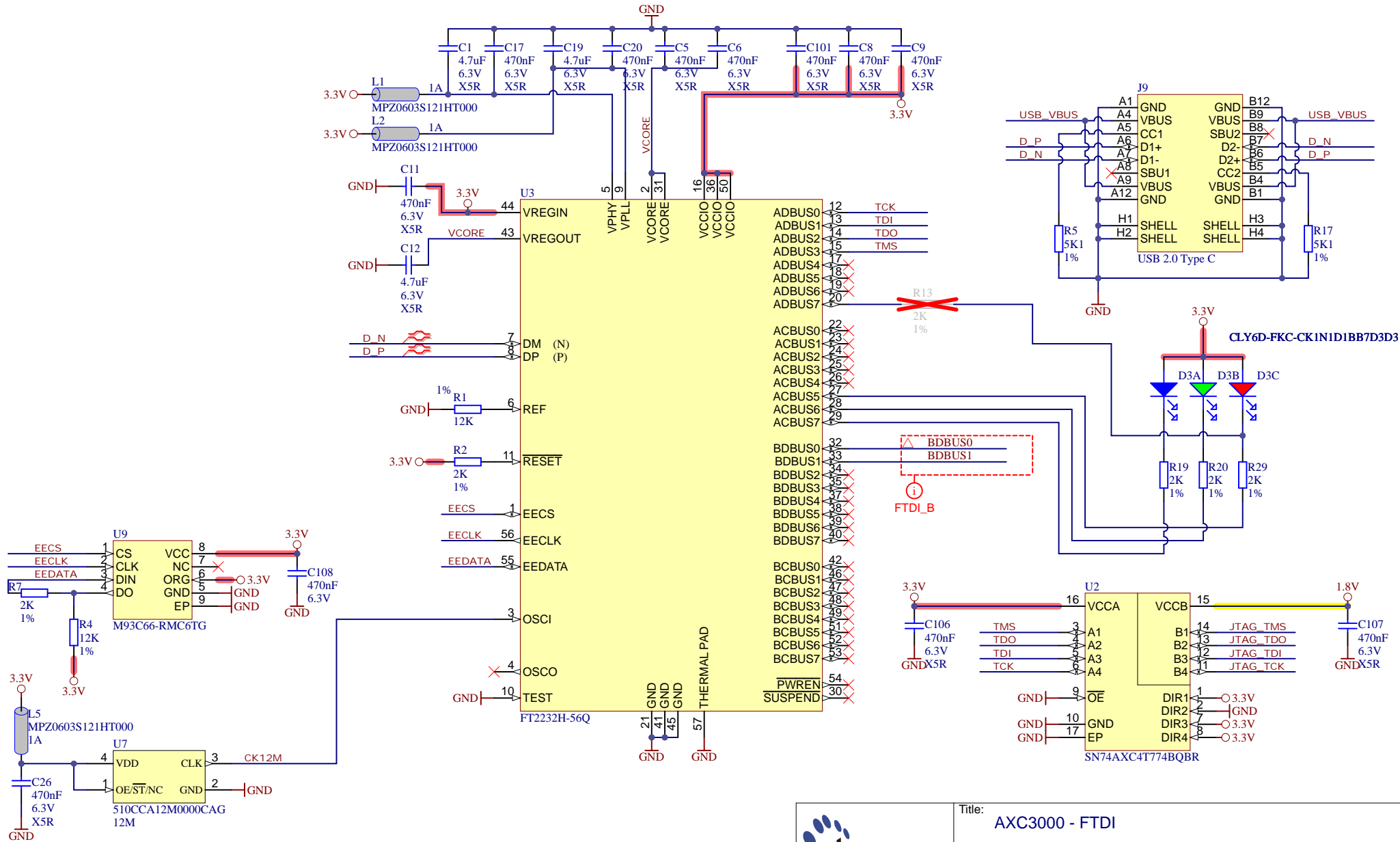
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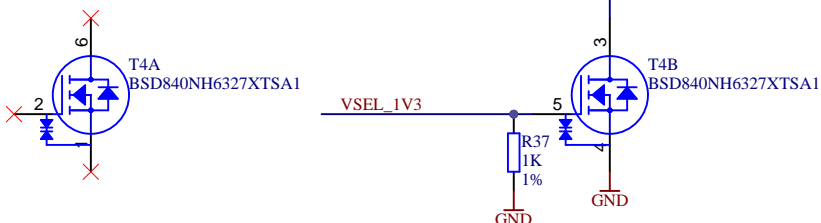
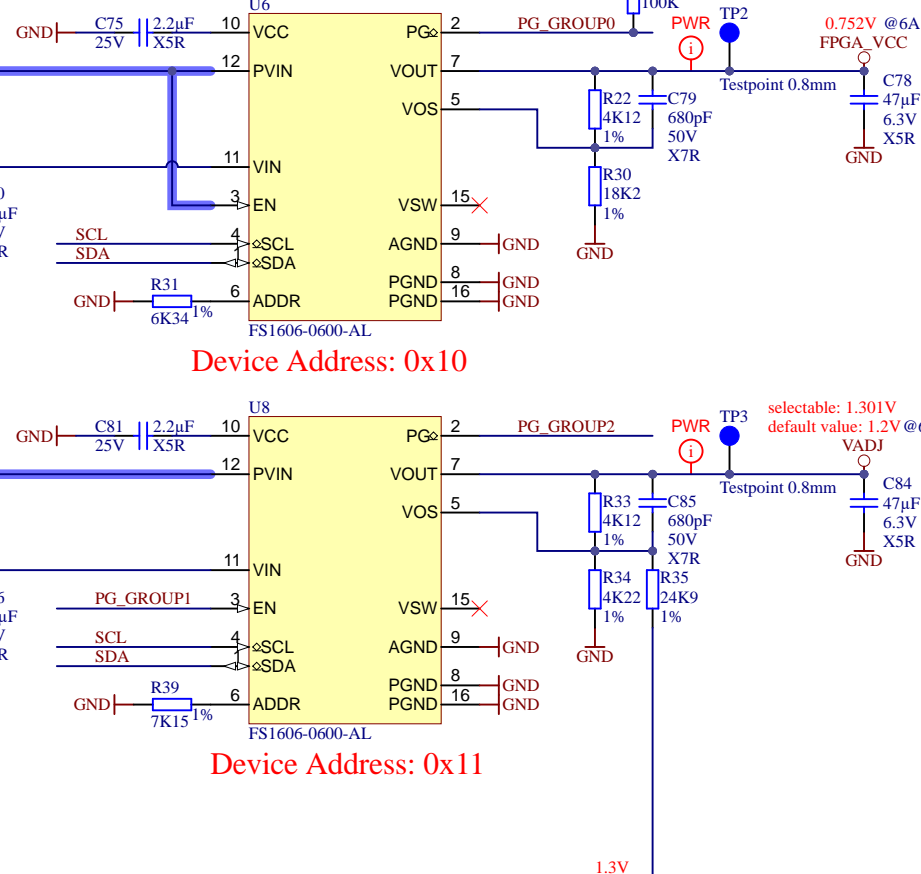
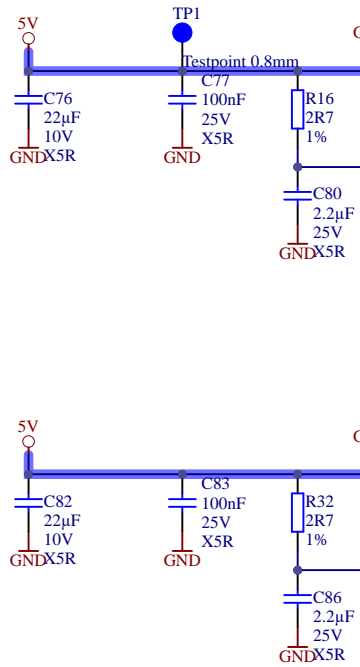
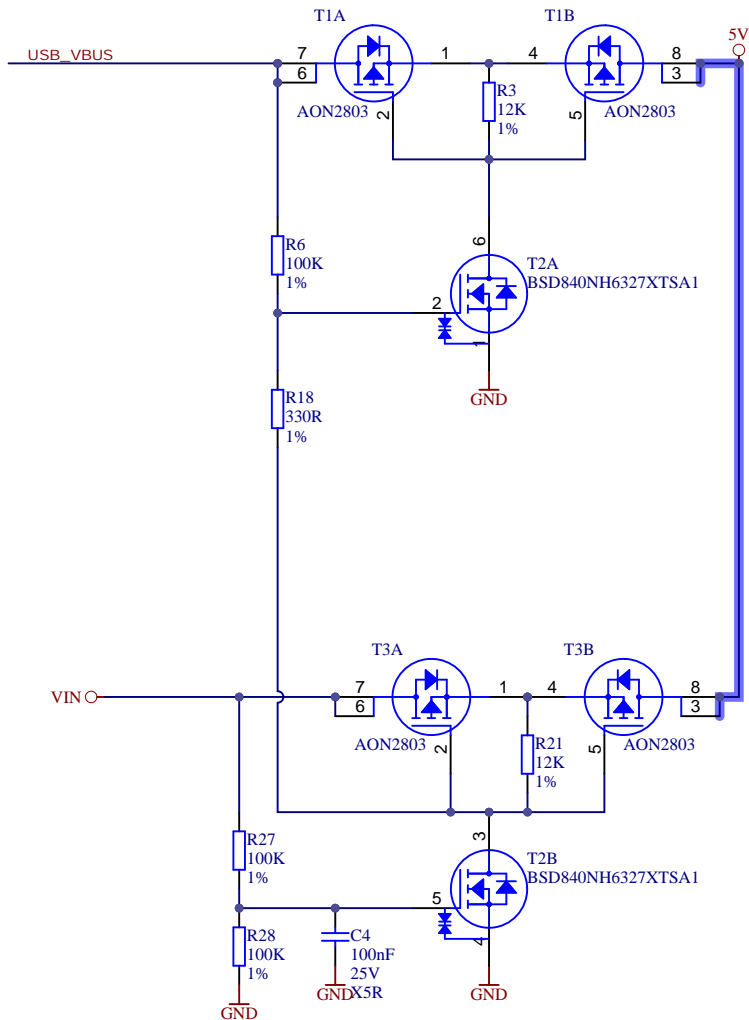
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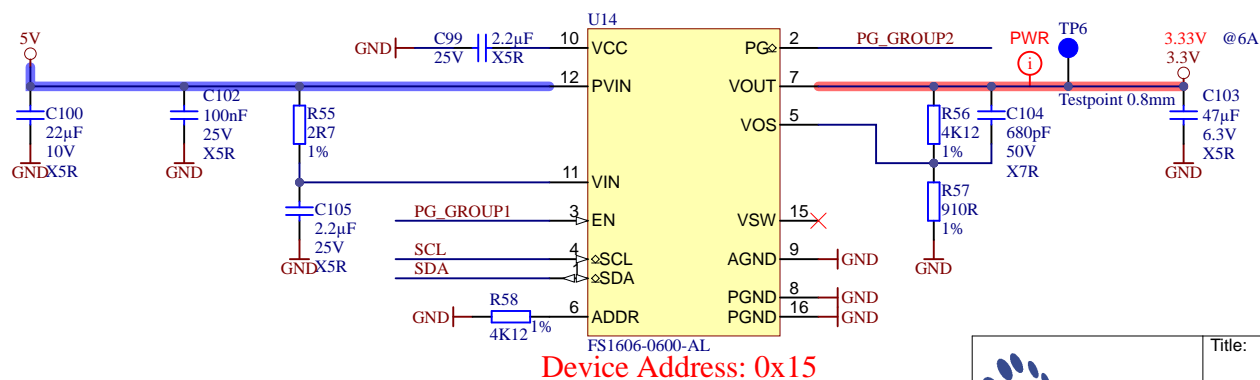
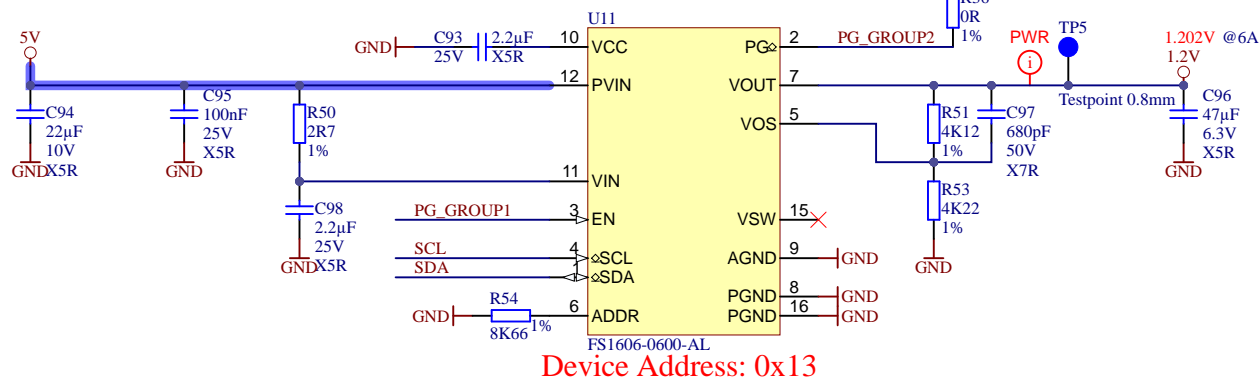
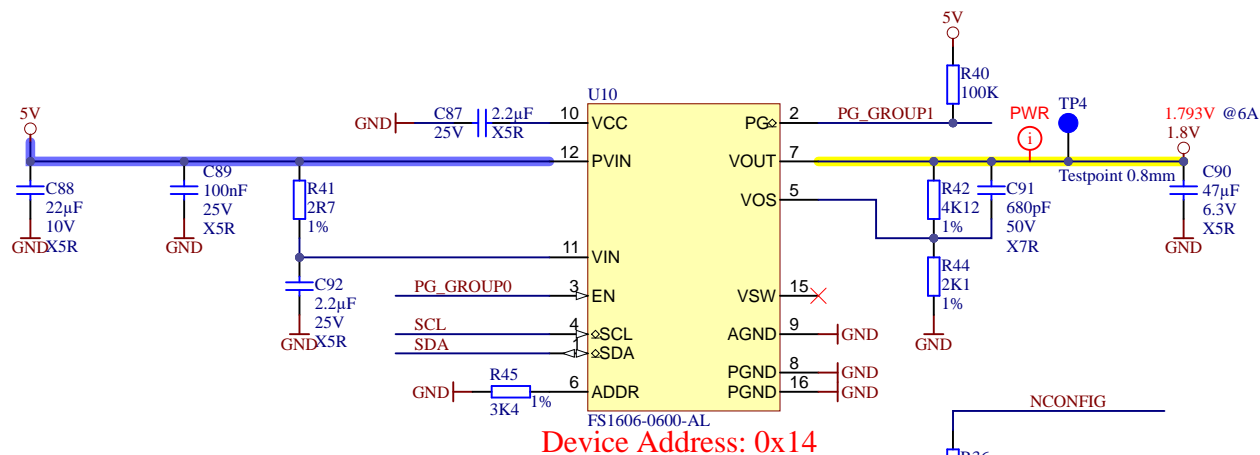
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Date: 28.05.2025	Copyright: Trenz Electronic GmbH	Page 15 of 16
Filename: PowerSupply.SchDoc		

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