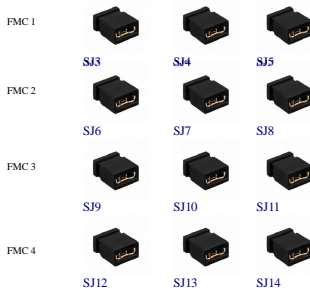
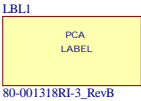
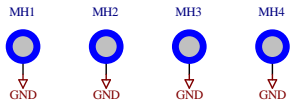
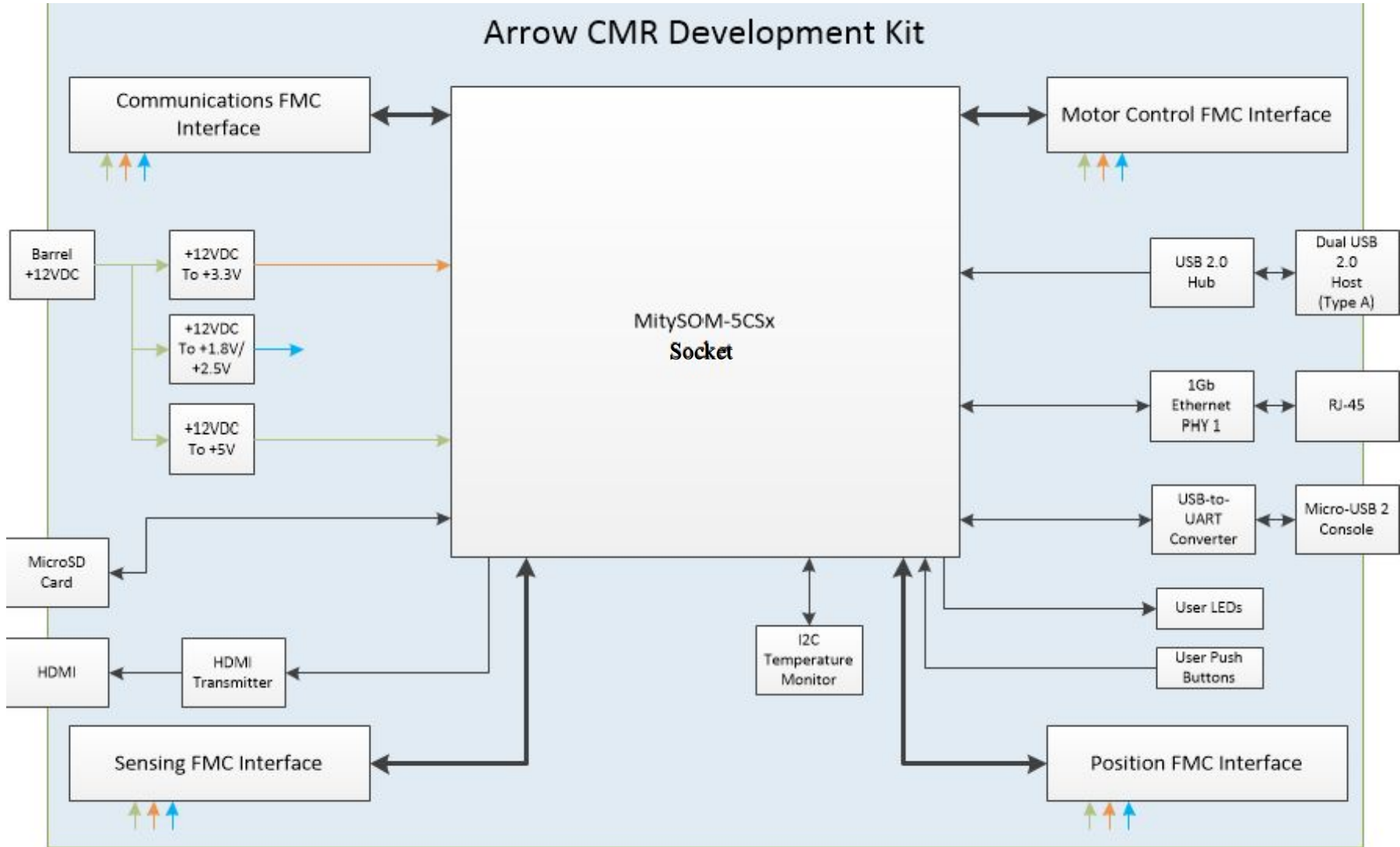
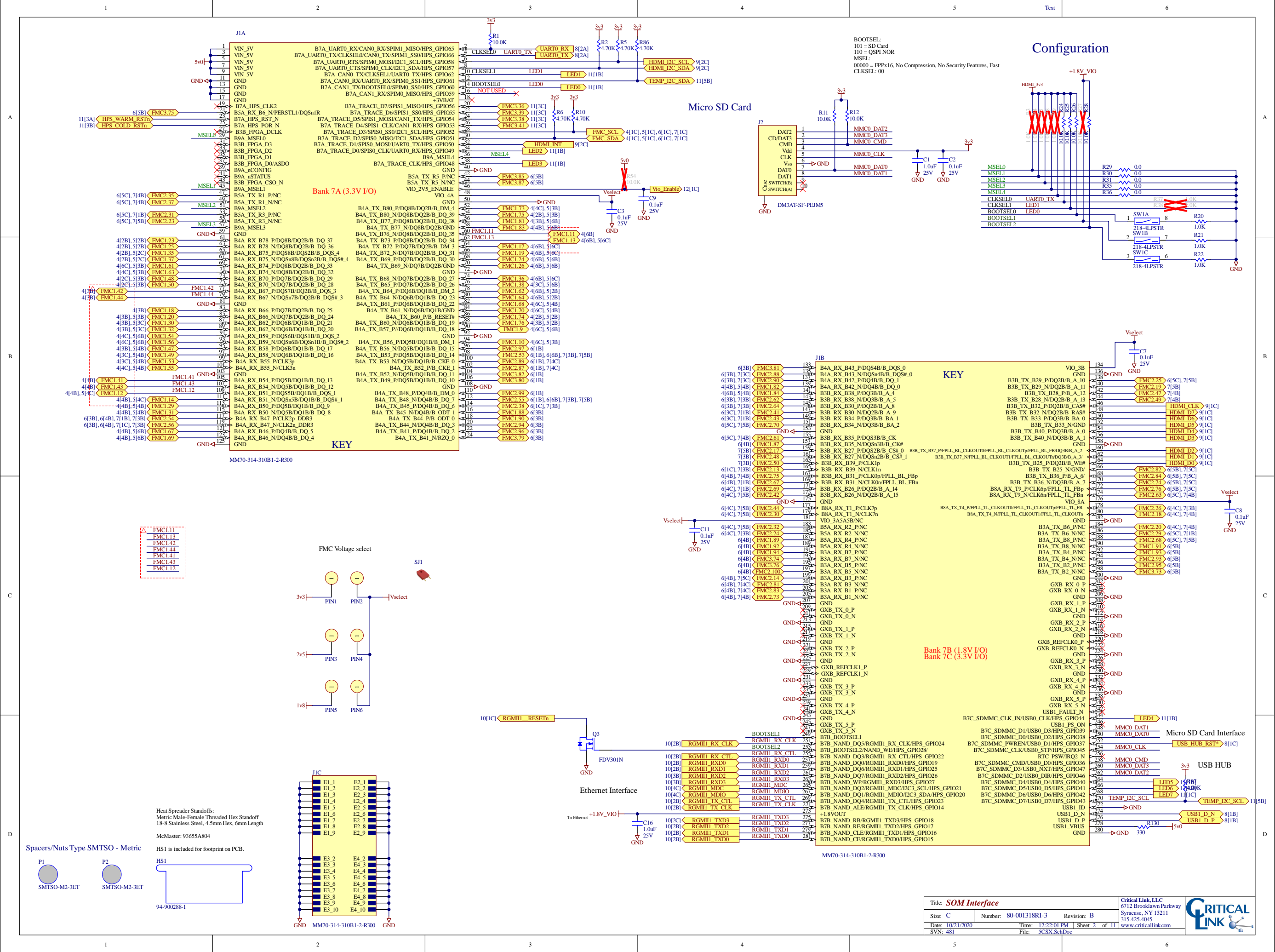
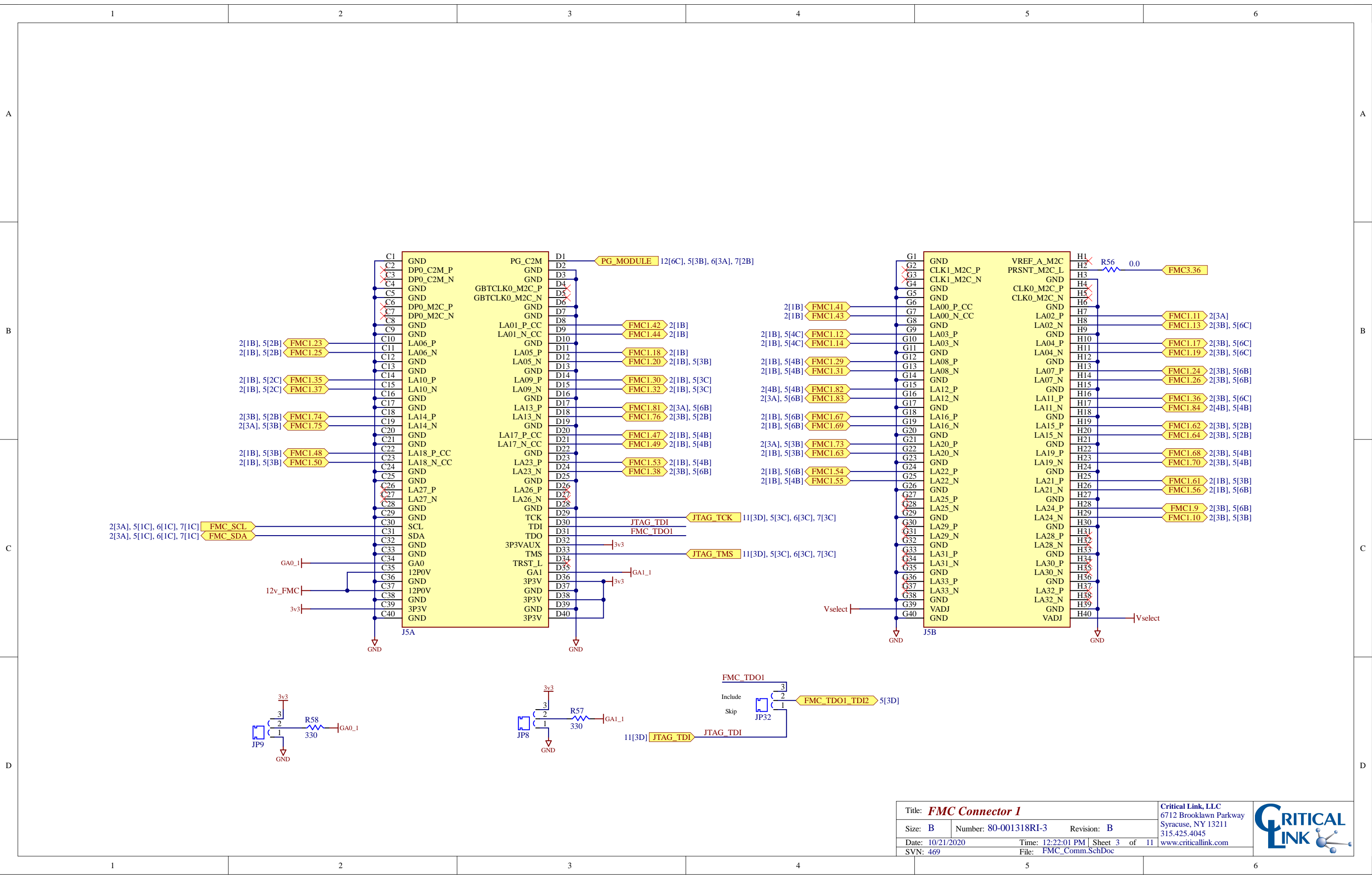


REV	DESCRIPTION	ECR No	INITIALS	DATE
1A	Initial Release	1939	PJB	04/23/20
2A	Update push button reset circuit	2000	PJB	05/05/20
3A	Updates from original build, removal of logic level shift.	2045	PJB	09/13/20
3B	5mil spacing on internal layers, Fixed board outline and missing trace.	2084	PJB	10/21/20







A

B

C

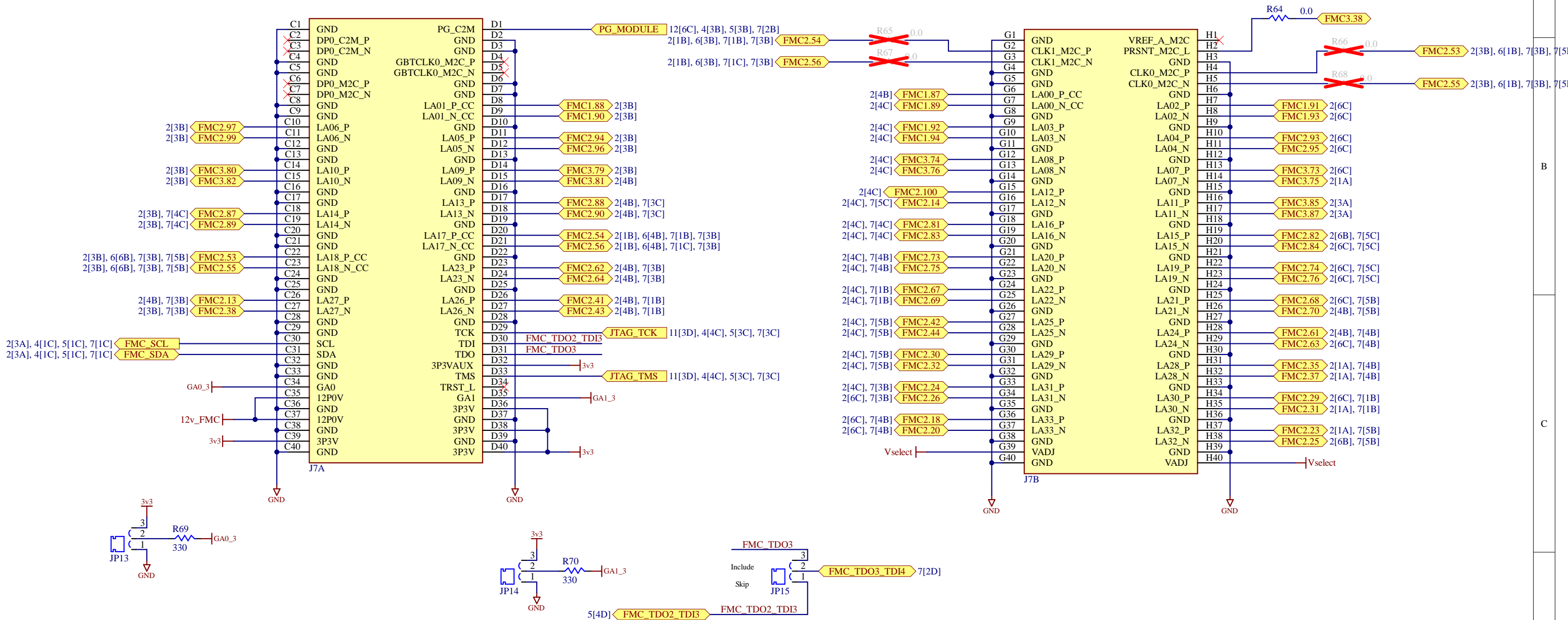
D

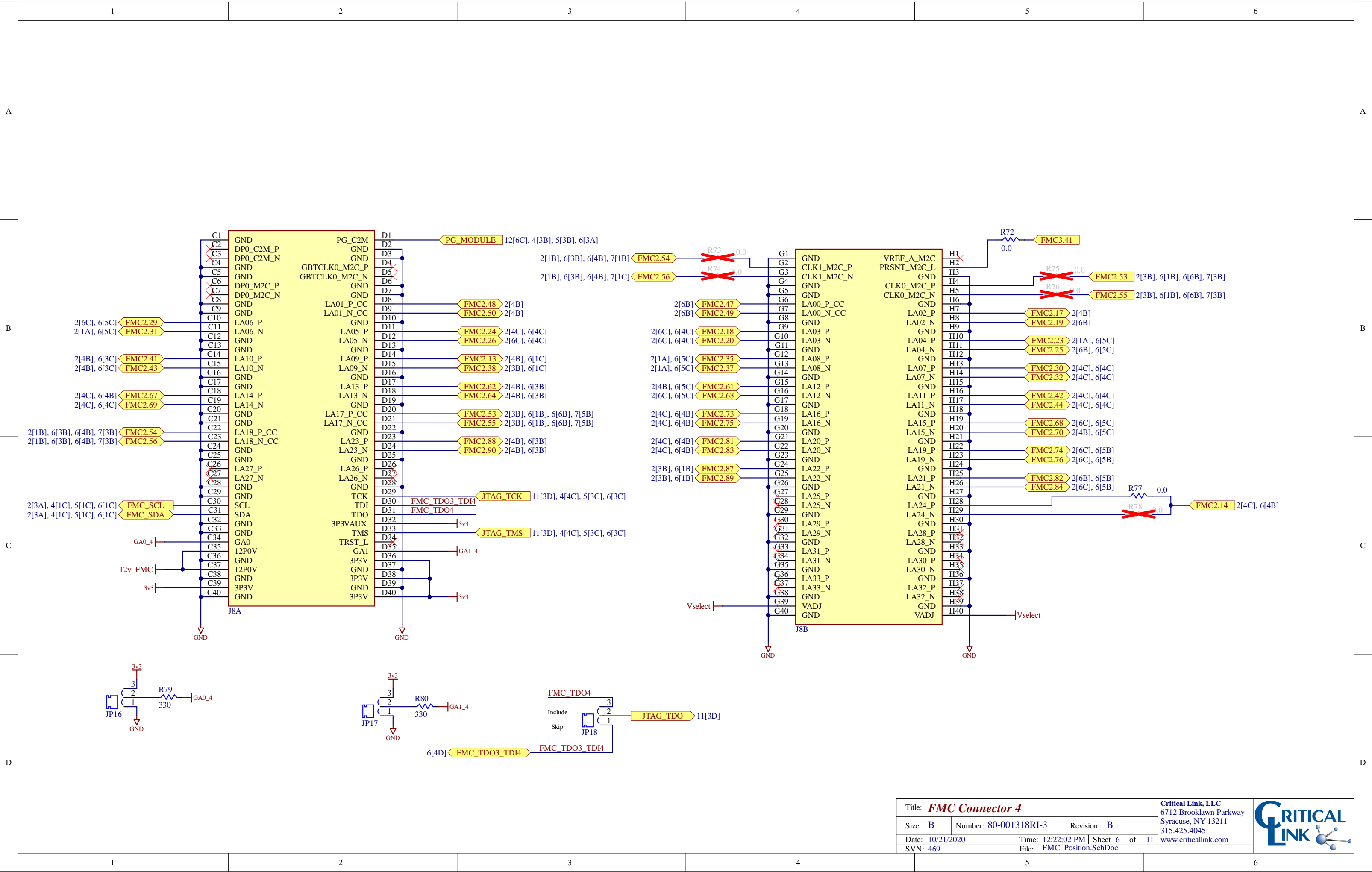
A

B

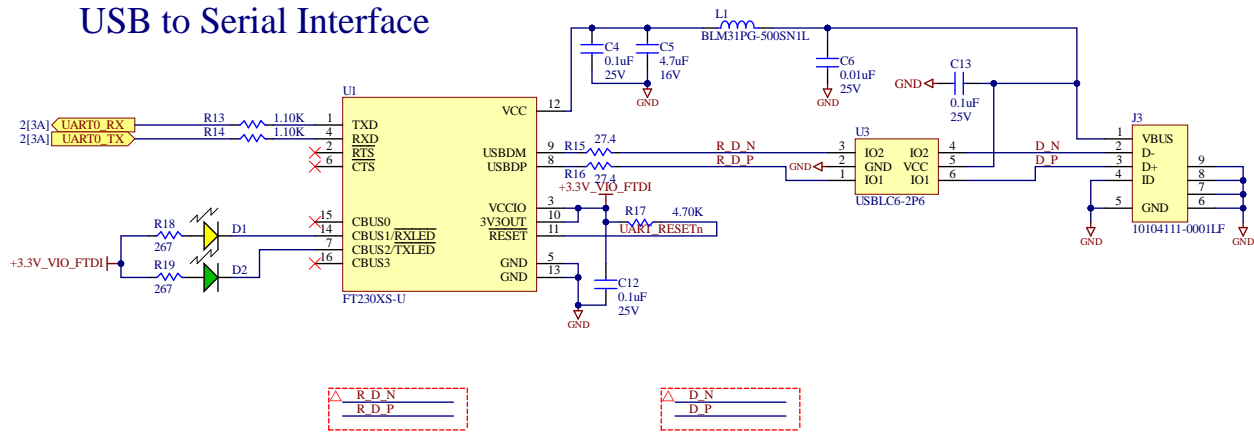
C

D



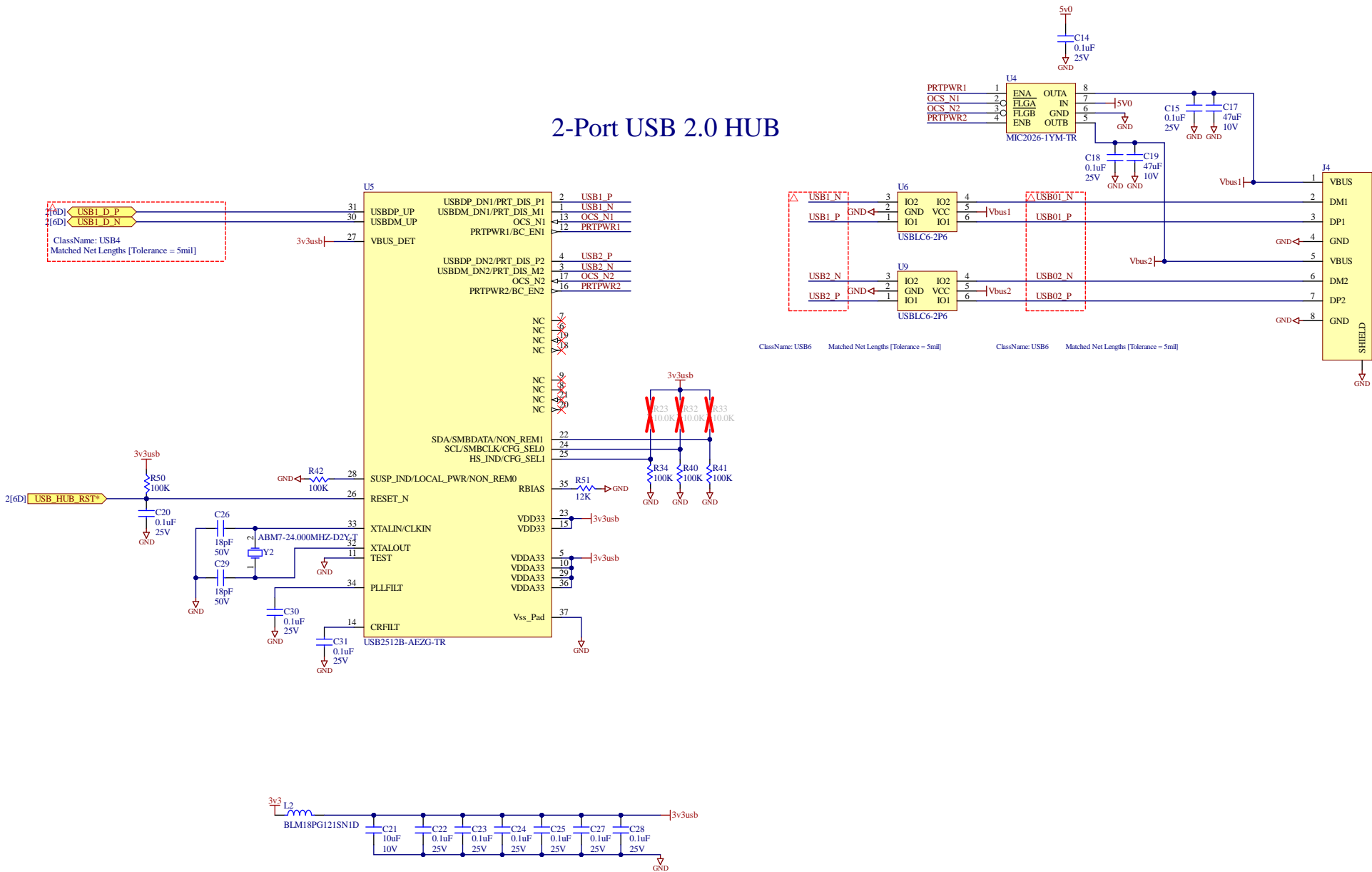


USB to Serial Interface



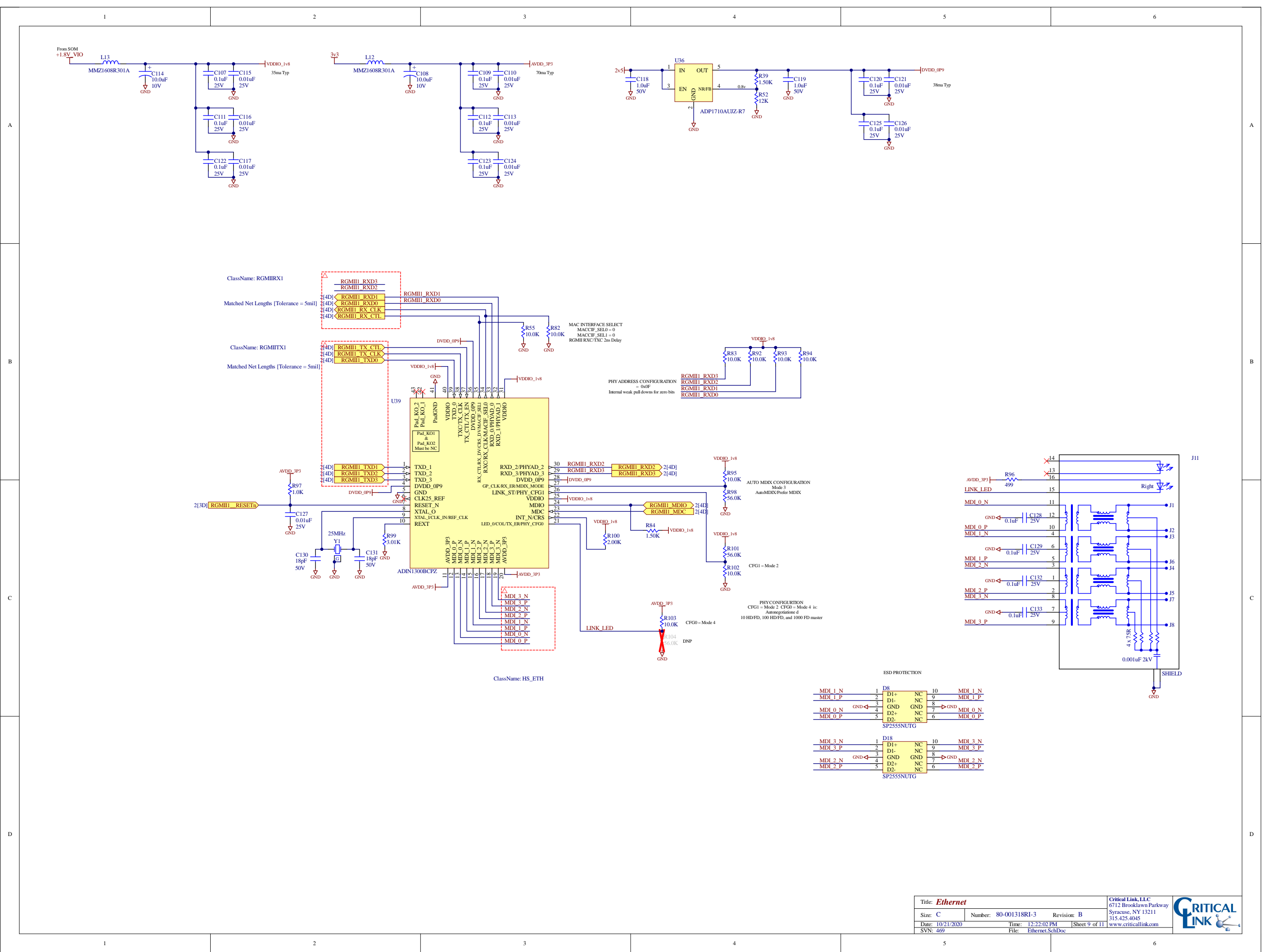
ClassName: USB Matched Net Lengths [Tolerance = 5mil] ClassName: USB Matched Net Lengths [Tolerance = 5mil]

2-Port USB 2.0 HUB



ClassName: USB6 Matched Net Lengths [Tolerance = 5mil] ClassName: USB6 Matched Net Lengths [Tolerance = 5mil]





A

B

C

D

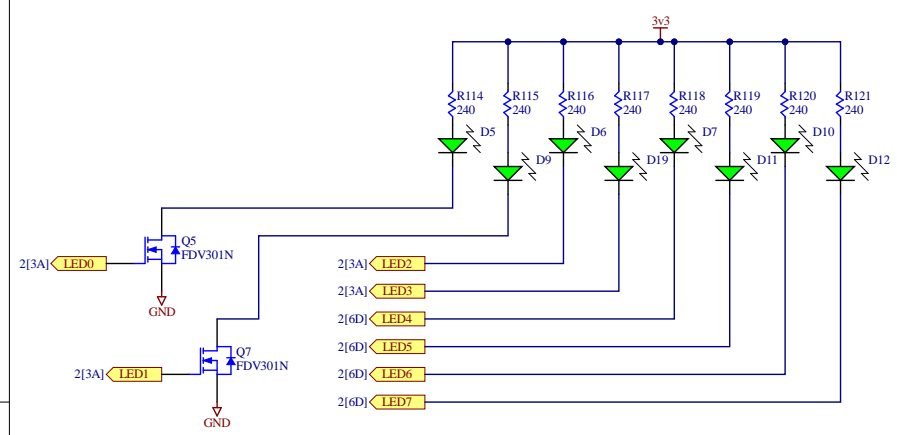
A

B

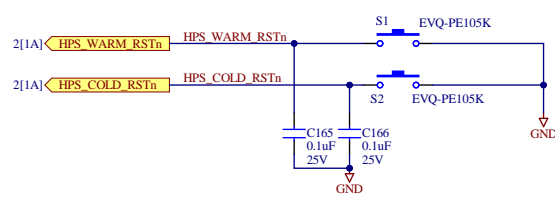
C

D

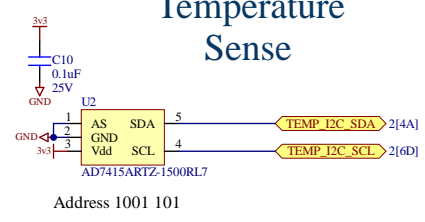
User LEDS



Resets

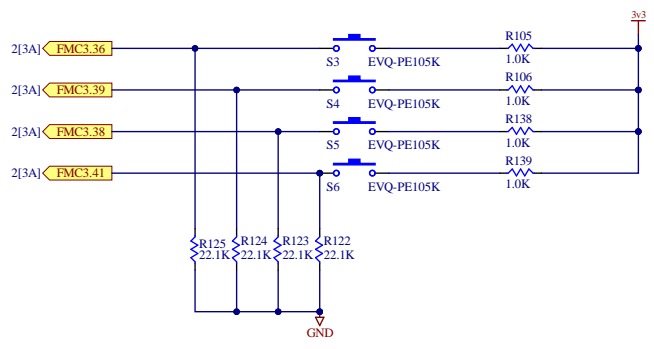


Temperature Sense

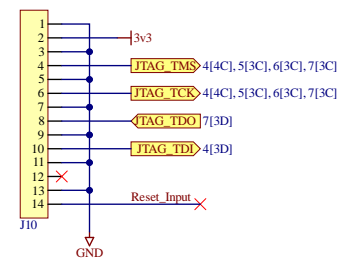


User Push Buttons

Optional use, may need to remove FMC present signal jumper.



JTAG



PL PMOD

