

1

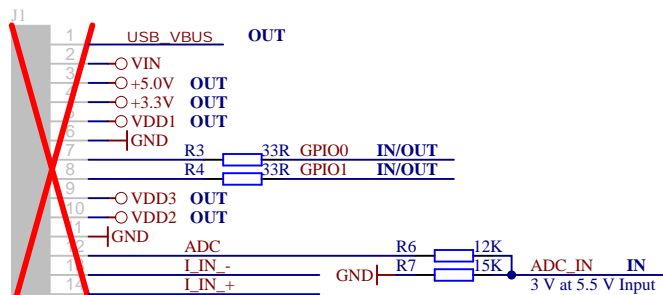
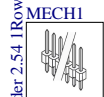
2

3

4

A

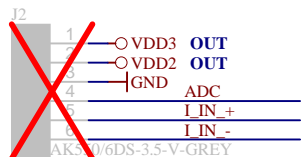
Pin Header 2.54 1Row 14Pins



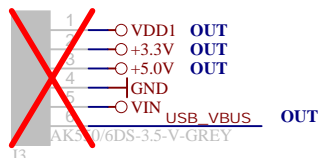
Pin Header 2.54 1Row 14Pins

B

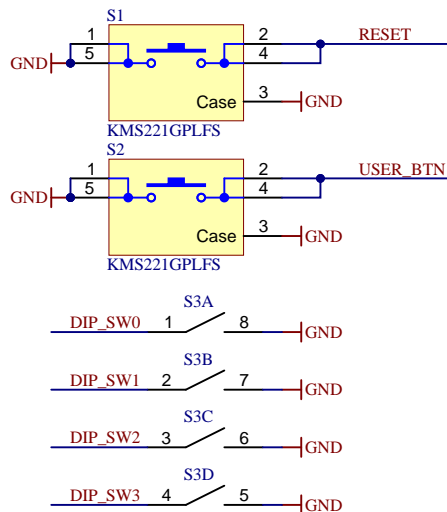
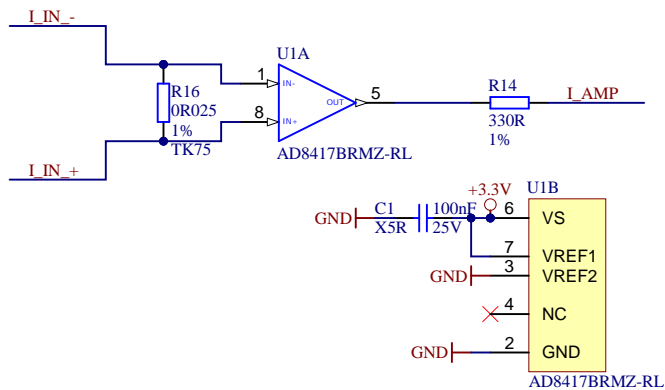
AK550/6DS-3.5-V-GREY



C



D



Title: TEI0026 - Headers		
A4	Number: TEI0026 A	Rev. 01
Date: 18.05.2020	Copyright: Trenz Electronic GmbH	Page 2 of 7
Drawn by: ED	Filename: A-Headers.SchDoc	

1

2

3

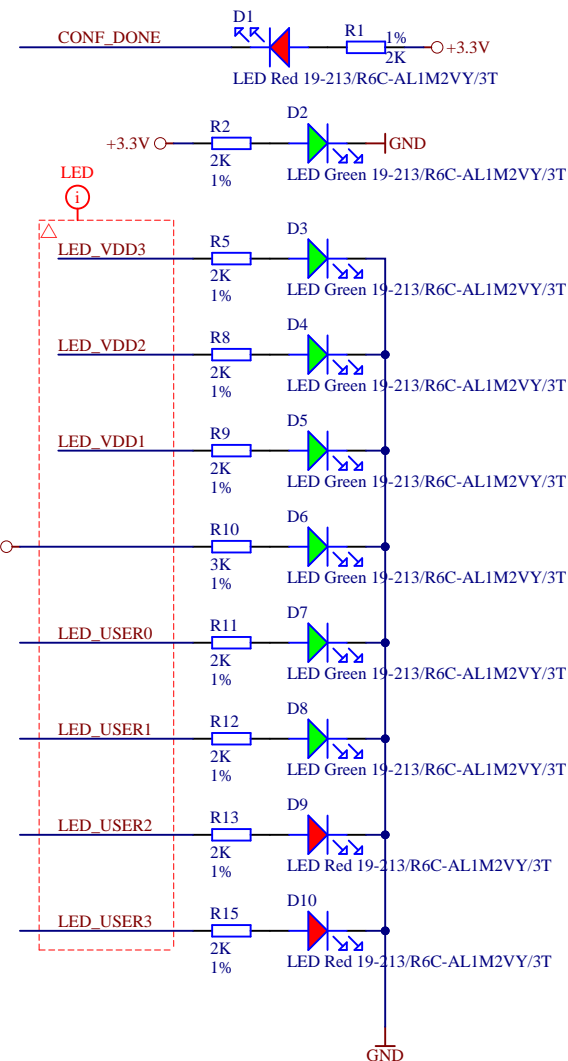
4

A

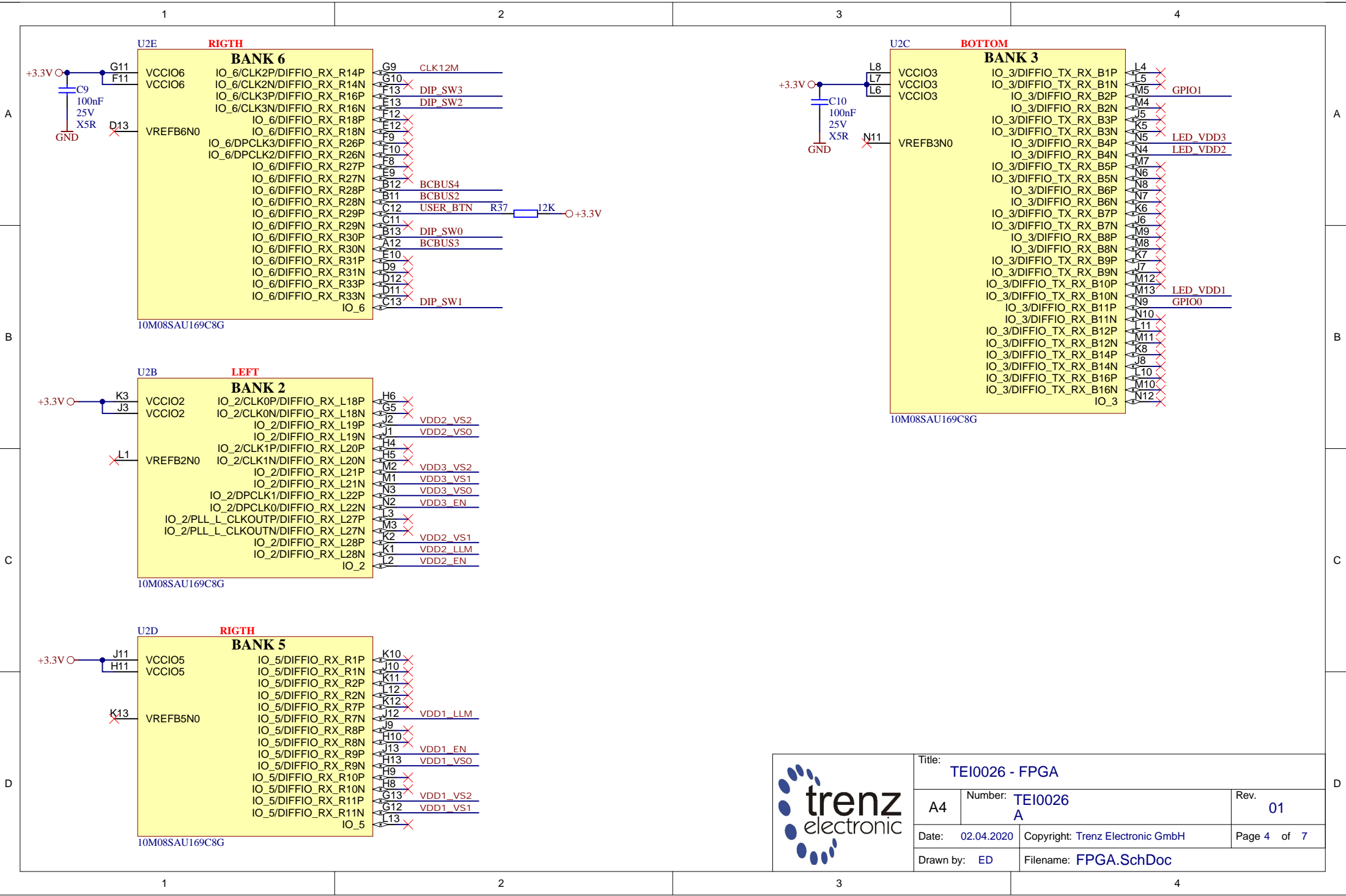
B


C

D







				Title: TEI0026 - FPGA	
				A4	Number: TEI0026 A
Date: 02.04.2020		Copyright: Trenz Electronic GmbH		Rev. 01	
Drawn by: ED		Filename: FPGA.SchDoc		Page 4 of 7	

1

2

3

4

A

A

B

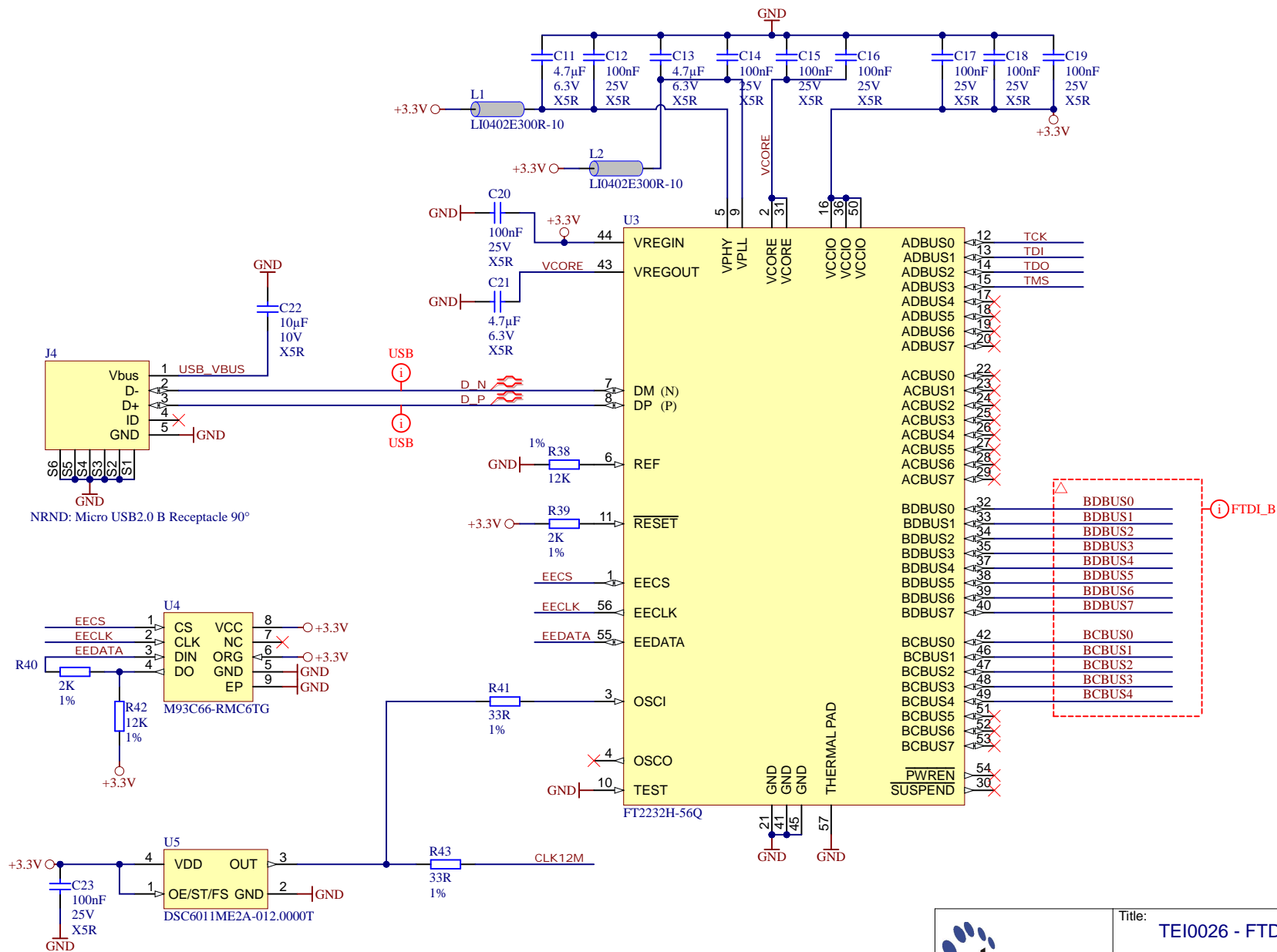
B

C

C

D

D



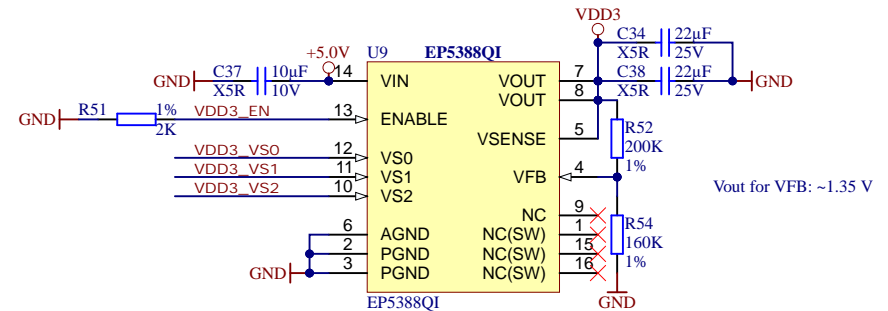
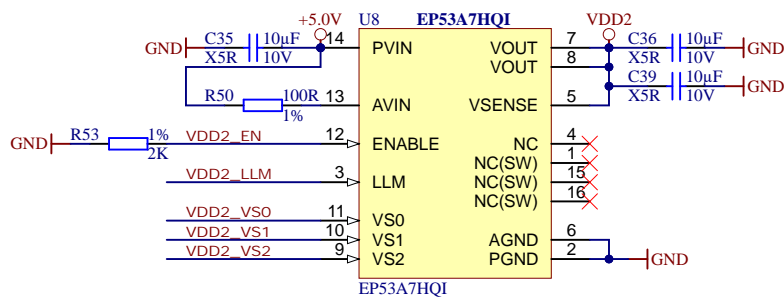
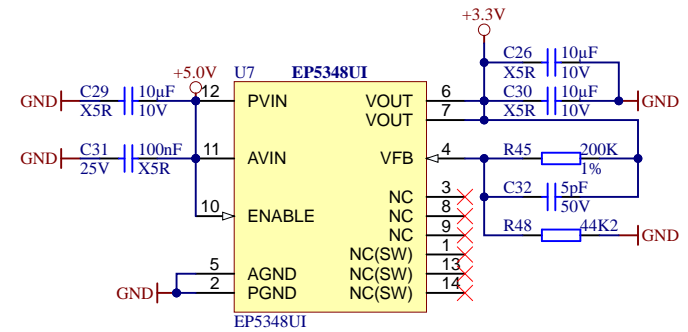
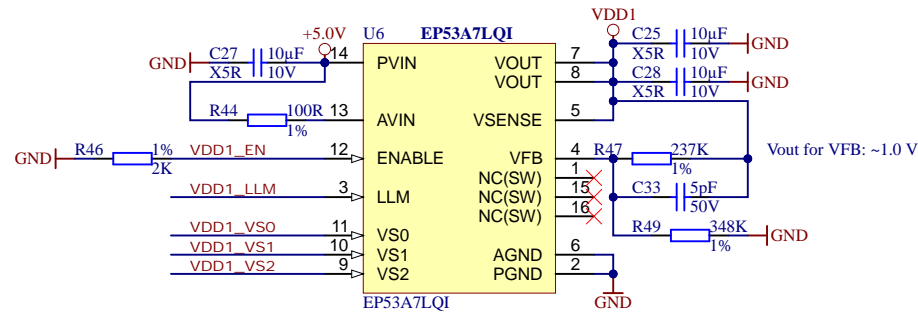
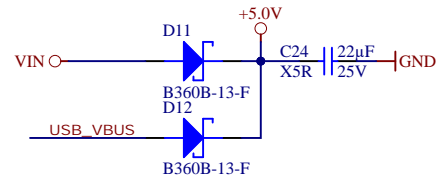
Title: TEI0026 - FTDI			
A4	Number: TEI0026 A	Rev. 01	
Date: 02.04.2020	Copyright: Trenz Electronic GmbH		Page 5 of 7
Drawn by: ED	Filename: FTDI.SchDoc		


1

2

3

4



	Title: TEI0026 - PowerSupply		
	A4	Number: TEI0026 A	Rev. 01
	Date: 02.04.2020	Copyright: Trenz Electronic GmbH	Page 6 of 7
	Drawn by: ED	Filename: PowerSupply.SchDoc	

1

2

3

4

Revision 01:

Initial

Revision 01a (05.05.2020):

1. VY: shunt resistor R16 value was changed from 0.05R to 0.025R

A

B

C


D

A

B

C

D

		Title: TEI0026 - Revision Changes	
		A4	Number: TEI0026 A
Date: 2020-03-02		Copyright: Trenz Electronic GmbH	Rev. 01
Filename: Revision_Changes.SchDoc		Page7	of 7

1

2

3

4