## LDO - Week 3

## **Previous Week:**

- Still working on the layout of the BGR.
- Still one unresolved DRC issue.
- Post-layout simulation still doesn't work.

```
☐ X Check IO_CONNECT_CORE_NET_VOLTAGE_IS_CORE:WARNING1

Cell OTA_symbol ☐
```

## This Week:

- Resolving the outstanding DRC issue with Tal.
- Running simulations to verify the layout.



