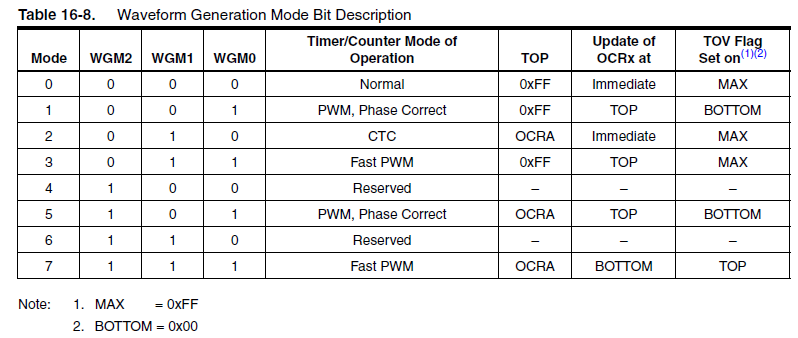
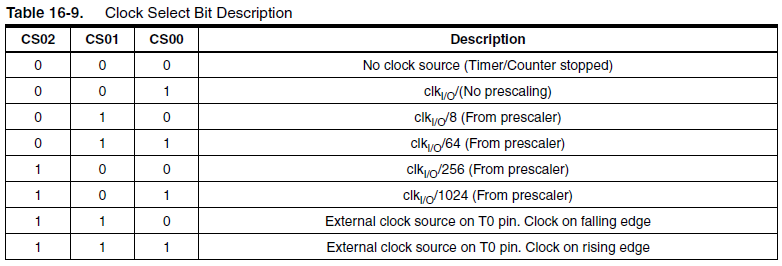
**ATmega2560**

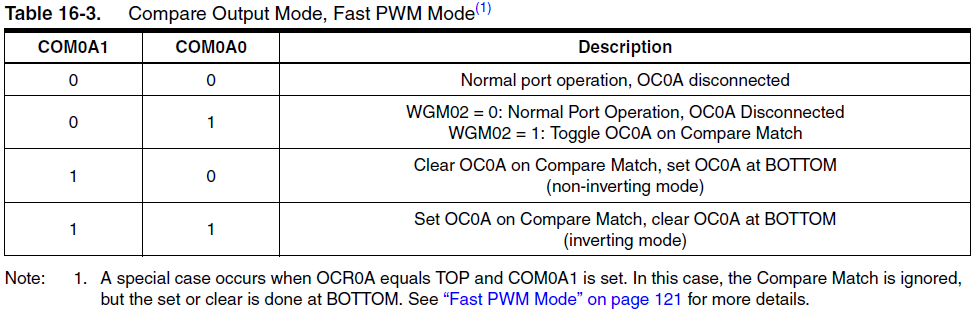
|  |  |
| --- | --- |
| **Vector name** | **Description** |
| ADC\_vect | ADC Conversion Complete |
| ANALOG\_COMP\_vect | Analog Comparator |
| EE\_READY\_vect | EEPROM Ready |
| INT0\_vect | External Interrupt 0 |
| INT1\_vect | External Interrupt Request 1 |
| INT2\_vect | External Interrupt Request 2 |
| INT3\_vect | External Interrupt Request 3 |
| INT4\_vect | External Interrupt Request 4 |
| INT5\_vect | External Interrupt Request 5 |
| INT6\_vect | External Interrupt Request 6 |
| INT7\_vect | External Interrupt Request 7 |
| PCINT0\_vect | Pin Change Interrupt Request 0 |
| PCINT1\_vect | Pin Change Interrupt Request 1 |
| PCINT2\_vect | Pin Change Interrupt Request 2 |
| SPI\_STC\_vect | Serial Transfer Complete |
| SPM\_READY\_vect | Store Program Memory Read |
| TIMER0\_COMPA\_vect | TimerCounter0 Compare Match A |
| TIMER0\_COMPB\_vect | Timer Counter 0 Compare Match B |
| TIMER0\_OVF\_vect | Timer/Counter0 Overflow |
| TIMER1\_CAPT\_vect | Timer/Counter Capture Event |
| TIMER1\_COMPA\_vect | Timer/Counter1 Compare Match A |
| TIMER1\_COMPB\_vect | Timer/Counter1 Compare MatchB |
| TIMER1\_COMPC\_vect | Timer/Counter1 Compare Match C |
| TIMER1\_OVF\_vect | Timer/Counter1 Overflow |
| TIMER2\_COMPA\_vect | Timer/Counter2 Compare Match A |
| TIMER2\_COMPB\_vect | Timer/Counter2 Compare Match A |
| TIMER2\_OVF\_vect | Timer/Counter2 Overflow |
| TIMER3\_CAPT\_vect | Timer/Counter3 Capture Event |
| TIMER3\_COMPA\_vect | Timer/Counter3 Compare Match A |
| TIMER3\_COMPB\_vect | Timer/Counter3 Compare Match B |
| TIMER3\_COMPC\_vect | Timer/Counter3 Compare Match C |
| TIMER3\_OVF\_vect | Timer/Counter3 Overflow |
| TIMER4\_CAPT\_vect | Timer/Counter4 Capture Event |
| TIMER4\_COMPA\_vect | Timer/Counter4 Compare Match A |
| TIMER4\_COMPB\_vect | Timer/Counter4 Compare Match B |
| TIMER4\_COMPC\_vect | Timer/Counter4 Compare Match C |
| TIMER4\_OVF\_vect | Timer/Counter4 Overflow |
| TIMER5\_CAPT\_vect | Timer/Counter5 Capture Event |
| TIMER5\_COMPA\_vect | Timer/Counter5 Compare Match A |
| TIMER5\_COMPB\_vect | Timer/Counter5 Compare Match B |
| TIMER5\_COMPC\_vect | Timer/Counter5 Compare Match C |
| TIMER5\_OVF\_vect | Timer/Counter5 Overflow |
| TWI\_vect | 2-wire Serial Interface |
| USART0\_RX\_vect | USART0, Rx Complete |
| USART0\_TX\_vect | USART0, Tx Complete |
| USART0\_UDRE\_vect | USART0 Data Register Empty |
| USART1\_RX\_vect | USART1, Rx Complete |
| USART1\_TX\_vect | USART1, Tx Complete |
| USART1\_UDRE\_vect | USART1, Data Register Empty |
| USART2\_RX\_vect | USART2, Rx Complete |
| USART2\_TX\_vect | USART2, Tx Complete |
| USART2\_UDRE\_vect | USART2 Data register Empty |
| USART3\_RX\_vect | USART3, Rx Complete |
| USART3\_TX\_vect | USART3, Tx Complete |
| USART3\_UDRE\_vect | USART3 Data register Empty |
| WDT\_vect | Watchdog Timeout Interrupt |
|  |  |

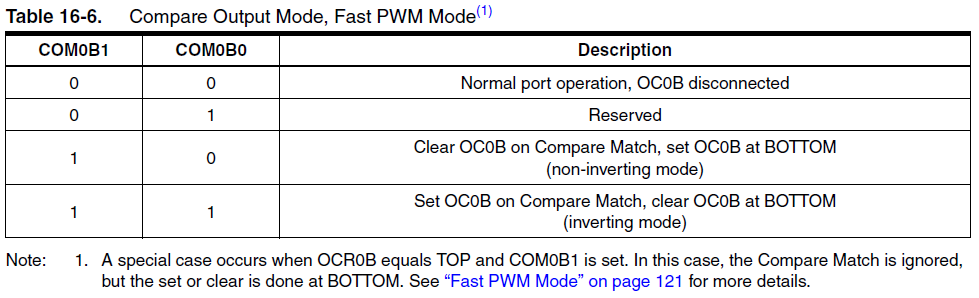
**TIMER0**

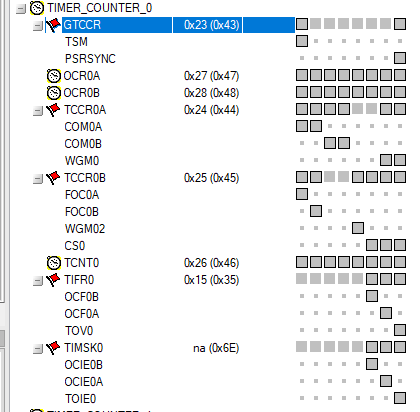




**TIMER0**







**TIMER1, 3, 4, and 5**

